

MAXIM

CMOS Monolithic Voltage Converter

MAX660

General Description

The MAX660 monolithic, charge-pump voltage inverter converts a +1.5V to +5.5V input to a corresponding -1.5V to -5.5V output. Using only two low-cost capacitors, the charge pump's 100mA output replaces switching regulators, eliminating inductors and their associated cost, size, and EMI. Greater than 90% efficiency over most of its load-current range combined with a typical operating current of only 200 μ A provides ideal performance for both battery-powered and board-level voltage conversion applications. The MAX660 can also double the output voltage of an input power supply or battery, providing +9.35V at 100mA from a +5V input.

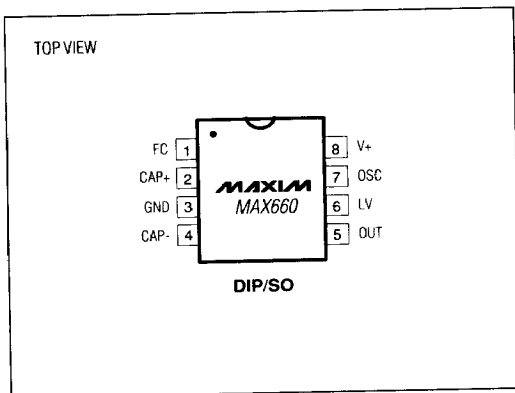
A Frequency Control (FC) pin selects either 10kHz or 45kHz operation to optimize capacitor size and quiescent current. The oscillator frequency can also be adjusted with an external capacitor or driven with an external clock. The MAX660 is a pin-compatible high-current upgrade of the ICL7660.

The MAX660 is available in both 8-pin DIP and small outline packages in commercial, extended, and military temperature ranges.

Applications

Laptop Computers
Medical Instruments
Interface Power Supplies
Handheld Instruments
Operational-Amplifier Power Supplies

Pin Configuration



Features

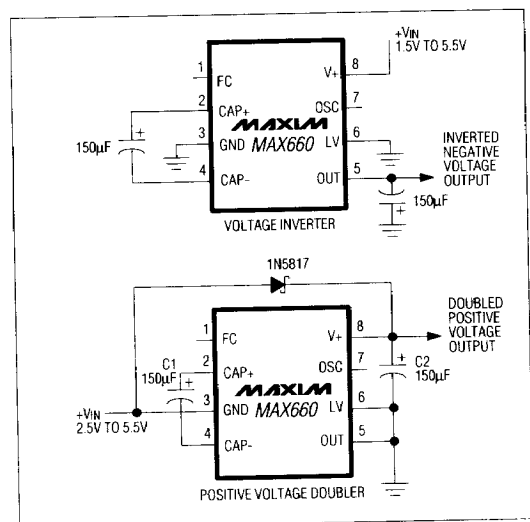
- ◆ 0.65V Typ Loss at 100mA Load
- ◆ 6.5 Ω Typ Output Impedance
- ◆ Pin-Compatible High-Current ICL7660 Upgrade
- ◆ Inverts or Doubles Input Supply Voltage
- ◆ Selectable Oscillator Frequency: 10kHz/45kHz
- ◆ 88% Typ Conversion Efficiency at 100mA (I_L to GND)
- ◆ 200 μ A Operating Current

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX660CPA	0°C to +70°C	8 Plastic DIP
MAX660CSA	0°C to +70°C	8 SO
MAX660C/D	0°C to +70°C	Dice*
MAX660EPA	-40°C to +85°C	8 Plastic DIP
MAX660ESA	-40°C to +85°C	8 SO
MAX660MJA	-55°C to +125°C	8 CERDIP

* Contact factory for dice specifications.

Typical Operating Circuits



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ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V+ to GND, or GND to OUT)	+6V
LV Input Voltage	OUT - 0.3V to V+ + 0.3V
FC and OSC Input Voltages	(the least negative of OUT - 0.3V or V+ - 6V) to V+ + 0.3V
OUT and V+ Continuous Output Current	120mA
Output Short-Circuit Duration to GND (Note 1)	10 sec.
Continuous Total Power Dissipation (TA = +70°C)	
Plastic DIP (derate 6.9mW/°C above +70°C)	552mW
SO (derate 5.88mW/°C above +70°C)	471mW
CERDIP (derate 8.0mW/°C above +70°C)	640mW

Operating Temperature Ranges:

MAX660C	0°C to +70°C
MAX660E	-40°C to +85°C
MAX660MJA	-55°C to +125°C
Storage Temperature	-65°C to +160°C
Lead Temperature (Soldering, 10 sec.)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V+ = 5V; C1, C2 = 150µF; test circuit of Figure 1; FC = open; TA = TMIN to TMAX, unless otherwise noted.) (Note 2)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Operating Supply Voltage	RL = 1kΩ	Inverter, LV = open	3	5.5	V
		Inverter, LV = GND	1.5	5.5	
		Doubler, LV = OUT	2.5	5.5	
Supply Current	No Load	FC = open	0.2	0.5	mA
		FC = V+	1	3	
Output Current	TA ≤ +85°C, OUT more negative than -4V	100			mA
	TA > +85°C, OUT more negative than -3.8V	100			
Output Resistance (Note 3)	IL = 100mA	TA ≤ +85°C	6.5	10	Ω
		TA > +85°C		12	
Oscillator Frequency	FC = open		10		kHz
	FC = V+		45		
OSC Input Current	FC = open		±1.1		µA
	FC = V+		±5		
Power Efficiency	RL = 1kΩ connected between V+ and OUT	96	98		%
	RL = 500Ω connected between OUT and GND	92	96		
	IL = 100mA to GND		88		
Voltage Conversion Efficiency	No Load	99	99.96		%

Note 1: In inverting mode, OUT may be shorted to GND for 10 sec. without damage, but shorting OUT to V+ may damage the device and should be avoided. In doubler mode, shorts of V+ to OUT can be tolerated indefinitely, but the Schottky diode current rating could be exceeded. In doubler mode, V+ may be shorted to GND for 10 sec. without damage.

Note 2: In the test circuit, capacitors C1 and C2 are 150µF, 0.2Ω maximum ESR, aluminum electrolytics (Maxim part # MAXC001). Capacitors with higher ESR may reduce output voltage and efficiency.

Note 3: Specified output resistance is a combination of internal switch resistance and capacitor ESR. See *Capacitor Selection* section.

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Typical Operating Characteristics

(All curves are generated using the test circuit of Figure 1 with $V_+ = 5V$, $LV = GND$, $FC = open$, and $T_A = +25^\circ C$, unless otherwise noted. The charge-pump frequency is one-half the oscillator frequency. Test results are also valid for doubler mode with $GND = +5V$, $LV = OUT$, and $OUT = 0V$, unless otherwise noted; however, the input voltage is restricted to $+2.5V$ to $+5.5V$.)

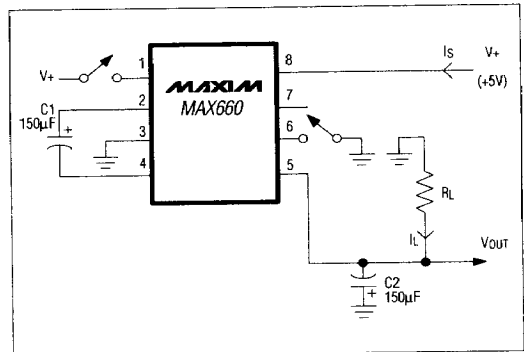
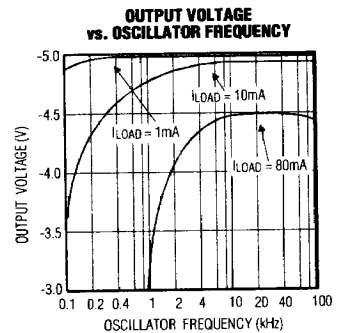
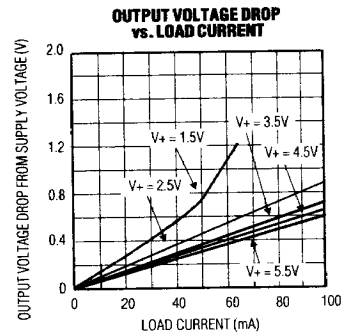
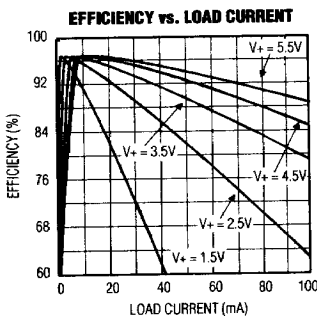
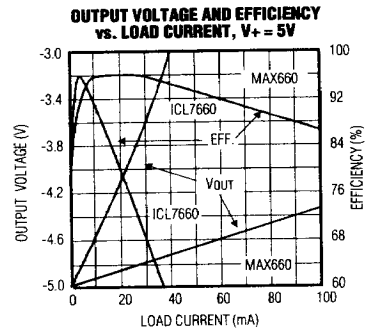
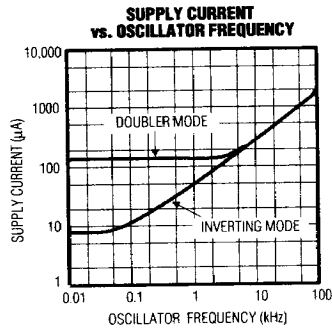
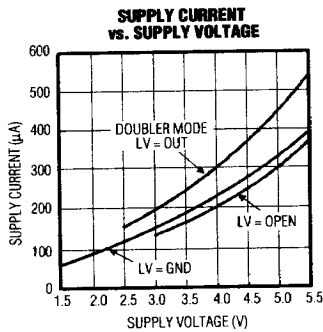


Figure 1. MAX660 Test Circuit

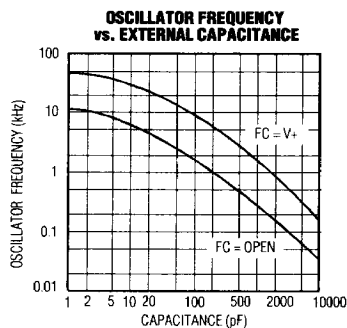
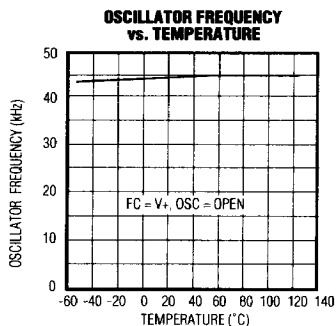
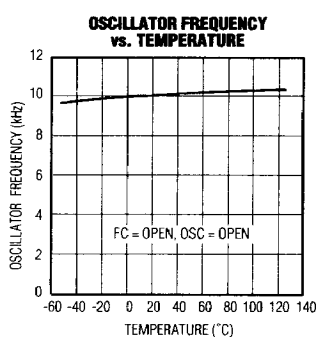
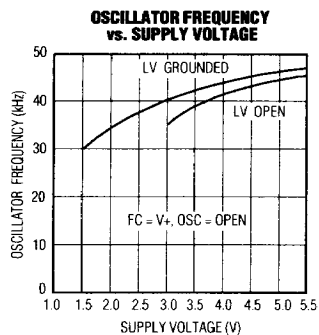
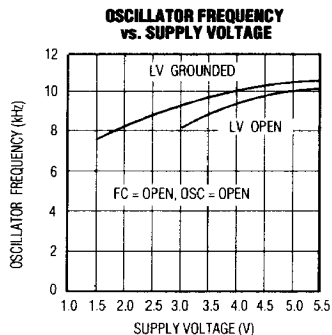
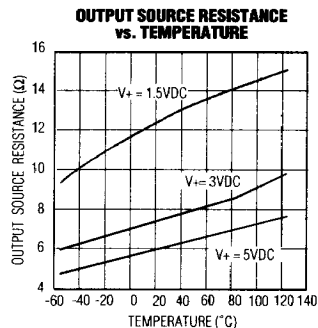
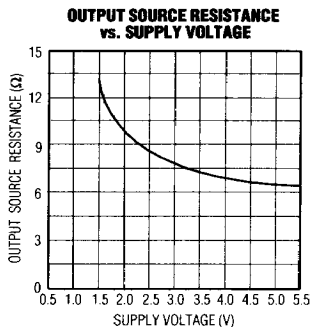
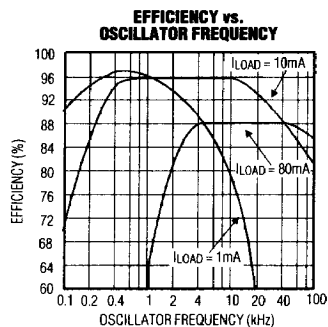


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Typical Operating Characteristics (continued)



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Pin Description

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PIN	NAME	FUNCTION	
		INVERTER	DOUBLER
1	FC	Frequency Control for internal oscillator. FC = open, f _{osc} = 10kHz typ; FC = V+, f _{osc} = 45kHz typ; FC has no effect when OSC pin is driven externally.	Same as Inverter
2	CAP+	Charge-Pump Capacitor, Positive Terminal	Same as Inverter
3	GND	Power-Supply Ground Input	Power-Supply Positive Voltage Input
4	CAP-	Charge-Pump Capacitor, Negative Terminal	Same as Inverter
5	OUT	Output, Negative Voltage	Power-Supply Ground Input
6	LV	Low-Voltage Operation Input. Tie LV to GND when input voltage is less than 3V. Above 3V, LV may be connected to GND or left open; when overdriving OSC, LV must be connected to GND.	LV must be tied to OUT for all input voltages.
7	OSC	Oscillator Control Input. OSC is connected to an internal 15pF capacitor. An external capacitor can be added to slow the oscillator. Care must be taken to minimize stray capacitance. An external oscillator may also be connected to overdrive OSC.	Same as Inverter; however standard logic levels cannot overdrive OSC in voltage doubler mode. Contact factory for details.
8	V+	Power-Supply Positive Voltage Input	Positive Voltage Output

Detailed Description

The MAX660 capacitive charge-pump circuit either inverts or doubles the input voltage. Two external capacitors are needed in the voltage inverting mode, while two capacitors and one diode are needed for the voltage doubling mode (see *Typical Operating Circuits*). For highest performance, low effective series resistance (ESR) capacitors should be used. See *Capacitor Selection* section for more details.

When using the inverting mode with a supply voltage less than 3V, LV must be connected to GND. This bypasses the internal regulator circuitry and provides best performance in low-voltage applications. When using the inverter mode with a supply voltage above 3V, LV may be connected to GND or left open. The part is typically operated with LV grounded, but since LV may be left open, the substitution of the MAX660 for the ICL7660 is simplified. LV must be grounded when overdriving OSC (see *Changing Oscillator Frequency* section). Connect LV to OUT (for any supply voltage) when using the doubler mode.

Application Hints

Negative Voltage Converter

The most common application of the MAX660 is as a charge-pump voltage inverter. The operating circuit

uses only two external capacitors, C1 and C2 (see *Typical Operating Circuits*). In most applications these are low-cost, low-ESR, 150μF electrolytic capacitors (Refer to *Capacitor Selection* section).

Even though its output is not actively regulated, the MAX660 is very insensitive to load current changes. A typical output source resistance of 6.5Ω means that with an input of +5V the output voltage is -5V under light load, and decreases only to -4.35V with a load of 100mA. Output source resistance vs. temperature and supply voltage are shown in the *Typical Operating Characteristics* graphs.

Output ripple voltage is calculated by noting the output current supplied is solely from capacitor C2 during one-half of the charge-pump cycle. This introduces a peak-to-peak ripple of:

$$V_{\text{RIPPLE}} = \frac{I_{\text{OUT}}}{2(f_{\text{PUMP}})(C2)} + I_{\text{OUT}}(\text{ESR}C2)$$

For a nominal f_{PUMP} of 5kHz (one-half the nominal 10kHz oscillator frequency) and C2 = 150μF with an ESR of 0.2Ω, ripple is approximately 90mV with a 100mA load current. If C2 is raised to 390μF, the ripple drops to 45mV.

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Positive Voltage Doubler

The MAX660 operates in the voltage-doubling mode as shown in the *Typical Operating Circuit*. The external Schottky (1N5817) diode is for start-up only. The no-load output is $2 \times V_{IN}$ and is not reduced by the diode forward drop.

Changing Oscillator Frequency

Four modes control the MAX660's clock frequency, as listed below:

FC	OSC	Oscillator Frequency
Open	Open	10kHz
FC = V+	Open	45kHz
Open or FC = V+	External Capacitor	See Typical Operating Characteristics
Open	External Clock	External Clock Frequency

When FC and OSC are unconnected (open), the oscillator runs at 10kHz typically. When FC is connected to V+, the charge and discharge current at OSC changes from 1.1 μ A to 5 μ A, thus increasing the oscillator frequency 4.5 times. In the third mode, the oscillator frequency is lowered by connecting a capacitor between OSC and GND. FC can still multiply the frequency by 4.5 times in this mode, but for a lower range of frequencies (see *Typical Operating Characteristics*).

In the inverter mode, OSC may also be overdriven by an external clock source that swings within 100mV of V+ and GND. Any standard CMOS logic output is suitable for driving OSC. When OSC is overdriven, FC has no effect. Also, LV must be grounded when overdriving OSC. Standard logic levels cannot overdrive OSC in voltage doubler mode. Contact factory for details.

Note: In all modes, the frequency of the signal appearing at CAP+ and CAP- is one-half that of the oscillator. Also, an undesirable effect of lowering the oscillator frequency is that the effective output resistance of the charge-pump increases. This can be compensated by increasing the value of the charge-pump capacitors (see *Capacitor Selection* section and *Typical Operating Characteristics*).

In some applications, the 5kHz output ripple frequency may be low enough to interfere with other circuitry. If desired, the oscillator frequency can then be increased through use of the FC pin or an external oscillator as described above. The output ripple frequency is one-half the selected oscillator frequency. Increasing the clock frequency increases the MAX660's quiescent current, but also allows smaller capacitance values to be used for C1 and C2.

Capacitor Selection

Three factors (in addition to load current) affect the MAX660 output voltage drop from its ideal value:

- 1) MAX660 output resistance
- 2) Pump (C1) and reservoir (C2) capacitor ESRs
- 3) C1 and C2 capacitance

The voltage drop caused by MAX660 output resistance is the load current times the output resistance. Similarly, the loss in C2 is the load current times C2's ESR. The loss in C1, however, is larger because it handles currents that are greater than the load current during charge-pump operation. The voltage drop due to C1 is therefore about 4 times C1's ESR times the load current. Consequently, a low (or high) ESR capacitor has much greater impact on performance for C1 than for C2.

Generally, as the pump frequency of the MAX660 increases, the capacitance values required to maintain comparable ripple and output resistance diminish proportionately. The curves of Figure 2 show the total circuit output resistance for various capacitor values (the pump and reservoir capacitors' values are equal) and oscillator frequencies. These curves assume 0.25 Ω capacitor ESRs and a 5.25 Ω MAX660 output resistance, which is why the flat portion of the curve shows a 6.5 Ω (R_O MAX660 + 4 (ESRC₁) + ESRC₂) effective output resistance. Note: $R_O = 5.25\Omega$ is used, rather than the typical 6.5 Ω , because the typical specification includes the effect of the ESRs of the capacitors in the test circuit.

To reduce the output ripple caused by the charge pump, increase the reservoir capacitor C2 and/or reduce its ESR. Also, the reservoir capacitor must have low ESR if filtering high-frequency noise at the output is important.

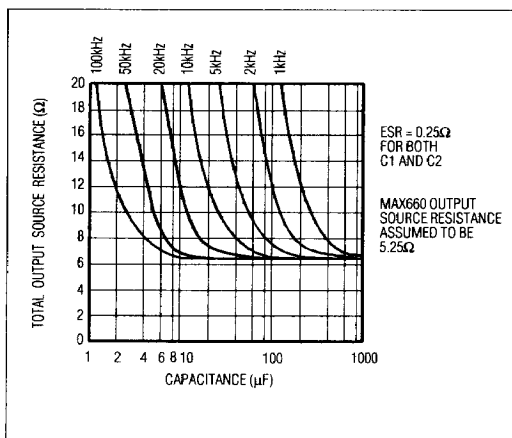


Figure 2. Total Output Source Resistance vs. C1 and C2 Capacitance (C1 = C2)

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Not all manufacturers guarantee capacitor ESR in the range required by the MAX660. In general, capacitor ESR is inversely proportional to physical size, so larger capacitance values and higher voltage ratings tend to reduce ESR. The capacitors used when testing the MAX660 are MAXC001 150 μ F aluminum electrolytics available from Maxim. They combine low cost, a guaranteed maximum ESR of 0.2 Ω at room temperature, and a low-temperature operating limit of -25°C. If operation at lower temperatures is required, certain tantalum capacitors provide good low-temperature ESR, but at added expense.

The following is a list of manufacturers who provide low-ESR electrolytic capacitors.

MANUFACTURER	CAPACITOR	CAPACITOR TYPE
Illinois Capacitor	RZS	Aluminum Electrolytic
Mallory	TDC & TDL	Tantalum
Nichicon	PF & PL	Aluminum Electrolytic
Sprague	672D, 673D, 674D, 678D	Aluminum Electrolytic
Sprague	135D, 173D, 199D	Tantalum
United Chemi-con	LXF & SXF	Aluminum Electrolytic

Cascading Devices

To produce larger negative multiplication of the initial supply voltage, the MAX660 may be cascaded as shown in Figure 3. The resulting output resistance is approximately equal to the sum of the individual MAX660 ROUT values. The output voltage, where n is an integer representing the number of devices cascaded, is defined by $V_{OUT} = -n(V_{IN})$.

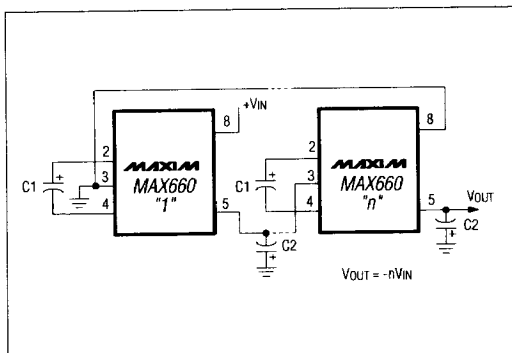


Figure 3. Cascading MAX660s to Increase Output Voltage

Paralleling Devices

Paralleling multiple MAX660s reduces the output resistance. As illustrated in Figure 4, each device requires its own pump capacitor C1, but the reservoir capacitor C2 serves all devices. The value of C2 should be increased by a factor of n, where n is the number of devices. Figure 4 shows the equation for calculating output resistance.

Combined Positive Supply Multiplication and Negative Voltage Conversion

This dual function is illustrated in Figure 5. In this circuit, capacitors C1 and C3 perform the pump and reservoir functions respectively for generation of the negative voltage. Capacitors C2 and C4 are respectively pump and reservoir for the multiplied positive voltage. This circuit configuration, however, leads to higher source impedances of the generated supplies. This is due to the finite impedance of the common charge-pump driver.

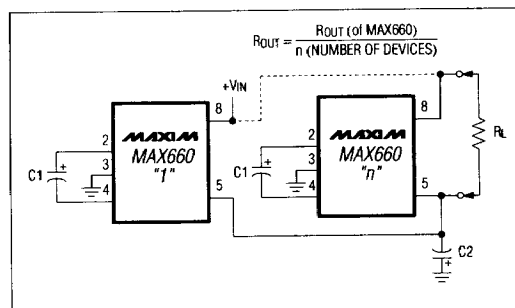


Figure 4. Paralleling MAX660s to Reduce Output Resistance

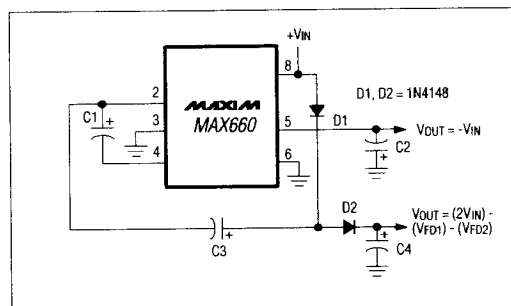


Figure 5. Combined Positive Multiplier and Negative Converter

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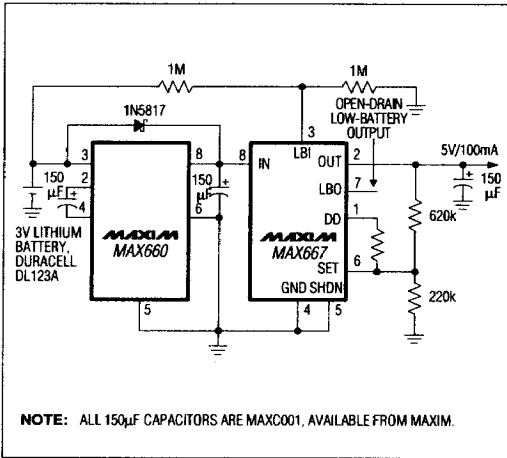
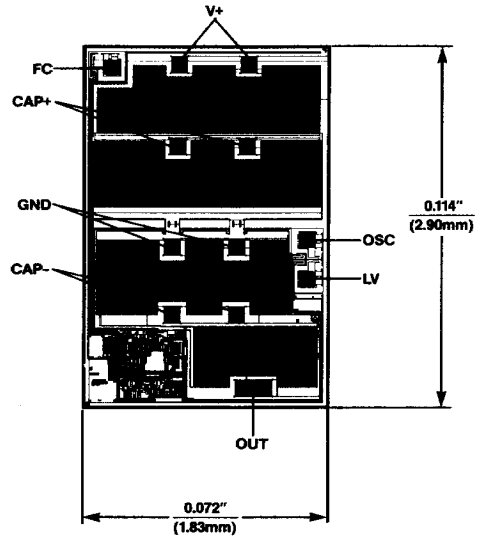


Figure 6. MAX660 generating a +5V regulated output from a 3V Lithium battery. Operates for 16 hours with 40mA load.

Chip Topography



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