

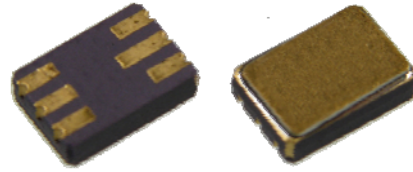
Dual Enhancement Mode MOSFET

HCT802, HCT802TX, HCT802TXV



Features:

- 6 pad surface mount package
- $V_{DS} = 90V$
- $R_{DS(on)} < 5\Omega$
- $I_{D(on)}$ N-Channel = 1.5A | P-Channel = 1.1A
- Two devices selected for V_{DS} , $I_{D(on)}$ and $R_{DS(on)}$ similarity
- Full TX Processing Available
- Gold plated contacts



Description:

HCT802 offers an N-Channel and P-Channel MOS transistor in a hermetic ceramic surface mount package. The devices used are similar to industry standards 2N6661 N-Channel device and VP1008 P-Channel device. These two enhancement mode MOSFETS are particularly well matched for V_{DS} , $I_{D(on)}$, $R_{DS(on)}$ and G_{fs} .

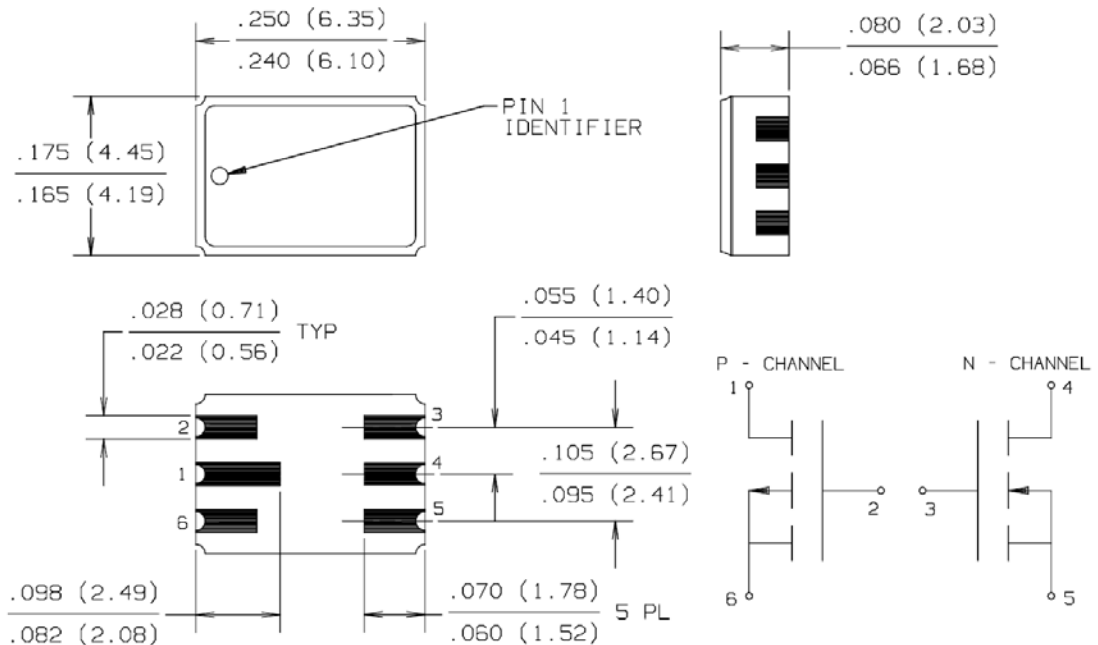
TX and TXV devices are processed to OPTEK's military screening program patterned after MIL-PRF-19500.

TX products receive a V_{GS} HTRB at 24 V for 48 hrs. at 150° C and a V_{DS} HTRB at 48 V for 260 hrs. at 150° C.

Applications:

- Drivers: Solid State Relays, Lamps, Solenoids, Displays, Memories, etc.
- Motor Control
- Power Supply Circuits

Part Number	Sensor Type	V_{DS} Min	$I_{D(on)}$ (mA) Min	G_{fs} (ms) Min	$t_{(ON)} / t_{(OFF)}$ (ns) Max	Package
HCT801	N & P-Channel Enhancement MOSFET	90	1.5 & -1.1	170 & 200	15/17 & 50/50	6-pin Ceramic
HCT801TX						
HCT801TXV						



DIMENSIONS ARE IN INCHES (MILLIMETERS)

General Note

TT Electronics reserves the right to make changes in product specification without notice or liability. All information is subject to TT Electronics' own data and is considered accurate at time of going to print.

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Absolute Maximum Ratings

Drain Source Voltage	90V
Gate-Source Voltage	±20 V
Drain Current (Limited by Tj max) N-Channel P-Channel	2A 1.1A
Operating and Storage Temperature	-55° C to +150° C

Power Dissipation

TA = 25°C (Both devices equally driven)	0.5 W Total
TA = 25°C (Both devices equally driven)	1.5 W Total ⁽¹⁾
(TS = Substrate that the package is soldered to)	

Electrical Characteristics (TA = 25° C unless otherwise noted)

SYMBOL	PARAMETER	DEVICE B=BOTH	MIN	MAX	UNITS	TEST CONDITIONS
B _{VDS}	Drain-Source Breakdown	B	90 ⁽²⁾		V	I _D = 10 µA ⁽²⁾ , V _{GS} = 0
V _{TH}	Gate Threshold Voltage	N	0.75	2.5	V	V _{GS} = V _{DS} , I _D = 1 mA
		P	-2.0	-4.5	V	I _D = -1 mA
I _{GSS}	Gate-Body Leakage	B		±100	nA	V _{GS} = ± 20 V, V _{DS} = 0
I _{DSS}	Zero Gate Voltage Drain Current	B		10 ⁽²⁾	µA	V _{DS} = 90 V ⁽²⁾ , V _{GS} = 0 V
		B		500 ⁽²⁾	µA	Tj = 150° C
I _{D(on)}	On-State Drain Current	N	1.5		A	V _{DS} = 25 V, V _{GS} = 10 V
		P	-1.1		A	V _{DS} = -15 V, V _{GS} = -10 V
R _{DS(on)}	Drain-Source on Resistance	B		5	Ω	V _{GS} = 10 V ⁽²⁾ , I _D = 1 A ⁽²⁾
G _{fs}	Forward Transconductance	N	170		mmho	V _{DS} = 25V, I _D = 0.5 A
		P	200		Mmho	V _{DS} = -10 V, I _D = -0.5 A
C _{iss}	Input Capacitance	N		70	pf	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz
		P		150	pf	V _{DS} = -25 V, V _{GS} = 0 V, f = 1 MHz
C _{oss}	Common Source Output Capacitance	N		40	pf	V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz
		P		60	pf	V _{DS} = -25 V, V _{GS} = 0 V, f = 1 MHz
C _{rss}	Reverse Transfer Capacitance	N		10	pf	V _{DS} = 25 V, V _{GS} = 0 A, f = 1 MHz
		P		25	pf	V _{DS} = -25 V, V _{GS} = 0 A, f = 1 MHz

Note:

- 1) This rating is provided as an aid to designers. It is dependent upon mounting material and methods and is not measurable as an outgoing test.
- 2) Reverse polarity for P-Channel device

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Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

SYMBOL	PARAMETER	DEVICE B=BOTH	MIN	MAX	UNITS	TEST CONDITIONS
$t_{(on)}$	Turn-on-time	N		15	ns	$V_{DD} = 25\text{ V}$, $I_D = 1\text{ A}$, $R_L = 50\ \Omega$
		P		50	ns	$V_{DD} = -25\text{ V}$, $I_D = -0.5\text{ A}$, $R_L = 50\ \Omega$
$t_{(off)}$	Turn-off-time	N		17	ns	$V_{DD} = 25\text{ V}$, $I_D = 1\text{ A}$, $R_L = 50\ \Omega$
		P		50	ns	$V_{DD} = -25\text{ V}$, $I_D = -0.5\text{ A}$, $R_L = 50\ \Omega$

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