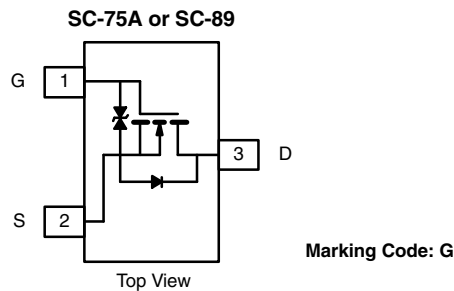


N-Channel 1.5 V (G-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (mA)
20	5 at $V_{GS} = 4.5$ V	200
	7 at $V_{GS} = 2.5$ V	175
	9 at $V_{GS} = 1.8$ V	150
	10 at $V_{GS} = 1.5$ V	50



Ordering Information:

Si1032R-T1-GE3 (SC-75A, Lead (Pb)-free and Halogen-free)
Si1032X-T1-GE3 (SC-89, Lead (Pb)-free -free Halogen-free)

FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- Low-Side Switching
- Low On-Resistance: 5 Ω
- Low Threshold: 0.9 V (typ.)
- Fast Switching Speed: 35 ns
- TrenchFET® Power MOSFETs: 1.5 V Rated
- 2000 V ESD Protection
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE

BENEFITS

- Ease in Driving Switches
- Low Offset (Error) Voltage
- Low-Voltage Operation
- High-Speed Circuits
- Low Battery Voltage Operation

APPLICATIONS

- Drivers: Relays, Solenoids, Lamps, Hammers, Displays, Memories
- Battery Operated Systems
- Power Supply Converter Circuits
- Load/Power Switching Cell Phones, Pagers

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)							
Parameter		Symbol	Si1032R		Si1032X		Unit
			5 s	Steady State	5 s	Steady State	
Drain-Source Voltage		V _{DS}	20				V
Gate-Source Voltage		V _{GS}	± 6				
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I _D	200	140	210	200	mA
	T _A = 85 °C		110	100	150	140	
Pulsed Drain Current ^a		I _{DM}	500		600		
Continuous Source Current (Diode Conduction) ^a		I _S	250	200	300	240	
Maximum Power Dissipation ^a for SC-75	T _A = 25 °C	P _D	280	250	340	300	mW
	T _A = 85 °C		145	130	170	150	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150				°C
Gate-Source ESD Rating (HBM, Method 3015)		ESD	2000				V

Notes:

a. Surface mounted on FR4 board.

SPECIFICATIONS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\text{ }\mu\text{A}$	0.40	0.7	1.2	V
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 2.8\text{ V}$		± 0.5	± 1.0	μA
		$V_{DS} = 0\text{ V}$, $V_{GS} = \pm 4.5\text{ V}$		± 1.0	± 3.0	
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20\text{ V}$, $V_{GS} = 0\text{ V}$			1	
		$V_{DS} = 20\text{ V}$, $V_{GS} = 0\text{ V}$, $T_J = 55\text{ }^{\circ}\text{C}$			10	
On-State Drain Current ^a	$I_{D(on)}$	$V_{DS} = 5\text{ V}$, $V_{GS} = 4.5\text{ V}$	250			mA
Drain-Source On-State Resistance ^a	$R_{DS(on)}$	$V_{GS} = 4.5\text{ V}$, $I_D = 200\text{ mA}$			5	Ω
		$V_{GS} = 2.5\text{ V}$, $I_D = 175\text{ mA}$			7	
		$V_{GS} = 1.8\text{ V}$, $I_D = 150\text{ mA}$			9	
		$V_{GS} = 1.5\text{ V}$, $I_D = 40\text{ mA}$			10	
Forward Transconductance ^a	g_{fs}	$V_{DS} = 10\text{ V}$, $I_D = 200\text{ mA}$		0.5		S
Diode Forward Voltage ^a	V_{SD}	$I_S = 150\text{ mA}$, $V_{GS} = 0\text{ V}$			1.2	V
Dynamic^b						
Total Gate Charge	Q_g	$V_{DS} = 10\text{ V}$, $V_{GS} = 4.5\text{ V}$, $I_D = 250\text{ mA}$		750		pC
Gate-Source Charge	Q_{gs}			75		
Gate-Drain Charge	Q_{gd}			225		
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 10\text{ V}$, $R_L = 47\text{ }\Omega$ $I_D \cong 200\text{ mA}$, $V_{GEN} = 4.5\text{ V}$, $R_g = 10\text{ }\Omega$			50	ns
Rise Time	t_r				25	
Turn-Off Delay Time	$t_{d(off)}$				50	
Fall Time	t_f				25	

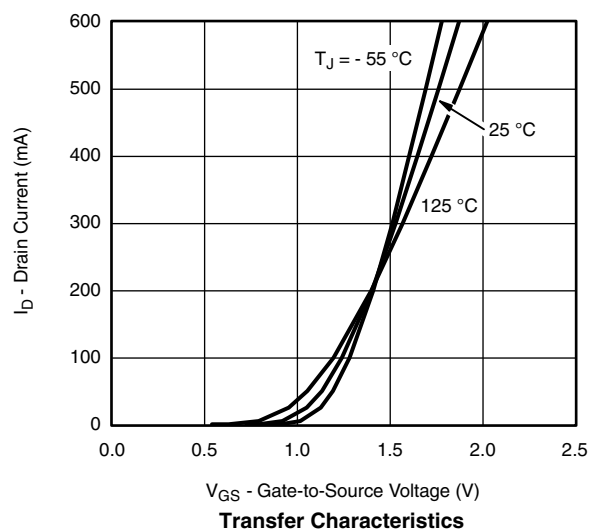
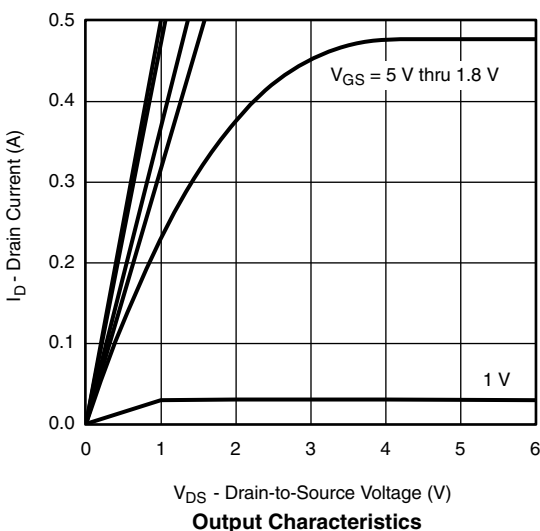
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

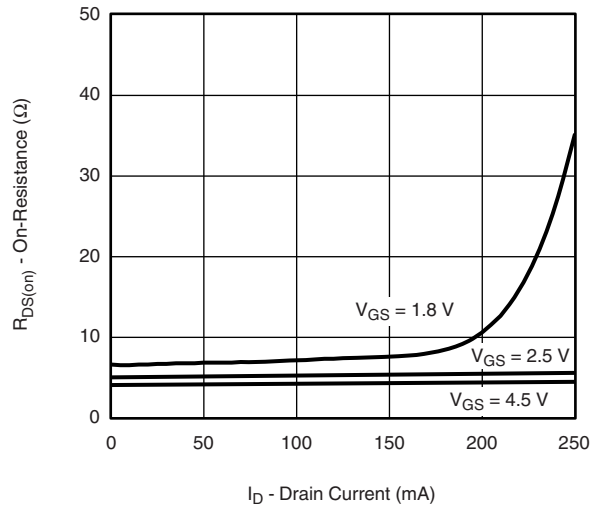
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

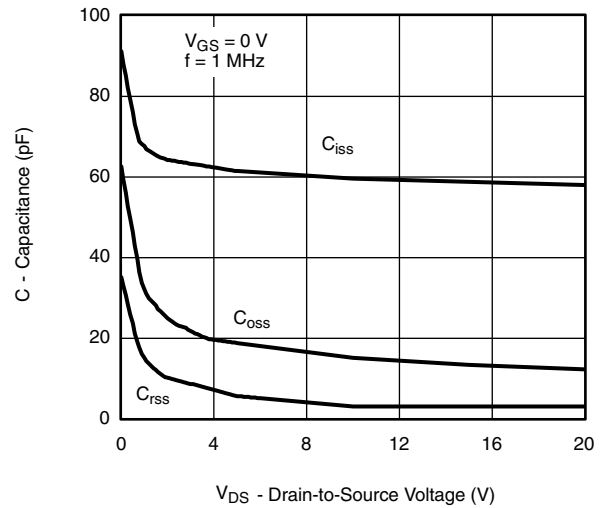
TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)



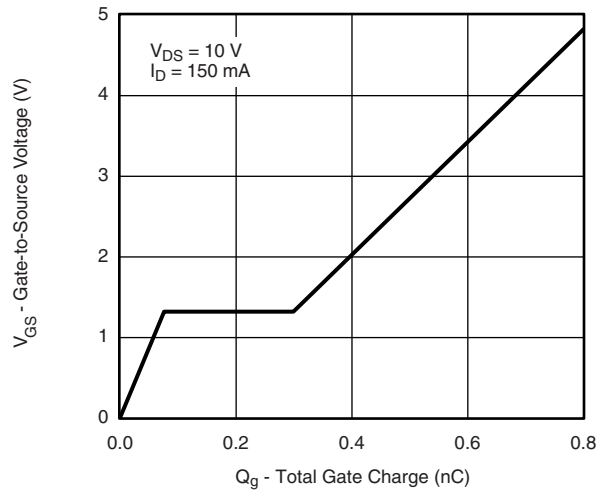
TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise noted)



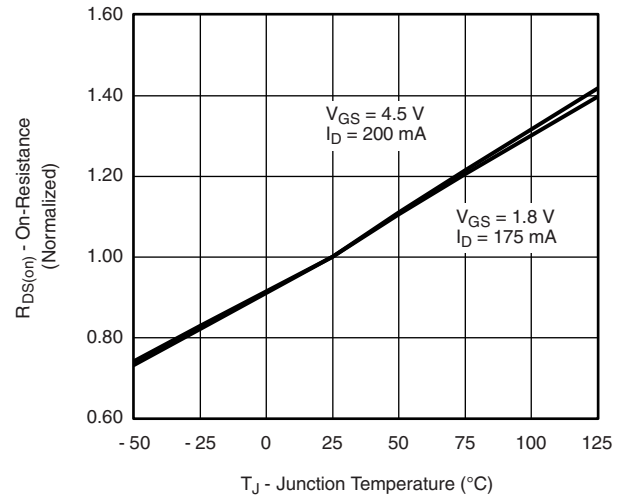
On-Resistance vs. Drain Current



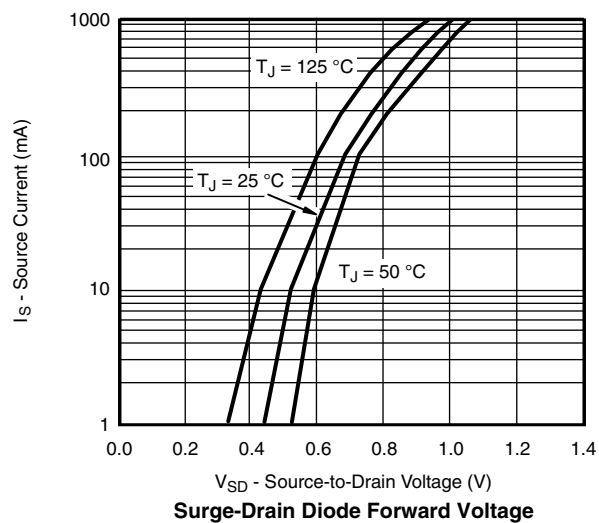
Capacitance



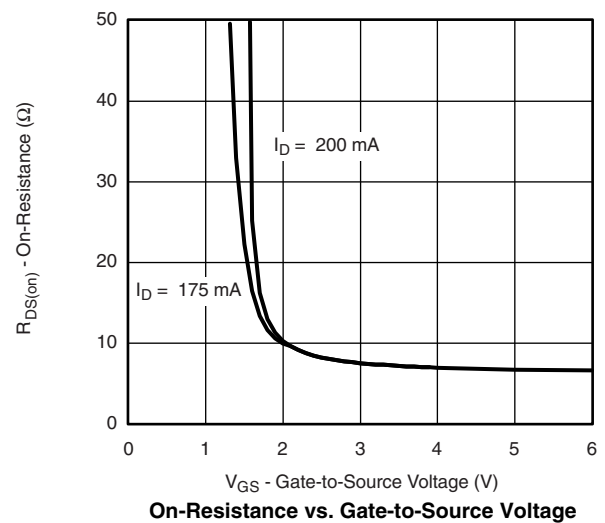
Gate Charge



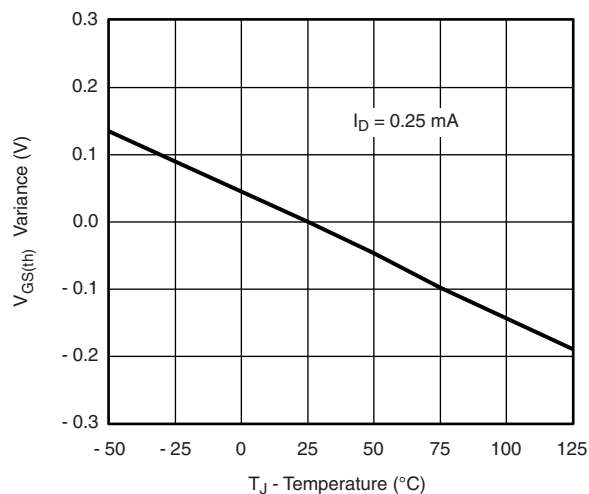
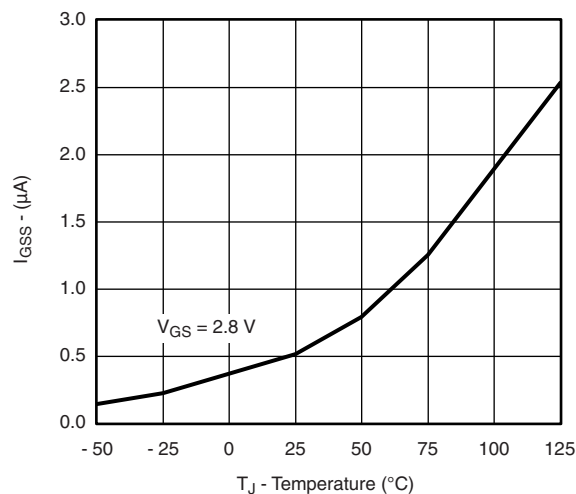
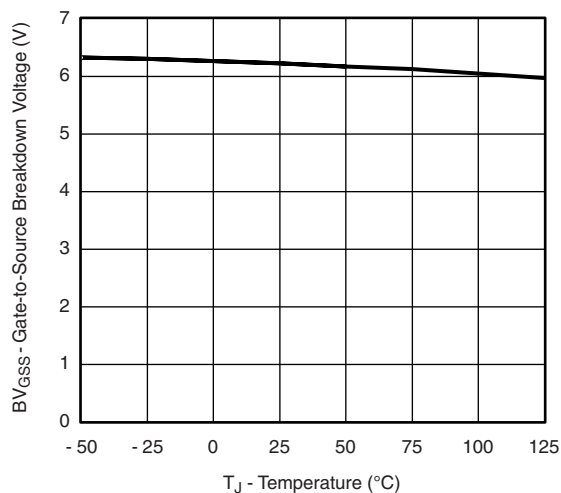
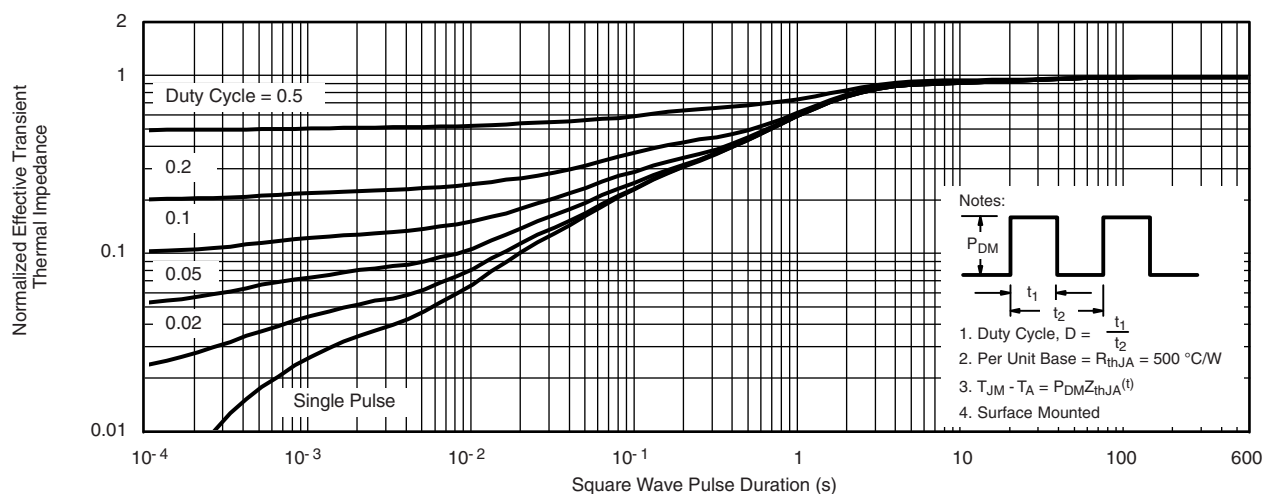
On-Resistance vs. Junction Temperature



Surge-Drain Diode Forward Voltage

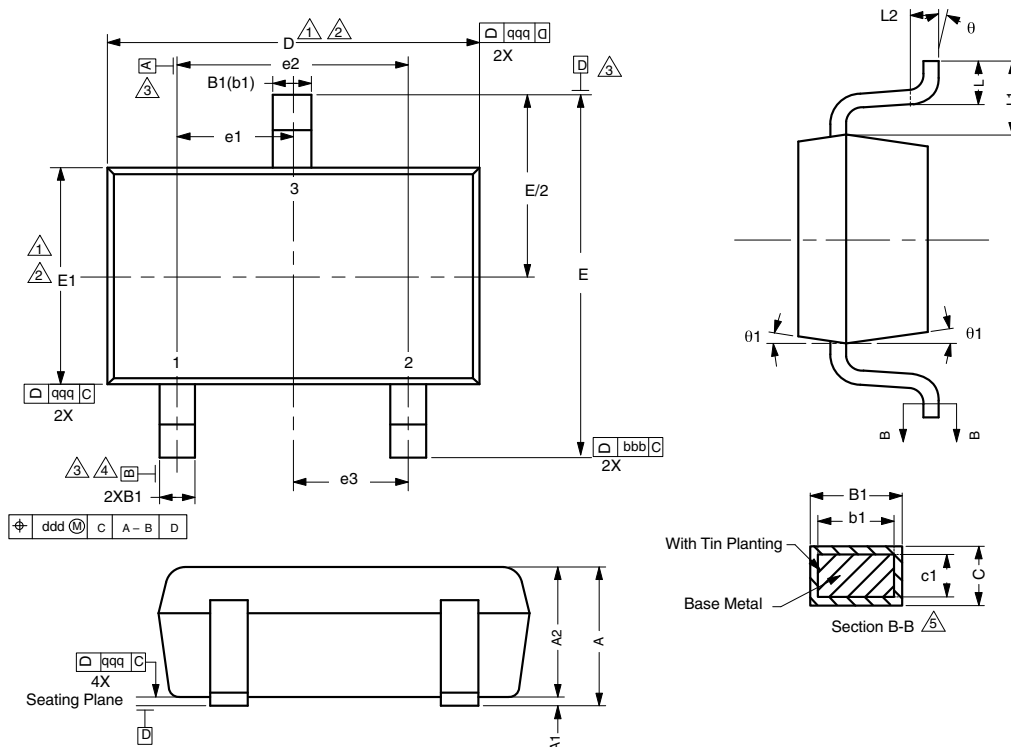


On-Resistance vs. Gate-to-Source Voltage

TYPICAL CHARACTERISTICS ($T_A = 25\text{ }^{\circ}\text{C}$, unless otherwise noted)**Threshold Voltage Variance vs. Temperature** **I_{GSS} vs. Temperature** **BV_{GSS} vs. Temperature****Normalized Thermal Transient Impedance, Junction-to-Ambient (SC-75A, Si1032R Only)**

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71172.

SC-75A: 3 Leads



DWG: 5868

Notes

Dimensions in millimeters will govern.

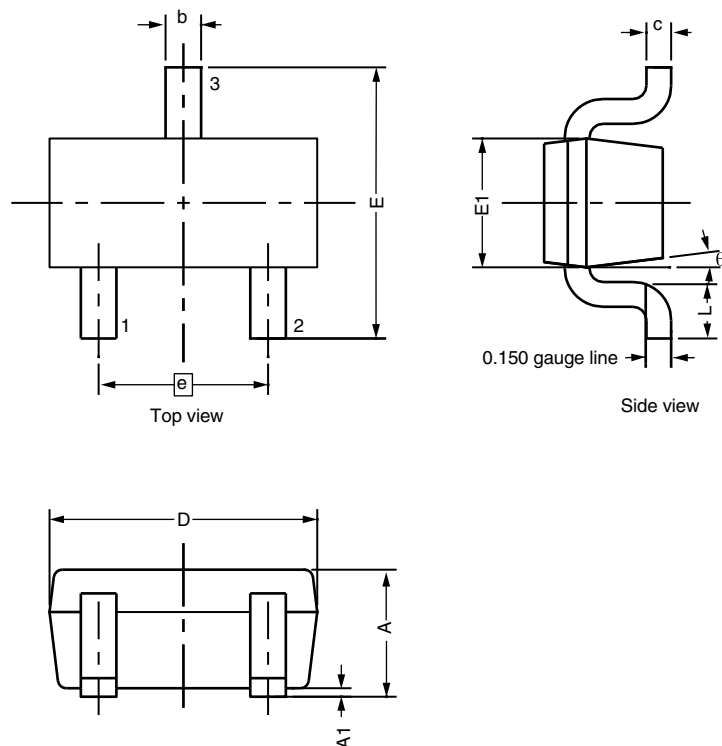
1. Dimension D does not include mold flash, protrusions or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.10 mm per end. Dimension E1 does not include interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.10 mm per side.
2. Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.
3. Datums A, B and D to be determined 0.10 mm from the lead tip.
4. Terminal positions are shown for reference only.
5. These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIMENSIONS	TOLERANCES
aaa	0.10
bbb	0.10
ccc	0.10
ddd	0.10

DIM.	MILLIMETERS			NOTE
	MIN.	NOM.	MAX.	
A	-	-	0.80	
A ₁	0.00	-	0.10	
A ₂	0.65	0.70	0.80	
B ₁	0.19	-	0.24	5
b ₁	0.17	-	0.21	
c	0.13	-	0.15	5
c ₁	0.10	-	0.12	5
D	1.48	1.575	1.68	1, 2
E	1.50	1.60	1.70	
E ₁	0.66	0.76	0.86	1, 2
e ₁	0.50 BSC			
e ₂	1.00 BSC			
e ₃	0.50 BSC			
L	0.15	0.205	0.30	
L ₁	0.40 ref.			
L ₂	0.15 BSC			
θ	0°	-	8°	
θ ₁	4°	-	10°	



For Samsung only



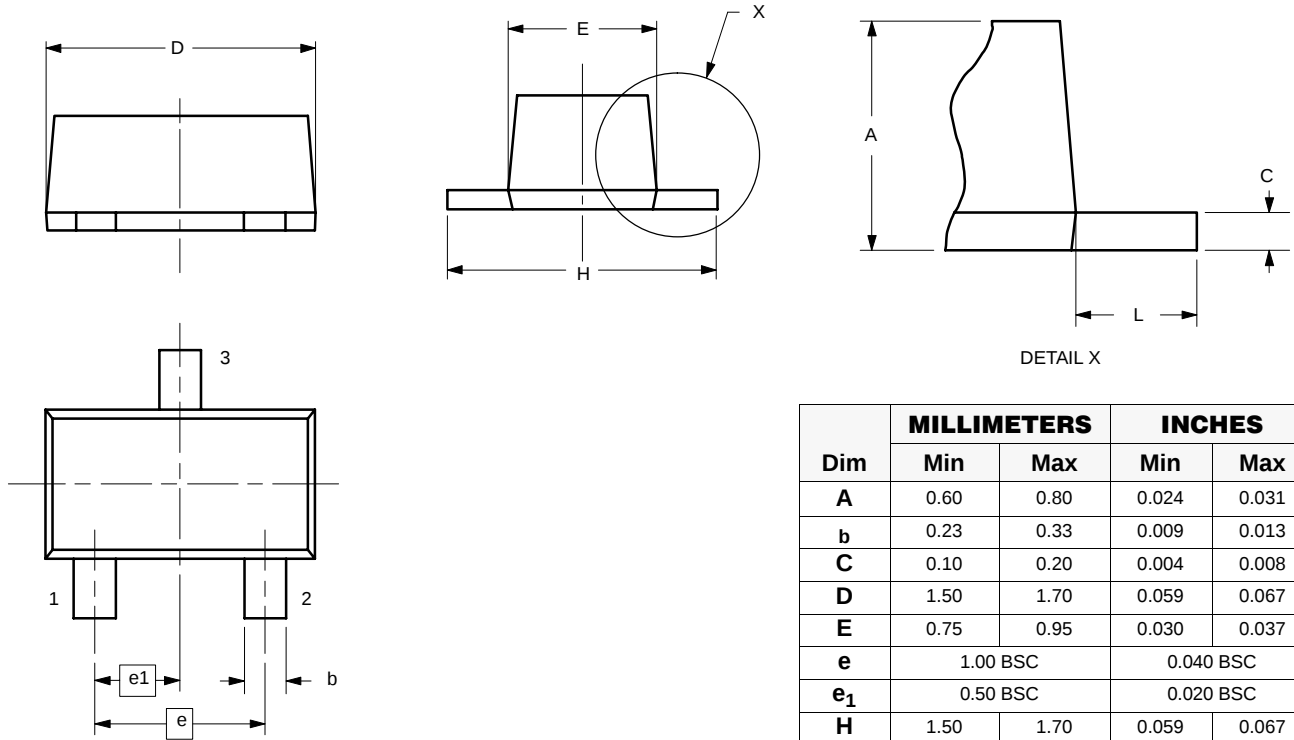
Notes

- (1) Millimeters will govern.
- (2) Dimension exclusive of mold gate burrs.
- (3) Dimension exclusive of mold flash and cutting burrs.

DIM.	MILLIMETERS			INCHES		
	MIN.	NOM.	MAX.	MIN.	NOM.	MAX.
A	0.60	0.70	0.80	0.024	0.028	0.031
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.18	0.22	0.32	0.007	0.009	0.013
c	0.11	0.13	0.21	0.004	0.005	0.008
D	1.48	1.58	1.68	0.058	0.062	0.066
E	1.50	1.60	1.70	0.059	0.063	0.067
E1	0.66	0.76	0.86	0.026	0.030	0.034
e	0.95	1.00	1.05	0.037	0.039	0.041
L	0.22	0.32	0.42	0.009	0.013	0.017
θ	4°	7°	10°	4°	7°	10°



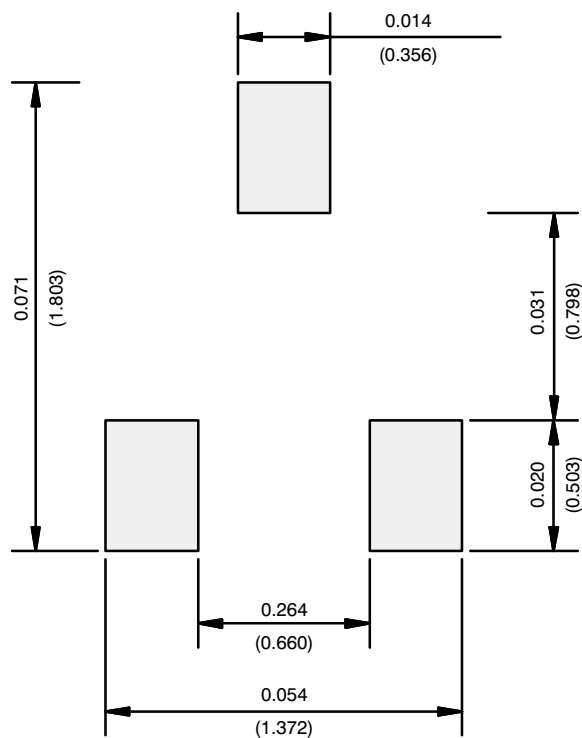
SC89-3



Dim	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	0.60	0.80	0.024	0.031
b	0.23	0.33	0.009	0.013
C	0.10	0.20	0.004	0.008
D	1.50	1.70	0.059	0.067
E	0.75	0.95	0.030	0.037
e	1.00 BSC		0.040 BSC	
e ₁	0.50 BSC		0.020 BSC	
H	1.50	1.70	0.059	0.067
L	0.30	0.50	0.012	0.020

ECN: S-03946—Rev. B, 09-Jul-01
DWG: 5869

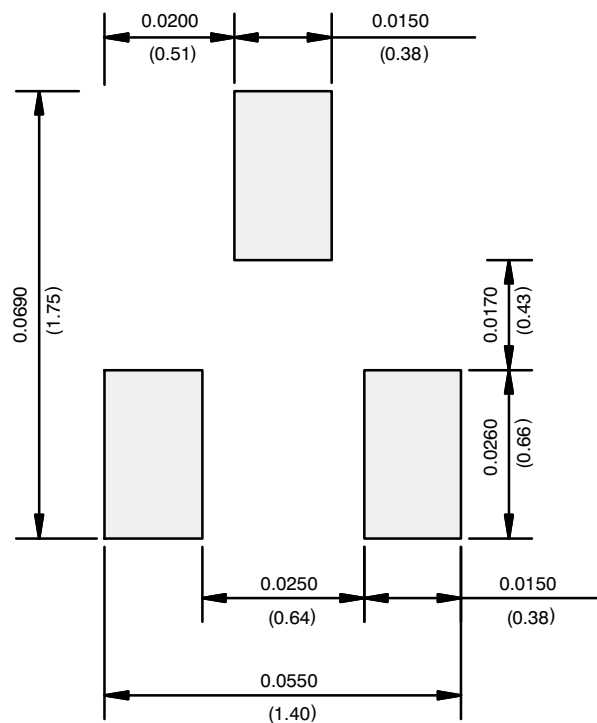
RECOMMENDED MINIMUM PADS FOR SC-75A: 3-Lead



Recommended Minimum Pads
Dimensions in Inches/(mm)

[Return to Index](#)

RECOMMENDED MINIMUM PADS FOR SC-89: 3-Lead



Recommended Minimum Pads
Dimensions in Inches/(mm)

[Return to Index](#)



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