

REV. Status

PRELIMINARY
04/26/00 JCPRELIMINARY 1
DEL. INSERTION LOSS
ADDED
INTERWINDING
CAPACITANCE
08/11/00 LLADSL transformer designed for use with Texas Instrument chip TNETD2000R,
TNETD3000R, TNETD3000P, TNETD4000R (CPE Side)

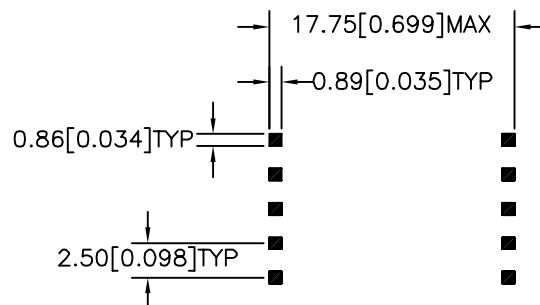
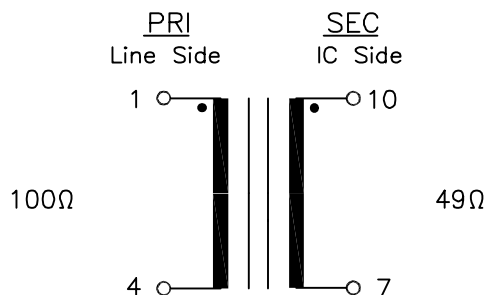
A. Electrical Specifications (@ 25 °C)

1. Primary (Line side) Impedance; 100 Ω
2. Secondary (IC side) Impedance; 49 Ω
3. Frequency Response; $\pm 0.5\text{dB}$ @ 100KHz to 1.1MHz, 0dBm
4. Longitudinal Balance; 50dB MIN @ 1.1MHz, 0dBm
5. Primary Open Circuit Inductance; 1.50mH $\pm 5\%$ @ 100KHz, 0.1Vrms Measured (1-4)
6. Leakage Inductance; 18.0 μH MAX @ 100KHz, 0.1Vrms Measured (1-4) with 7 & 10 shorted
7. Interwinding Capacitance; 35pF MAX. Measured (1-10)
8. Total Harmonic Distortion; -80dB MAX @ 100KHz, 15vpp, Measured (1-4)
9. DC Resistance; (1-4)=4.0 Ω MAX
(7-10)=0.7 Ω MAX
10. Turns Ratio; (1-4):(7-10)=1.42:1.0 $\pm 2\%$
11. Dielectric Strength; 1875Vrms 1 second @ Pri to Sec

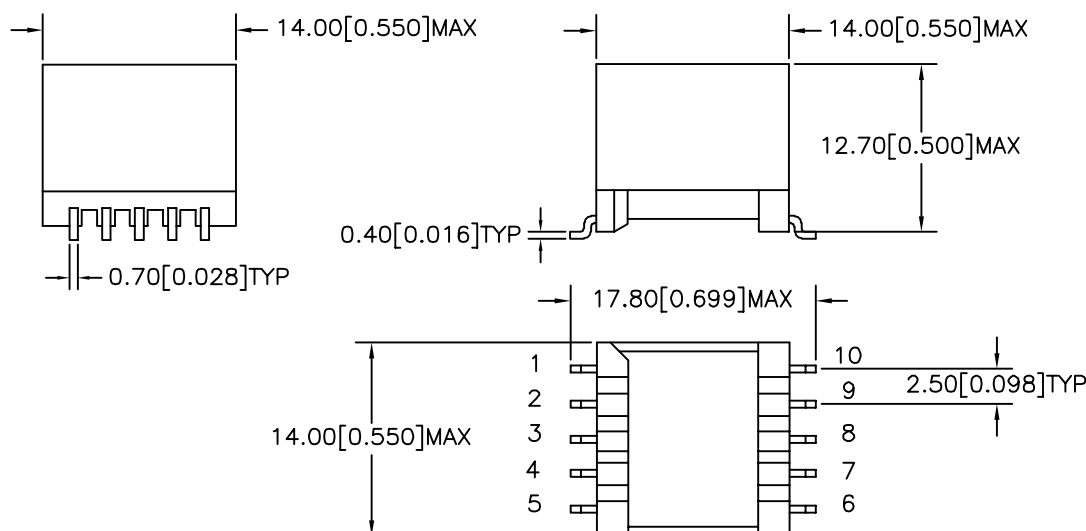
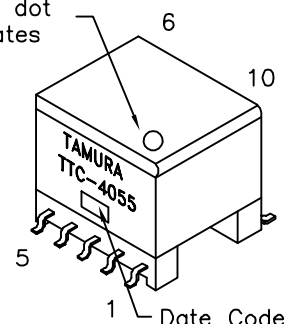
B. Marking; TTC-4055, TAMURA, date code and country of origin

C. Safety; Complies with UL1950 3rd Edition and IEC950

D. Schematic;



E. Mechanical Specifications;

White dot
indicates
Pin 1

MODEL NUMBER

TTC-4055

PREPARED BY:

D. Rund

ENGINEER:

L. Le

QUALITY CONTROL:

D. Kelley

APPROVED:

J. Coleman

DWG CONTROL NO.

P-A1-12480

ACAD\TTC\A1124801.DWG

REV

PR1

ADSL

TRANSFORMER

TTC-4055

MODEL SPECIFICATION

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DIM: mm(In) SCL: 2/1 SH: 1 OF 1

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