

TOSHIBA Field Effect Transistor Silicon N/P Channel MOS Type

SSM6L05FU

Power Management Switch

High Speed Switching Applications

- Small package
- Low on resistance Q1: $R_{on} = 0.8 \Omega$ (max) (@ $V_{GS} = 4 V$)
Q2: $R_{on} = 3.3 \Omega$ (max) (@ $V_{GS} = -4 V$)
- Low gate threshold voltage

Q1 Maximum Ratings ($T_a = 25^\circ C$)

Characteristics		Symbol	Rating	Unit
Drain-Source voltage		V_{DS}	20	V
Gate-Source voltage		V_{GSS}	± 12	V
Drain current	DC	I_D	400	mA
	Pulse	I_{DP}	800	

Q2 Maximum Ratings ($T_a = 25^\circ C$)

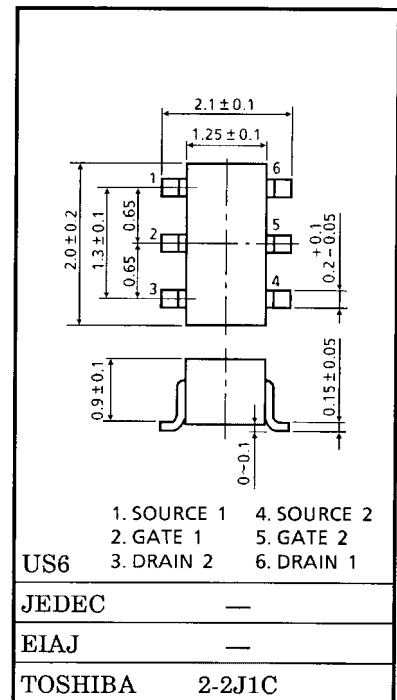
Characteristics		Symbol	Rating	Unit
Drain-Source voltage		V_{DS}	-20	V
Gate-Source voltage		V_{GSS}	± 12	V
Drain current	DC	I_D	-200	mA
	Pulse	I_{DP}	-400	

Maximum Ratings (Q1, Q2 common) ($T_a = 25^\circ C$)

Characteristics	Symbol	Rating	Unit
Drain power dissipation ($T_a = 25^\circ C$)	P_D (Note1)	300	mW
Channel temperature	T_{ch}	150	$^\circ C$
Storage temperature range	T_{stg}	-55~150	$^\circ C$

Note1: Total rating, mounted on FR4 board
(25.4 mm $\times$ 25.4 mm $\times$ 1.6 t, Cu Pad: 0.32 mm² $\times$ 6)

Unit in mm



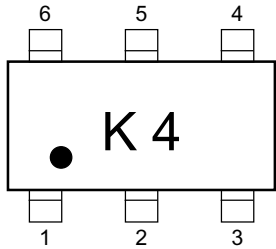
000707EAA1

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.

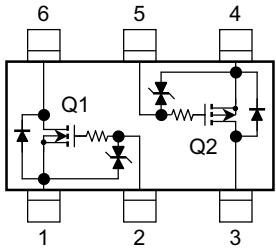
Handling Precaution

When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

Marking



Equivalent Circuit (top view)



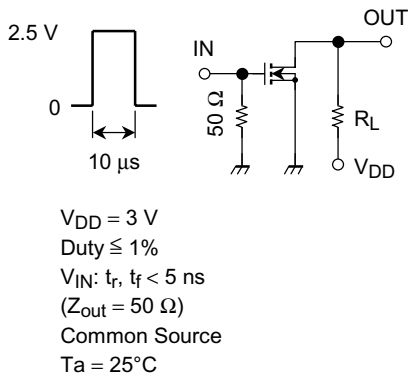
Q1 Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	$V_{GS} = \pm 12\text{ V}$, $V_{DS} = 0$	—	—	± 1	μA
Drain-Source breakdown voltage		$V_{(BR)DSS}$	$I_D = 1\text{ mA}$, $V_{GS} = 0$	20	—	—	V
Drain cut-off current		I_{DSS}	$V_{DS} = 20\text{ V}$, $V_{GS} = 0$	—	—	1	μA
Gate threshold voltage		V_{th}	$V_{DS} = 3\text{ V}$, $I_D = 0.1\text{ mA}$	0.6	—	1.1	V
Forward transfer admittance		$ Y_{fs} $	$V_{DS} = 3\text{ V}$, $I_D = 200\text{ mA}$ (Note2)	350	—	—	mS
Drain-Source ON resistance		$R_{DS(ON)}$	$I_D = 200\text{ mA}$, $V_{GS} = 4\text{ V}$ (Note2)	—	0.6	0.8	Ω
			$I_D = 200\text{ mA}$, $V_{GS} = 2.5\text{ V}$ (Note2)	—	0.85	1.2	
Input capacitance		C_{iss}	$V_{DS} = 3\text{ V}$, $V_{GS} = 0$, $f = 1\text{ MHz}$	—	22	—	pF
Reverse transfer capacitance		C_{rss}		—	9	—	pF
Output capacitance		C_{oss}		—	21	—	pF
Switching time	Turn-on time	t_{on}	$V_{DD} = 3\text{ V}$, $I_D = 100\text{ mA}$, $V_{GS} = 0 \sim 2.5\text{ V}$	—	60	—	ns
	Turn-off time	t_{off}		—	70	—	

Note2: Pulse test

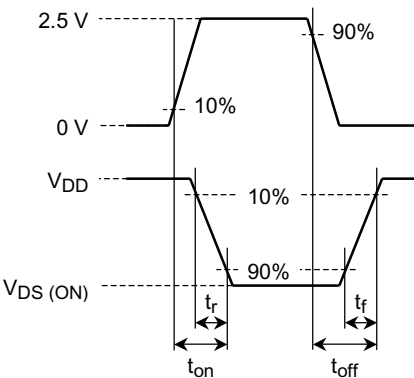
Switching Time Test Circuit (Q1: Nch MOS FET)

(a) Test circuit



(b) VIN

(c) VOUT



Precaution

V_{th} can be expressed as voltage between gate and source when low operating current value is $I_D = 100 \mu A$ for this product. For normal switching operation, $V_{GS(on)}$ requires higher voltage than V_{th} and $V_{GS(off)}$ requires lower voltage than V_{th} . (Relationship can be established as follows: $V_{GS(off)} < V_{th} < V_{GS(on)}$)

Please take this into consideration for using the device. V_{GS} recommended voltage of 2.5 V or higher to turn on this product.

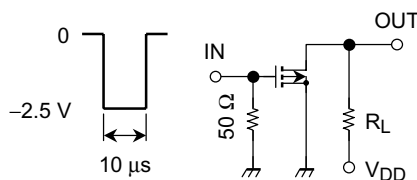
Q2 Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Typ.	Max	Unit
Gate leakage current		I_{GSS}	$V_{GS} = \pm 12 V, V_{DS} = 0$	—	—	± 1	μA
Drain-Source breakdown voltage		$V_{(BR)DSS}$	$I_D = -1 mA, V_{GS} = 0$	-20	—	—	V
Drain cut-off current		I_{DSS}	$V_{DS} = -20 V, V_{GS} = 0$	—	—	-1	μA
Gate threshold voltage		V_{th}	$V_{DS} = -3 V, I_D = -0.1 mA$	-0.6	—	-1.1	V
Forward transfer admittance		$ Y_{fs} $	$V_{DS} = -3 V, I_D = -50 mA$ (Note2)	100	—	—	mS
Drain-Source ON resistance		$R_{DS(ON)}$	$I_D = -100 mA, V_{GS} = -4 V$ (Note2)	—	2.1	3.3	Ω
			$I_D = -50 mA, V_{GS} = -2.5 V$ (Note2)	—	3.2	4.0	
Input capacitance		C_{iss}	$V_{DS} = -3 V, V_{GS} = 0, f = 1 MHz$	—	27	—	pF
Reverse transfer capacitance		C_{rss}		—	7	—	pF
Output capacitance		C_{oss}		—	21	—	pF
Switching time	Turn-on time	t_{on}	$V_{DD} = -3 V, I_D = -50 mA,$ $V_{GS} = 0 \sim -2.5 V$	—	70	—	ns
	Turn-off time	t_{off}		—	70	—	

Note2: Pulse test

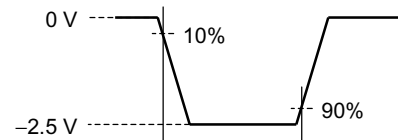
Switching Time Test Circuit (Q2: Pch MOS FET)

(a) Test circuit

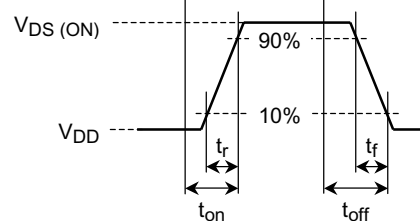


$V_{DD} = -3 V$
Duty $\leq 1\%$
 V_{IN} : $t_r, t_f < 5 ns$
($Z_{out} = 50 \Omega$)
Common Source
 $T_a = 25^\circ C$

(b) V_{IN}



(c) V_{OUT}

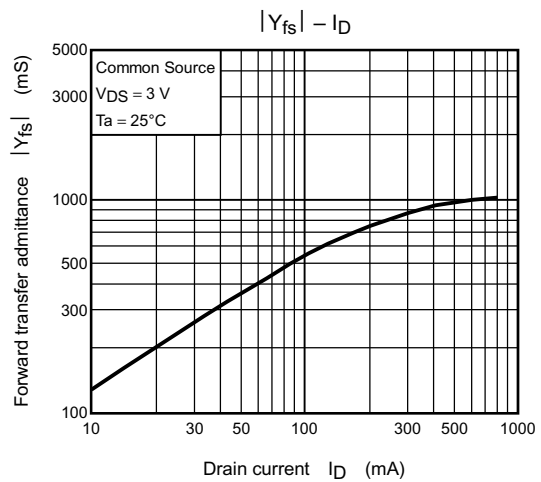
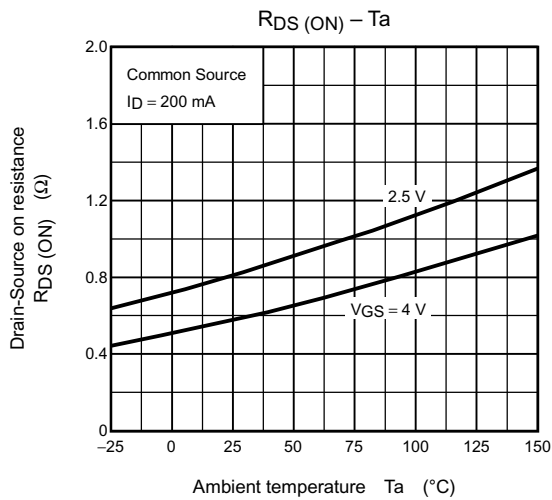
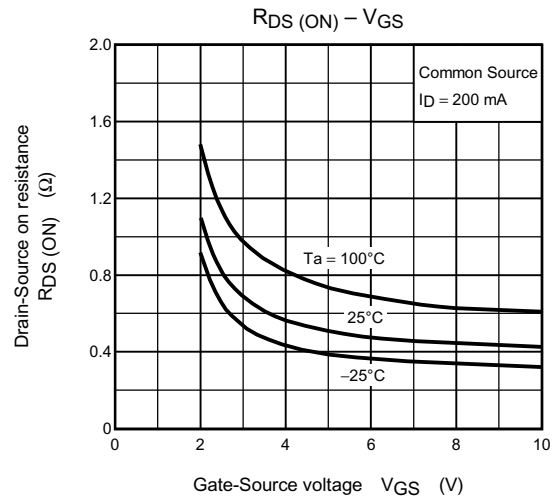
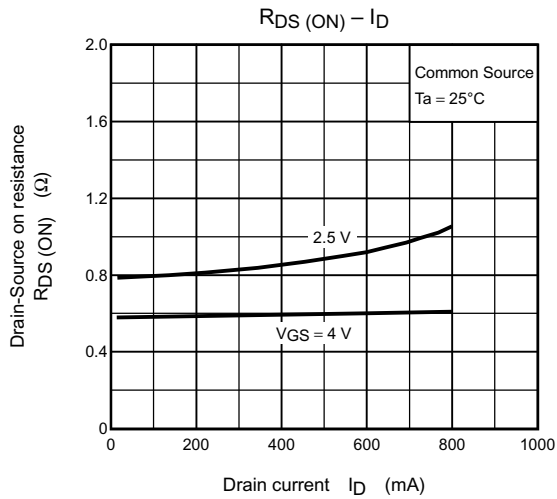
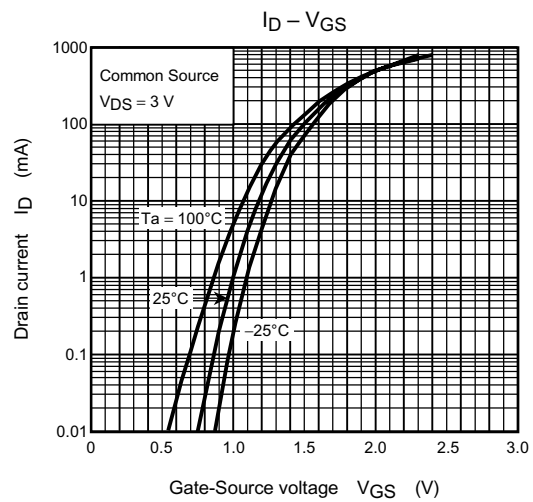
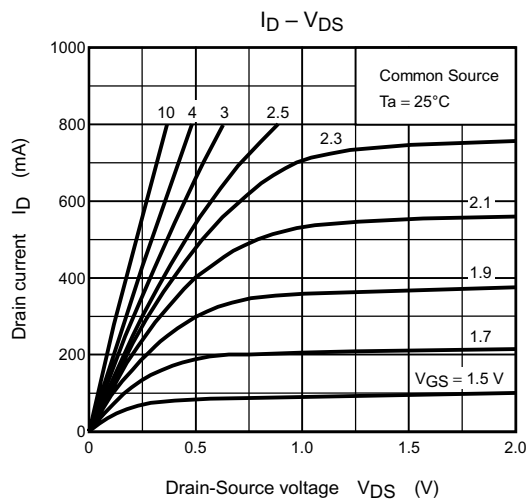


Precaution

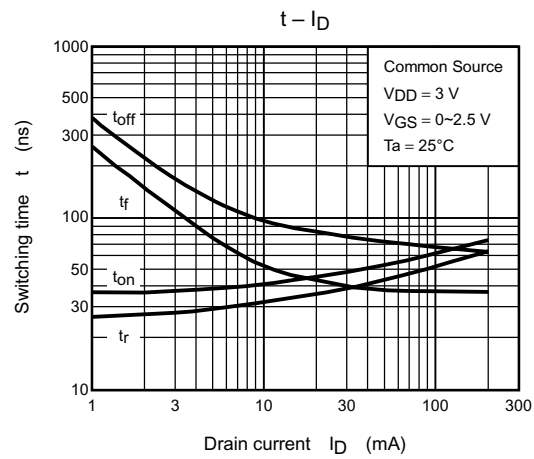
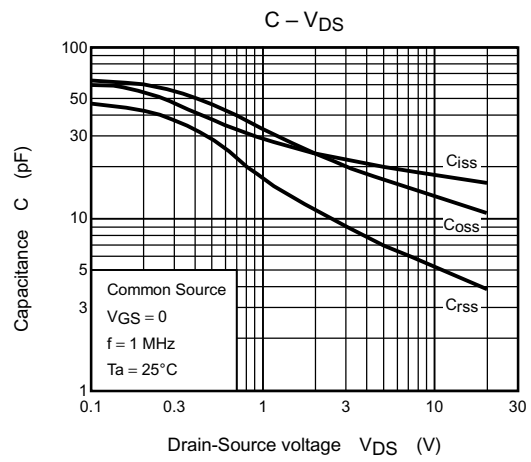
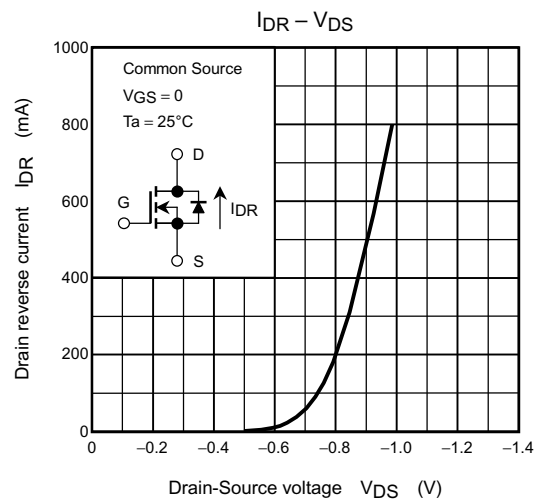
V_{th} can be expressed as voltage between gate and source when low operating current value is $I_D = -100 \mu A$ for this product. For normal switching operation, $V_{GS(on)}$ requires higher voltage than V_{th} and $V_{GS(off)}$ requires lower voltage than V_{th} . (Relationship can be established as follows: $V_{GS(off)} < V_{th} < V_{GS(on)}$)

Please take this into consideration for using the device. V_{GS} recommended voltage of -2.5 V or higher to turn on this product.

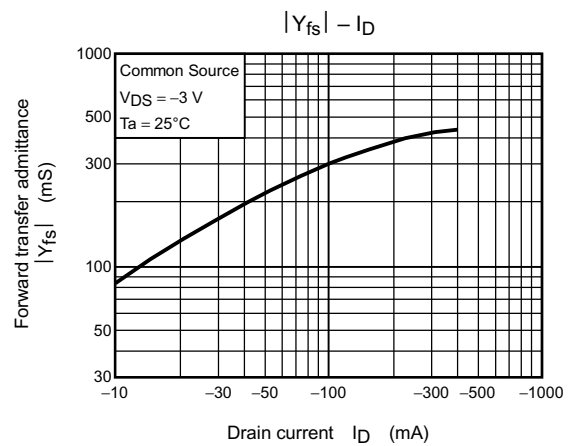
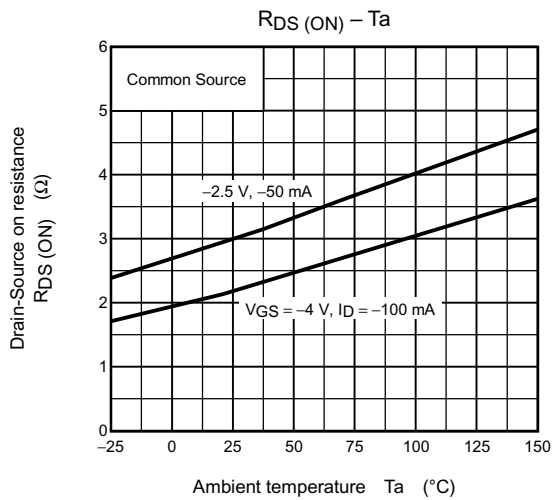
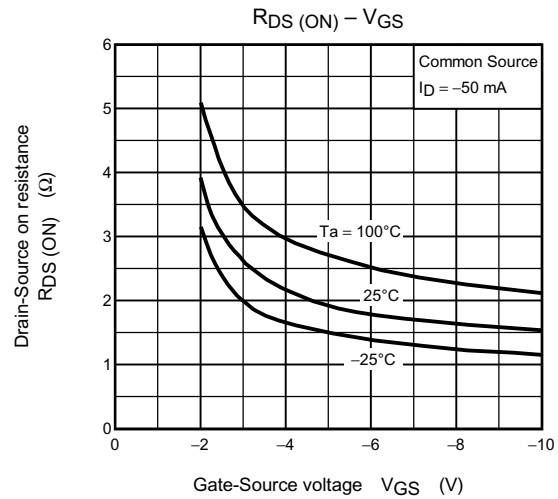
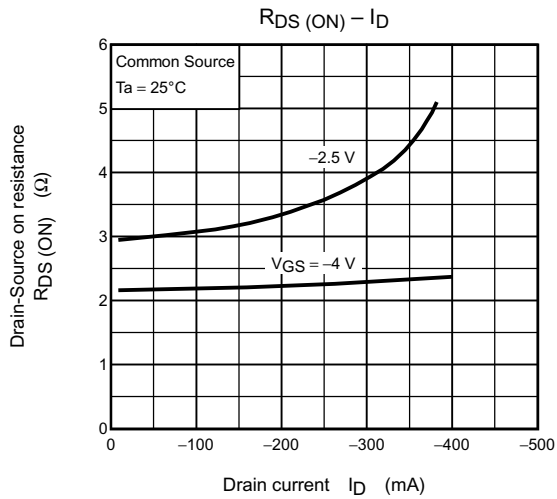
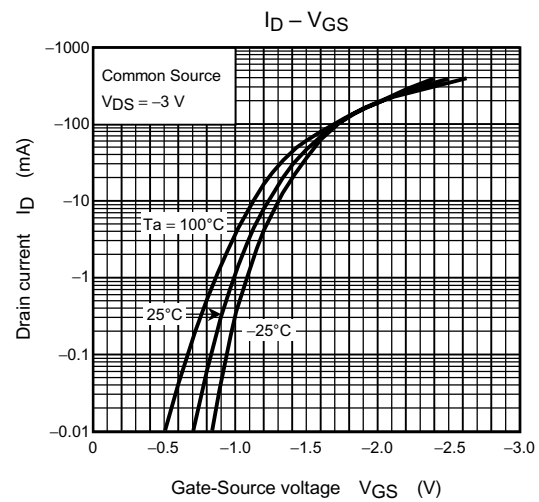
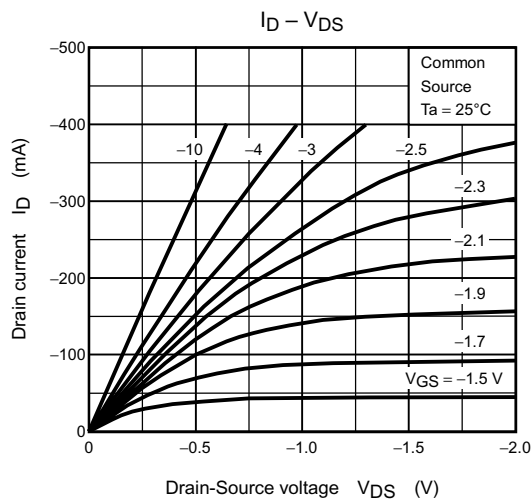
Q1 (Nch MOS FET)



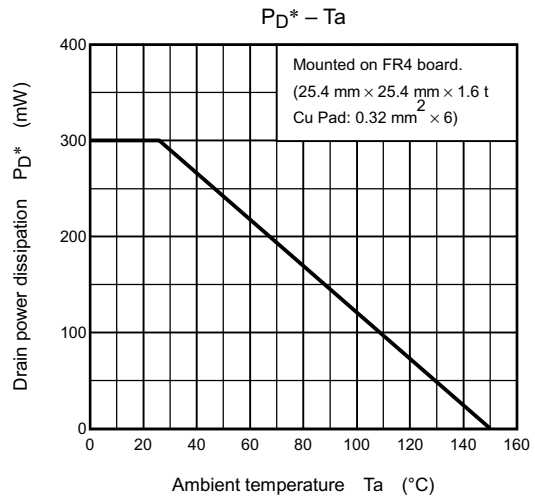
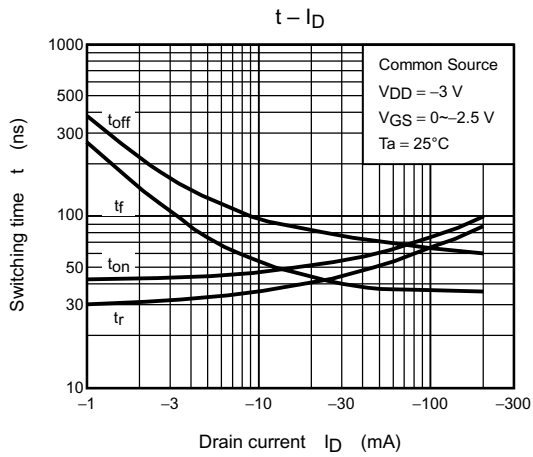
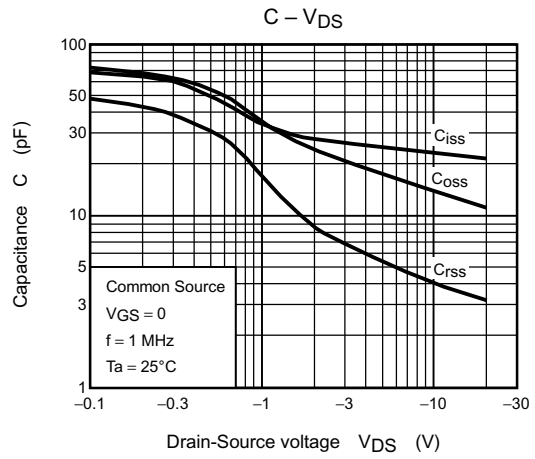
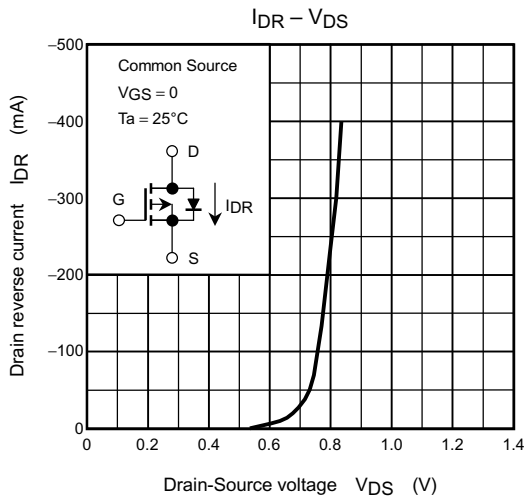
Q1 (Nch MOS FET)



Q2 (Pch MOS FET)



Q2 (Pch MOS FET)



*: Total rating