

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)} \text{ max}$	$I_D \text{ max}$ $T_A = +25^\circ\text{C}$
-20V	1.0Ω @ $V_{GS} = -4.5\text{V}$	-330mA
	1.2Ω @ $V_{GS} = -2.5\text{V}$	
	1.6Ω @ $V_{GS} = -1.8\text{V}$	
	3.0Ω @ $V_{GS} = -1.5\text{V}$	

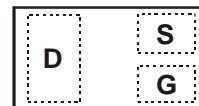
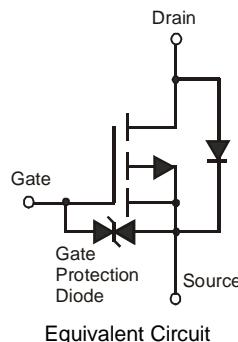
Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch



Bottom View

Top View
Package Pin Configuration

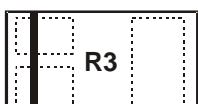
Ordering Information (Note 4)

Part Number	Case	Packaging
DMP21D2UFA-7B	X2-DFN0806-3	10K/Tape & Reel

Notes:

- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



R3 = Product Type Marking Code

Top View
Bar Denotes Gate
and Source Side

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	-20	V
Gate-Source Voltage			V_{GSS}	± 8	V
Continuous Drain Current (Note 5) $V_{GS} = -4.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$ $T_A = +70^\circ\text{C}$	I_D	-330 -260	mA
Pulsed Drain Current (Note 6)			I_{DM}	1.5	A

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)	Steady state	P_D	360	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	$R_{\theta JA}$	353	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-20	—	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current @ $T_C = +25^\circ\text{C}$	I_{DSS}	—	—	100	nA	$V_{DS} = -16\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 8\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.3	—	-1.0	V	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(\text{ON})}$	—	0.5	1.0	Ω	$V_{GS} = -4.5\text{V}, I_D = -200\text{mA}$
		—	0.6	1.2		$V_{GS} = -2.5\text{V}, I_D = -100\text{mA}$
		—	0.8	1.6		$V_{GS} = -1.8\text{V}, I_D = -50\text{mA}$
		—	1.0	3.0		$V_{GS} = -1.5\text{V}, I_D = -10\text{mA}$
Diode Forward Voltage	V_{SD}	—	—	-1.0	V	$V_{GS} = 0\text{V}, I_S = -10\text{mA}$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	49	—	pF	$V_{DS} = -15\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	6.5	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	5.0	—	pF	
Total Gate Charge	Q_g	—	0.8	—	nC	$V_{GS} = -4.5\text{V}, V_{DS} = -10\text{V}, I_D = -200\text{mA}$
Gate-Source Charge	Q_{gs}	—	0.1	—	nC	
Gate-Drain Charge	Q_{gd}	—	0.2	—	nC	
Turn-On Delay Time	$t_{D(\text{on})}$	—	10.3	—	ns	$V_{DD} = -15\text{V}, V_{GS} = -4.5\text{V}, R_G = 2\Omega, I_D = -200\text{mA}$
Turn-On Rise Time	t_r	—	37.3	—	ns	
Turn-Off Delay Time	$t_{D(\text{off})}$	—	330	—	ns	
Turn-Off Fall Time	t_f	—	163	—	ns	

Notes:

5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
6. Device mounted on minimum recommended pad layout test board, 10 μs pulse duty cycle = 1%.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

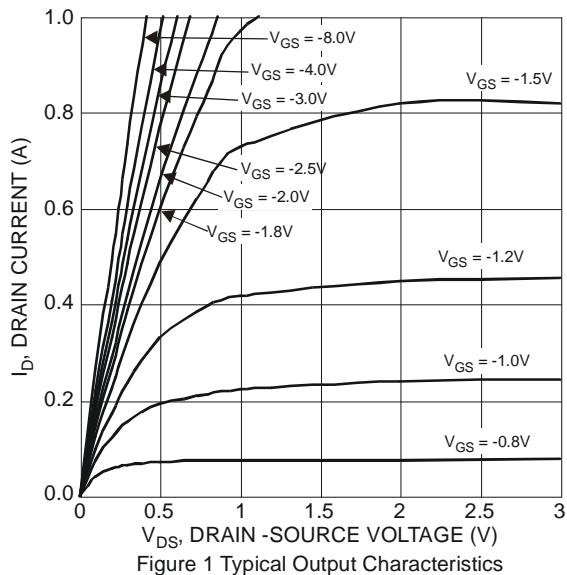


Figure 1 Typical Output Characteristics

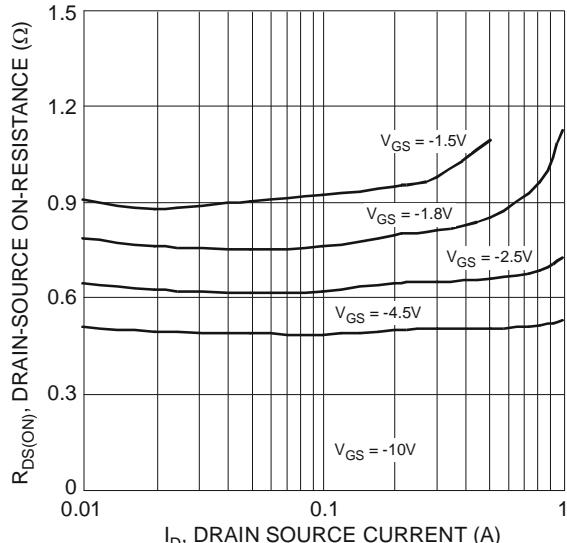
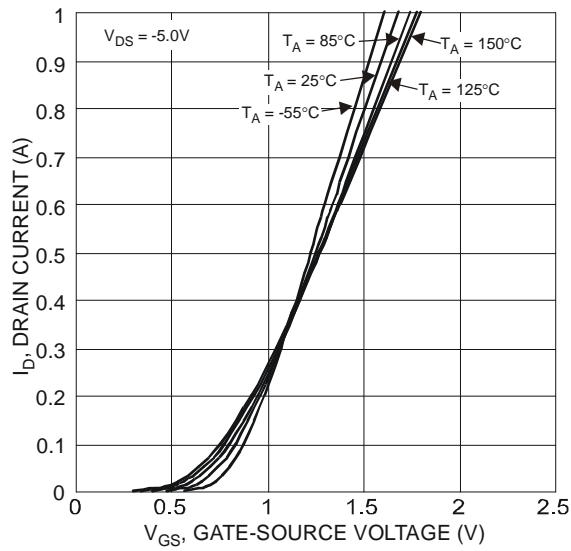
Figure 3 Typical On-Resistance vs.
Drain Current and Gate Voltage

Figure 2 Typical Transfer Characteristics

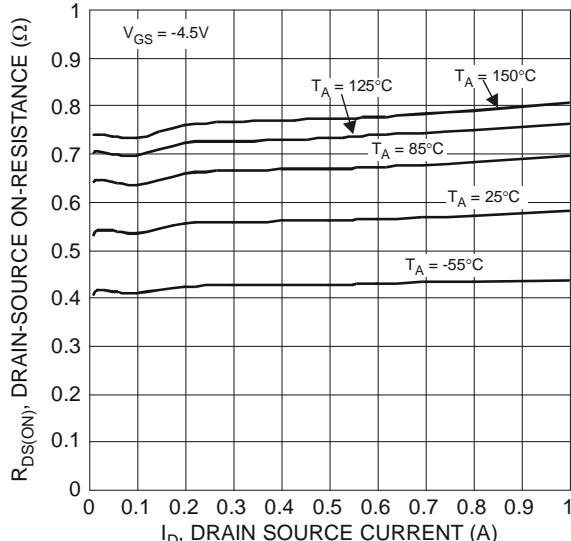
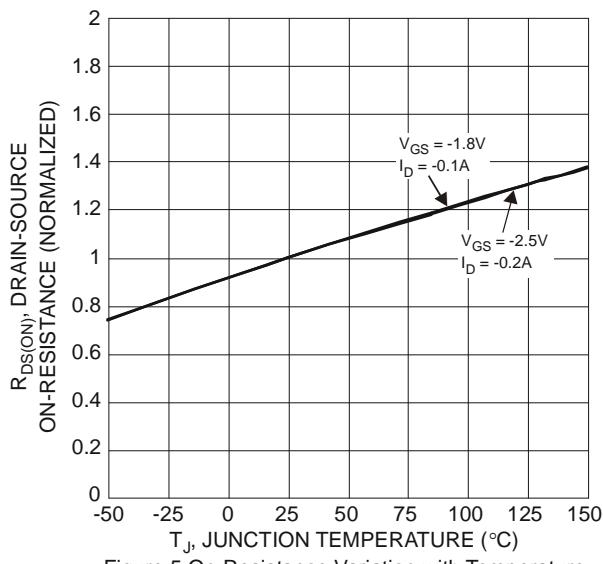
Figure 4 Typical On-Resistance vs.
Drain Current and Temperature

Figure 5 On-Resistance Variation with Temperature

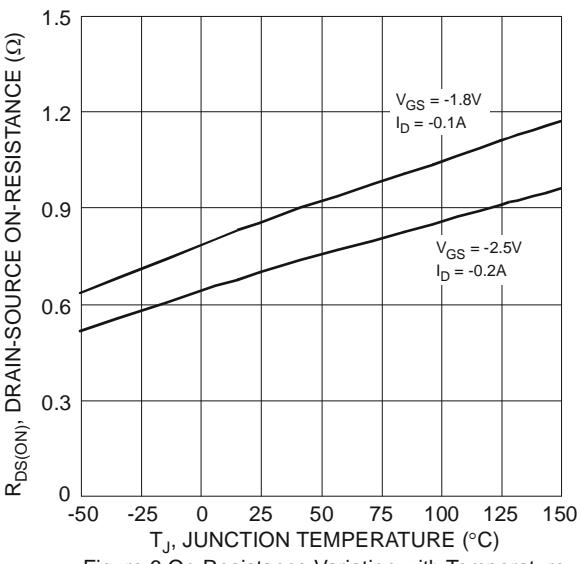


Figure 6 On-Resistance Variation with Temperature

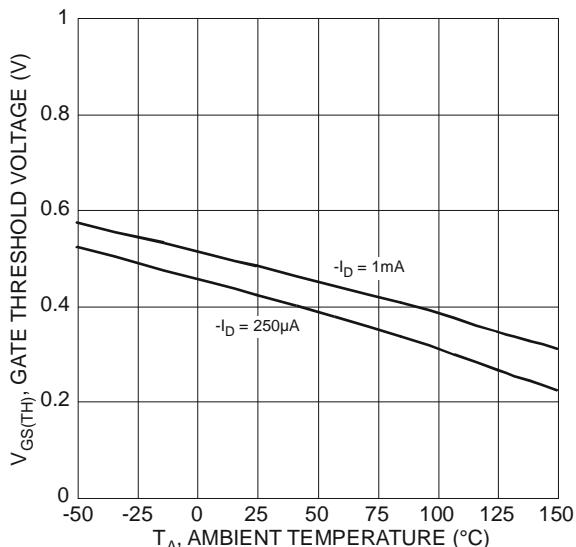


Figure 7 Gate Threshold Variation vs. Ambient Temperature

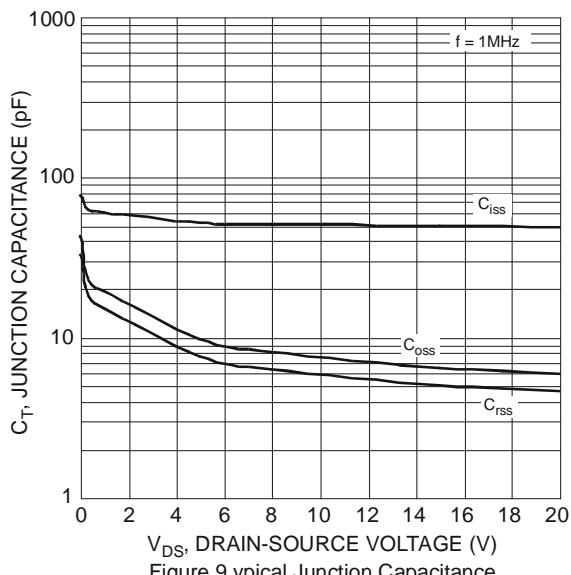


Figure 9 typical Junction Capacitance

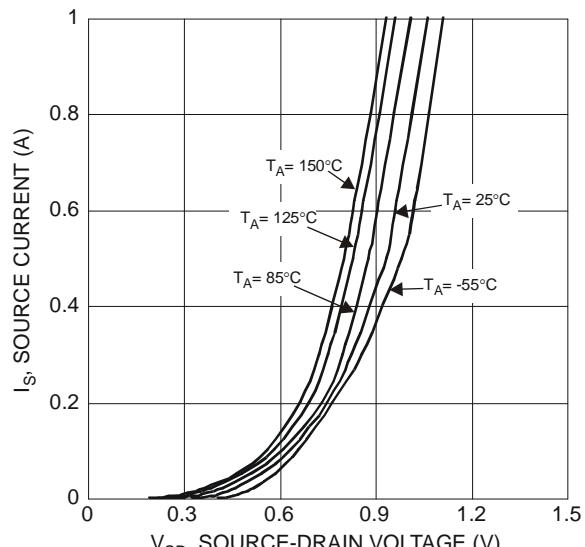


Figure 8 Diode Forward Voltage vs. Current

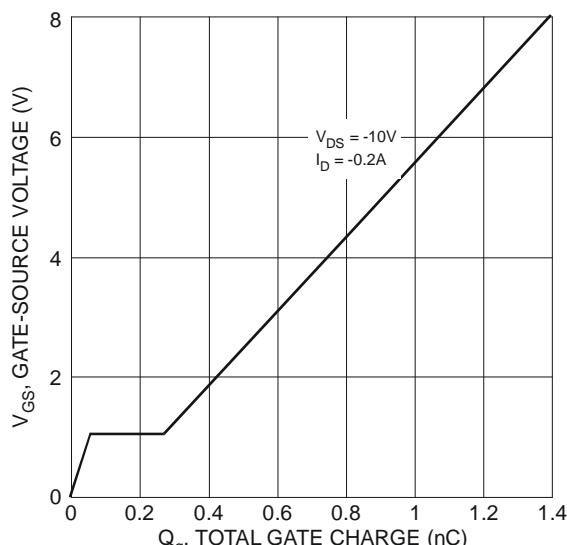


Figure 10 Gate-Charge Characteristics

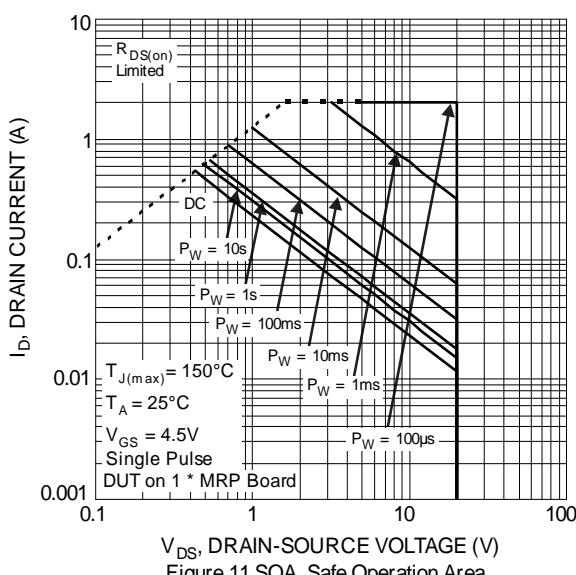
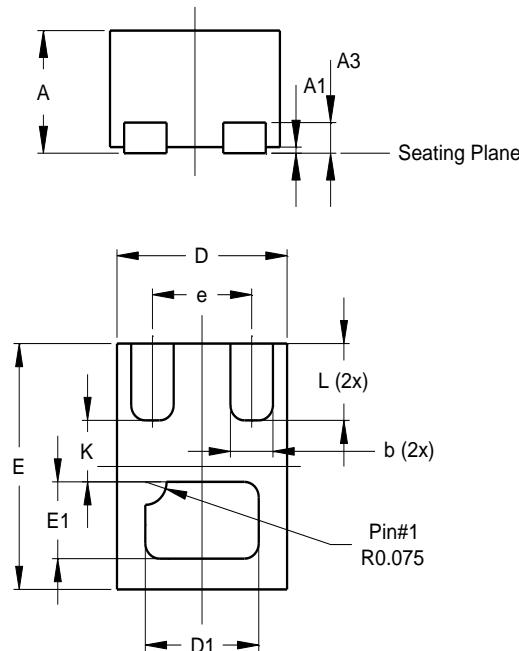


Figure 11 SOA, Safe Operation Area

Package Outline Dimensions

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.

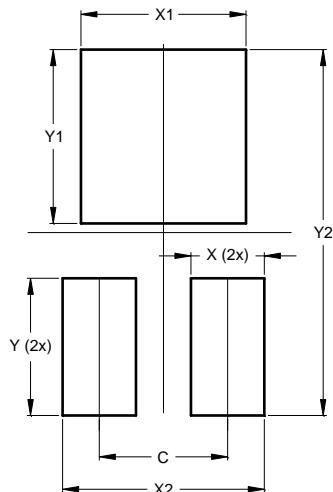


X2-DFN0806-3			
Dim	Min	Max	Typ
A	0.375	0.40	0.39
A1	0	0.05	0.02
A3	-	-	0.10
b	0.10	0.20	0.15
D	0.55	0.65	0.60
D1	0.35	0.45	0.40
E	0.75	0.85	0.80
E1	0.20	0.30	0.25
e	-	-	0.35
K	-	-	0.20
L	0.20	0.30	0.25

All Dimensions in mm

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	0.350
X	0.200
X1	0.450
X2	0.550
Y	0.375
Y1	0.475
Y2	1.000

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