

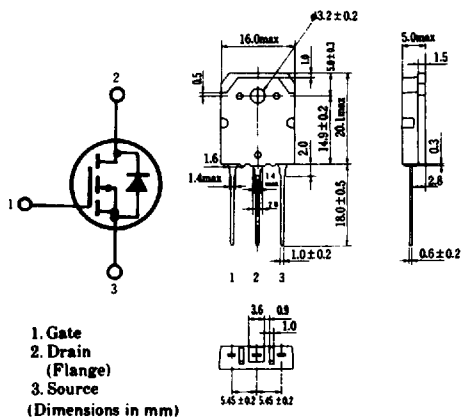
SILICON N-CHANNEL MOS FET

HIGH SPEED POWER SWITCHING, HIGH FREQUENCY POWER AMPLIFIER

Complementary pair with 2SJ113

■ FEATURES

- **Low On-Resistance.**
- **High Speed Switching.**
- **High Cutoff Frequency.**
- **No Secondary Breakdown.**
- **Suitable for Switching Regulator, DC-DC Converter, Motor Control, and Ultrasonic Power Oscillators.**



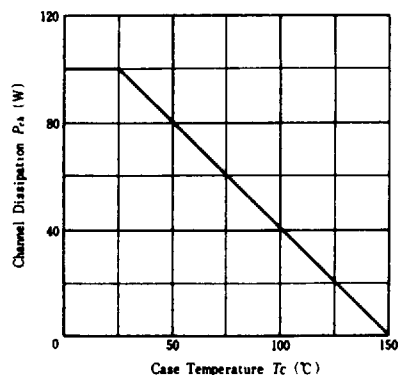
(TO-3P)

■ **ABSOLUTE MAXIMUM RATINGS** ($T_a=25^\circ\text{C}$)

Item	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current	I_D	10	A
Drain Peak Current	$I_{D(\text{peak})}$	15	A
Body-Drain Diode Reverse Drain Current	I_{DR}	10	A
Channel Dissipation	P_{ch}^*	100	W
Channel Temperature	T_{ch}	150	$^{\circ}\text{C}$
Storage Temperature	T_{stg}	$-55 \sim +150$	$^{\circ}\text{C}$

*Value at $T_c=25^\circ\text{C}$

POWER VS. TEMPERATURE DERATING

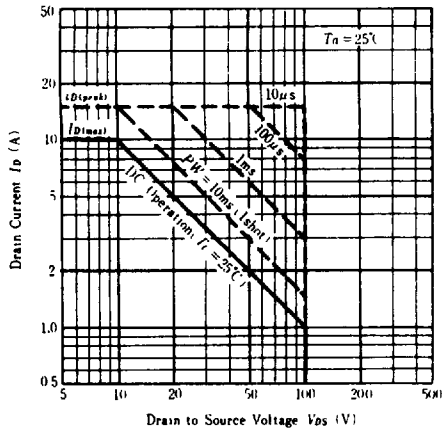


■ ELECTRICAL CHARACTERISTICS ($T_a=25^\circ\text{C}$)

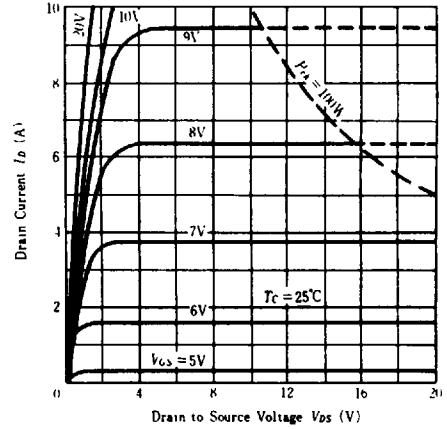
Item	Symbol	Test Condition	min.	typ.	max.	Unit
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=10mA, V_{GS}=0$	100	—	—	V
Gate-Source Leak Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0$	—	—	± 1	μA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=80V, V_{GS}=0$	—	—	1	mA
Gate-Source Cutoff Voltage	$V_{GS(off)}$	$I_D=1mA, V_{DS}=10V$	2.0	—	5.0	V
Static Drain-Source On State Resistance	$R_{DS(on)}$	$I_D=5A, V_{GS}=15V^*$	—	0.20	0.25	Ω
Drain-Source Saturation Voltage	$V_{DS(sat)}$	$I_D=5A, V_{GS}=15V^*$	—	1.0	1.25	V
Forward Transfer Admittance	$ y_{fs} $	$I_D=5A, V_{DS}=10V^*$	1.5	2.0	—	S
Input Capacitance	C_{iss}	$V_{DS}=10V, V_{GS}=0$ $f=1MHz$	—	800	—	pF
Output Capacitance	C_{oss}		—	500	—	pF
Reverse Transfer Capacitance	C_{rss}		—	70	—	pF
Turn-on Delay Time	$t_{d(on)}$	$I_D=2A, V_{GS}=15V$ $R_L=15\Omega$	—	15	—	ns
Rise Time	t_r		—	35	—	ns
Turn-off Delay Time	$t_{d(off)}$		—	60	—	ns
Fall Time	t_f		—	50	—	ns
Body-Drain Diode Forward Voltage	V_{DF}	$I_F=5A, V_{GS}=0$	—	0.9	—	V
Body-Drain Diode Reverse Recovery Time	t_r	$I_F=5A, V_{GS}=0$ $di_F/dt=50A/\mu s$	—	300	—	ns

- Pulse Test

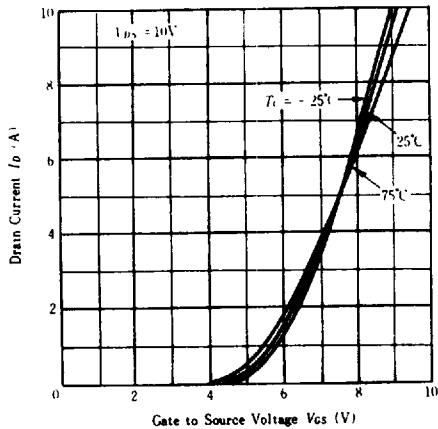
MAXIMUM SAFE OPERATION AREA



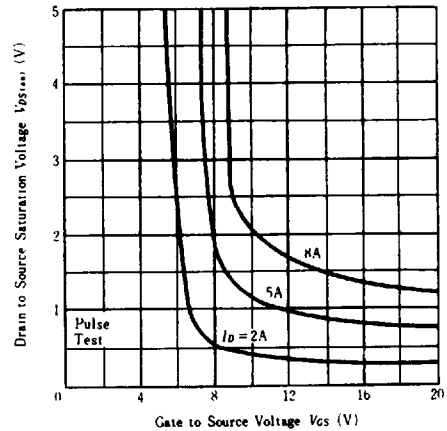
TYPICAL OUTPUT CHARACTERISTICS



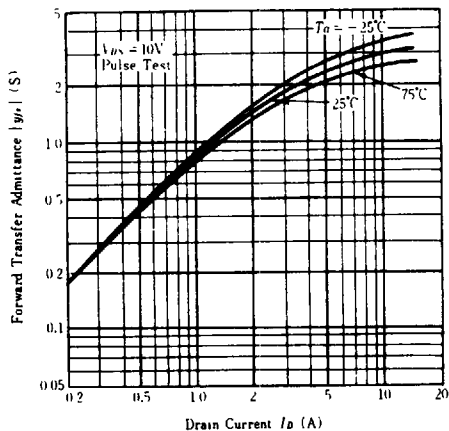
TYPICAL TRANSFER CHARACTERISTICS



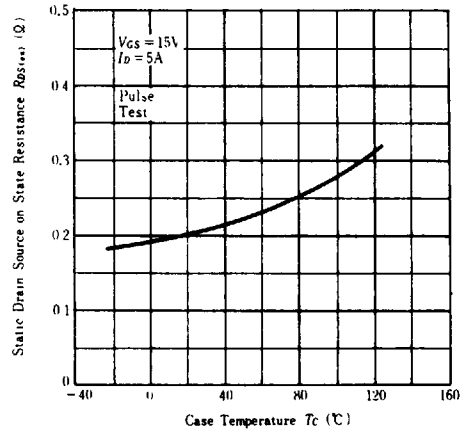
DRAIN-SOURCE SATURATION VOLTAGE VS. GATE-SOURCE VOLTAGE



FORWARD TRANSFER ADMITTANCE VS. DRAIN CURRENT

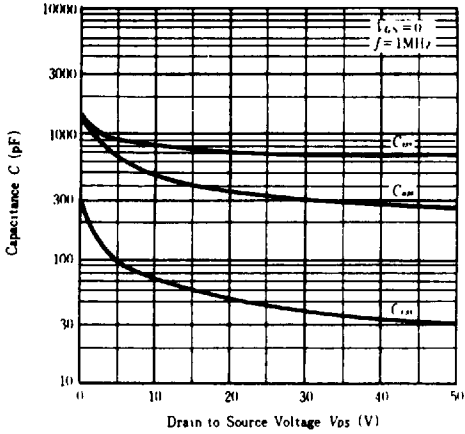


STATIC DRAIN-SOURCE ON STATE RESISTANCE VS. TEMPERATURE

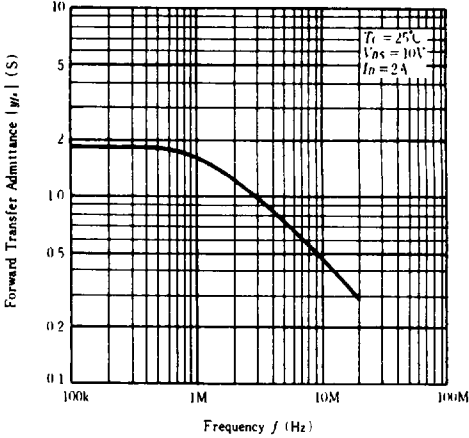


HITACHI/(OPTOELECTRONICS)

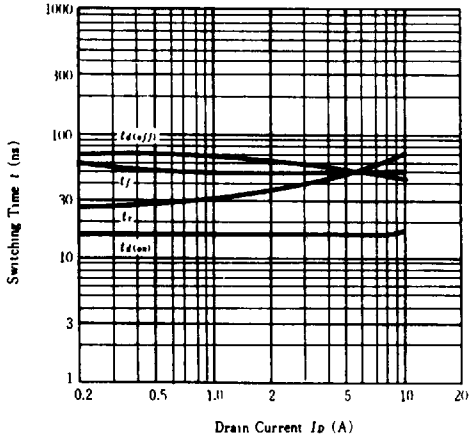
TYPICAL CAPACITANCE VS. DRAIN-SOURCE VOLTAGE



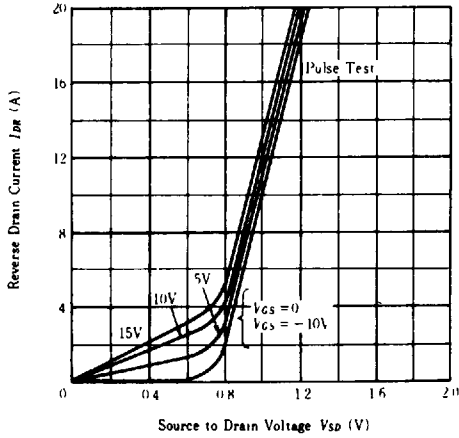
FORWARD TRANSFER ADMITTANCE VS. FREQUENCY



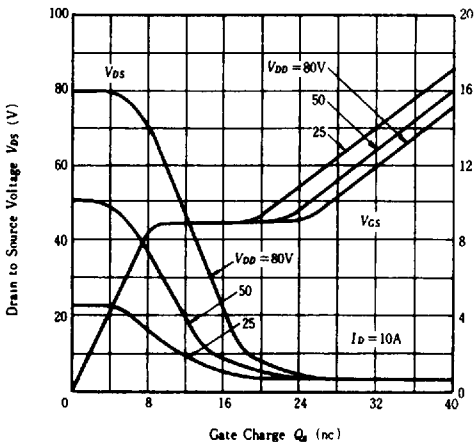
SWITCHING CHARACTERISTICS



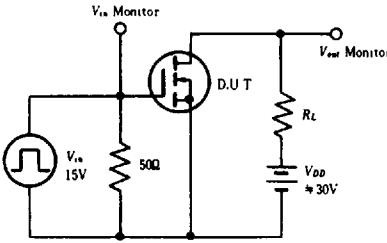
MAXIMUM BODY-DRAIN DIODE FORWARD VOLTAGE



DYNAMIC INPUT CHARACTERISTICS



SWITCHING TIME TEST CIRCUIT



WAVEFORMS

