

DF3A6.2LFV

Diodes for Protecting against ESD

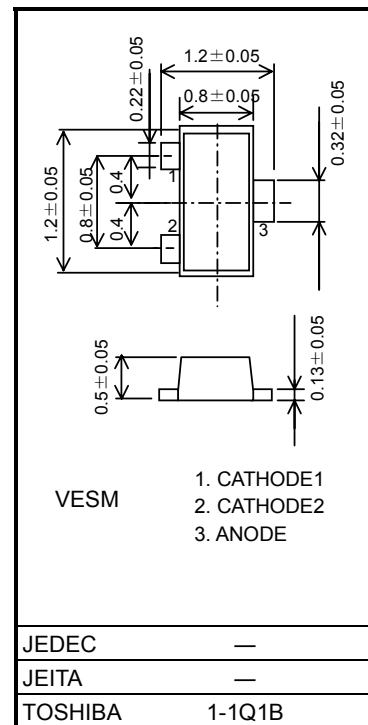
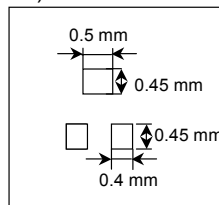
Unit: mm

- The mounting of two devices in an ultra-compact package enables a reduction in the number of parts and in the mounting cost.
- The Zener voltage corresponds to that of the E24 Series.
- Low total capacitance: $C_T = 6.5$ pF (typ.)
- Lead Pb - free

Maximum Ratings ($T_a = 25^\circ\text{C}$)

Characteristic	Symbol	Rating	Unit
Power dissipation	P	150 *	mW
Junction temperature	T_j	150	$^\circ\text{C}$
Storage temperature range	T_{stg}	-55~150	$^\circ\text{C}$

* : Mounted on FR4 board (25.4 mm × 25.4 mm × 1.6 mm)



JEDEC	—
JEITA	—
TOSHIBA	1-1Q1B

Weight: 0.0015 g (typ.)

Electrical Characteristics ($T_a = 25^\circ\text{C}$)

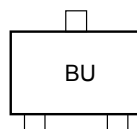
Characteristic	Symbol	Test Condition	Min	Typ.	Max	Unit
Zener voltage	V_Z	$I_Z = 5$ mA	5.9	6.2	6.5	V
Dynamic impedance	Z_Z	$I_Z = 5$ mA	—	—	50	Ω
Reverse current	I_R	$V_R = 5$ V	—	—	2.5	μA
Terminal capacitance (between cathode and anode)	C_T	$V_R = 0$ V, $f = 1$ MHz	—	6.5	—	pF

Guaranteed Level of ESD Immunity

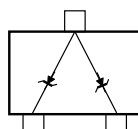
Test Condition	ESD Immunity Level
IEC61000-4-2 (Contact discharge)	± 8 kV

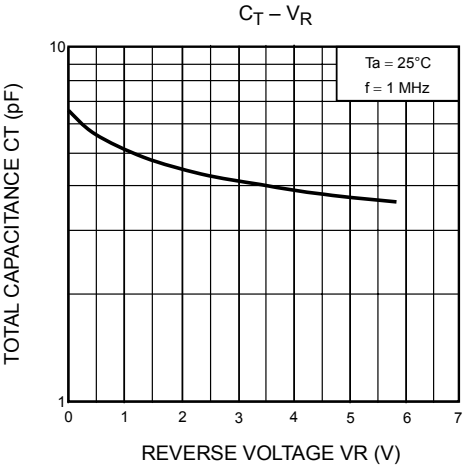
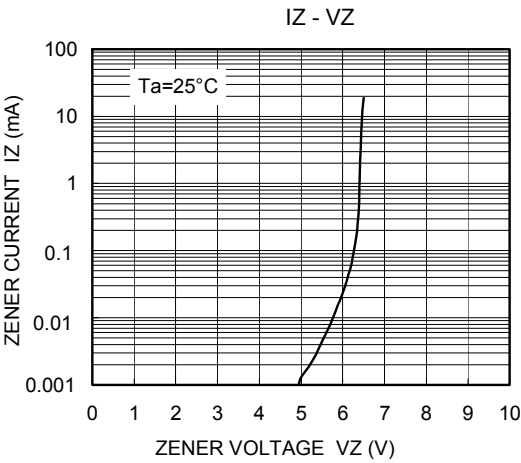
Criterion: No damage to device elements

Marking



Equivalent Circuit (top view)





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