

Wide dynamic range AGC transimpedance amplifier(150MHz)

SA5223

DESCRIPTION

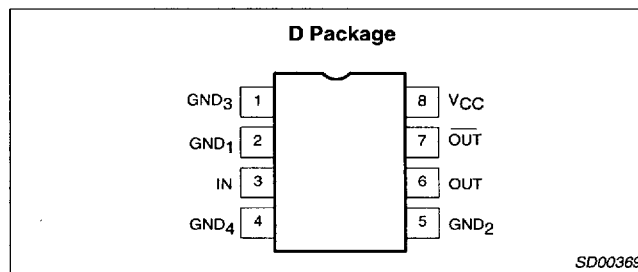
The SA5223 is a wide-band, low-noise transimpedance amplifier with differential outputs, incorporating AGC and optimized for signal recovery in wide-dynamic-range fiber optic receivers, such as SONET. The part is also suited for many other RF and fiber optic applications as a general purpose gain block.

The SA5223 is the first AGC amplifier to incorporate internal AGC loop hold capacitor, therefore, no external components are required. The internal AGC loop enables the SA5223 to effortlessly handle bursty data over a range of nA to mA of signal current, positive direction (sinking) only.

FEATURES

- Extremely low noise: $1.17\text{pA}/\sqrt{\text{Hz}}$
- Single 5V supply
- Low supply current: 22mA
- Large bandwidth: 150MHz
- Differential outputs
- Internal hold capacitor
- Low input/output impedances
- High power-supply-rejection ratio: 55dB
- Tight transresistance control
- High input overload: 4mA
- 2000V HBM ESD protection

PIN DESCRIPTION



APPLICATIONS

- OC3 SONET preamp (see AN1431 for detailed analysis)
- Current-to-voltage converters
- Wide-band gain block
- Medical and scientific instrumentation
- Sensor preamplifiers
- Single-ended to differential conversion
- Low noise RF amplifiers
- RF signal processing

ORDERING INFORMATION

DESCRIPTION	TEMPERATURE RANGE	ORDER CODE	DWG #
8-Pin Plastic Small Outline	-40 to +85°C	SA5223D	SOT96-1

For unpackaged die please contact factory.

ABSOLUTE MAXIMUM RATINGS

SYMBOL	PARAMETER	RATING	UNITS
V _{CC}	Power supply voltage	6	V
T _A	Ambient temperature range	-40 to +85	°C
T _J	Junction temperature range	-55 to +150	°C
T _{STG}	Storage temperature range	-65 to +150	°C
P _D	Power dissipation T _A = 25°C (still air) ¹	0.78	W
I _{INMAX}	Maximum input current	5	mA

NOTE:

1. Maximum power dissipation is determined by the operating ambient temperature and the thermal resistance $\theta_{JA} = 158^\circ\text{C}/\text{W}$. Derate 6.2mW/°C above 25°C.

RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	RATING	UNITS
V _{CC}	Power supply voltage	4.5 to 5.5	V
T _A	Ambient temperature range: SA grade	-40 to +85	°C
T _J	Junction temperature range: SA grade	-40 to +105	°C

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DC ELECTRICAL CHARACTERISTICS

Typical data and Min and Max limits apply at $T_A = 25^\circ\text{C}$, and $V_{CC} = +5\text{V}$, unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	SA5223			UNIT
			Min	Typ	Max	
V_{IN}	Input bias voltage		1.3	1.55	1.8	V
$V_{O\pm}$	Output bias voltage		2.9	3.2	3.5	V
V_{OS}	Output offset voltage ($V_{PIN6} - V_{PIN7}$)		-200	80	+200	mV
I_{CC}	Supply current		15	22	29	mA
I_{OMAX}	Output sink/source current		1.5	2		mA

NOTE: Standard deviations are estimated from design simulations to represent manufacturing variations over the life of the product.

AC ELECTRICAL CHARACTERISTICS

Typical data and Min and Max limits apply at $T_A = 25^\circ\text{C}$ and $V_{CC} = +5\text{V}$, unless otherwise specified.

SYMBOL	PARAMETER	TEST CONDITIONS	SA5223			UNIT
			Min	Typ	Max	
R_T	Transresistance (differential output)	DC tested, $R_L = \infty$, $I_{IN} = 0-1\mu\text{A}$	90	125	160	k Ω
R_T	Transresistance (single-ended output)	DC tested, $R_L = \infty$, $I_{IN} = 0-1\mu\text{A}$	45	62.5	80	k Ω
R_O	Output resistance (differential output)	DC tested		140		Ω
R_O	Output resistance (single-ended output)	DC tested		70		Ω
f_{3dB}	Bandwidth (-3dB)	Test Circuit 1	110	150		MHz
R_{IN}	Input resistance	DC tested		250		Ω
C_{IN}	Input capacitance ¹			0.7		pF
C_{INT}	Input capacitance including Miller multiplied capacitance			4.0		pF
$\Delta R/\Delta V$	Transresistance power supply sensitivity	$V_{CC1} = V_{CC2} = 5 \pm 0.5\text{V}$		3		%/V
$\Delta R/\Delta T$	Transresistance ambient temperature sensitivity	$\Delta T_A = T_{A\text{ MAX}} - T_{A\text{ MIN}}$		0.09		%/°C
I_{IN}	RMS noise current spectral density (referred to input) ²	Test Circuit 2, $f = 10\text{MHz}$		1.17		pA/ $\sqrt{\text{Hz}}$
I_T	Integrated RMS noise current over the bandwidth (referred to input) $C_S = 0.1\text{pF}$	Test circuit 2, $\Delta f = 50\text{MHz}$		7		nA
		$\Delta f = 100\text{MHz}$		12		
		$\Delta f = 150\text{MHz}$		16		
	$C_S = 0.4\text{pF}$	$\Delta f = 50\text{MHz}$		8		
		$\Delta f = 100\text{MHz}$		13		
		$\Delta f = 150\text{MHz}$		18		
PSRR	Power supply rejection ratio (change in V_{OS})	DC Tested, $\Delta V_{CC} = \pm 0.5\text{V}$		-55		dB
PSRR	Power supply rejection ratio ³	$f = 1.0\text{MHz}$, Test Circuit 3		-20		dB
V_{OLMAX}	Maximum differential output AC voltage	$I_i = 0-2\text{mA}$ peak AC		800		mV
$\frac{dR_T}{dt}$	AGC loop time constant parameter ⁴	10 μA to 20 μA steps		1		dB/ms
I_{INMAX}	Maximum input amplitude for output duty cycle of $50 \pm 5\%$	Test circuit 4		+2		mA
t_r, t_f	Output rise and fall times	10 – 90%		2.2		ns
t_D	Group delay	$f = 10\text{MHz}$		2.2		ns

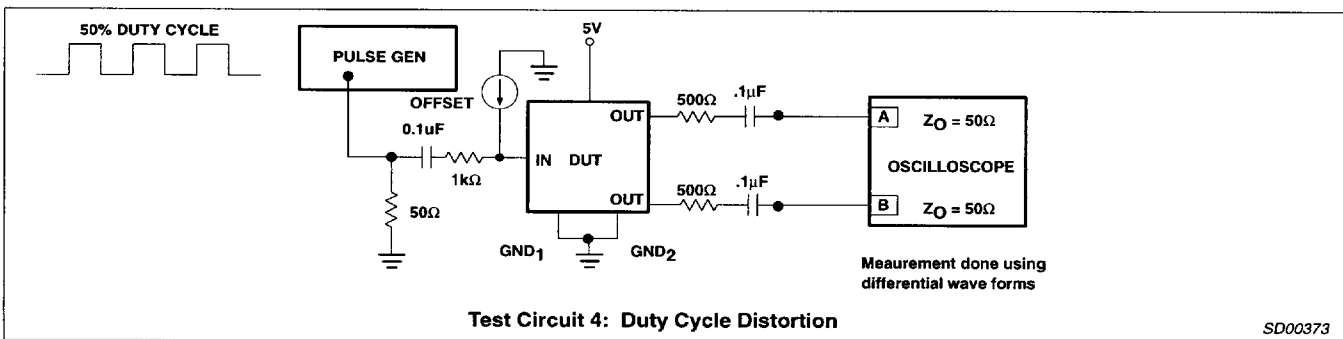
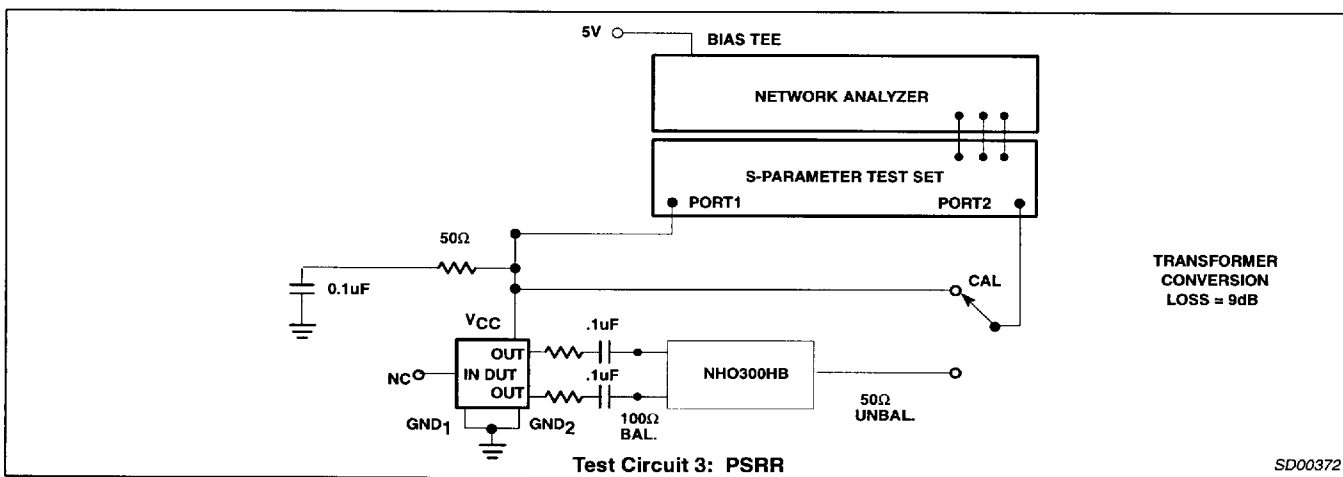
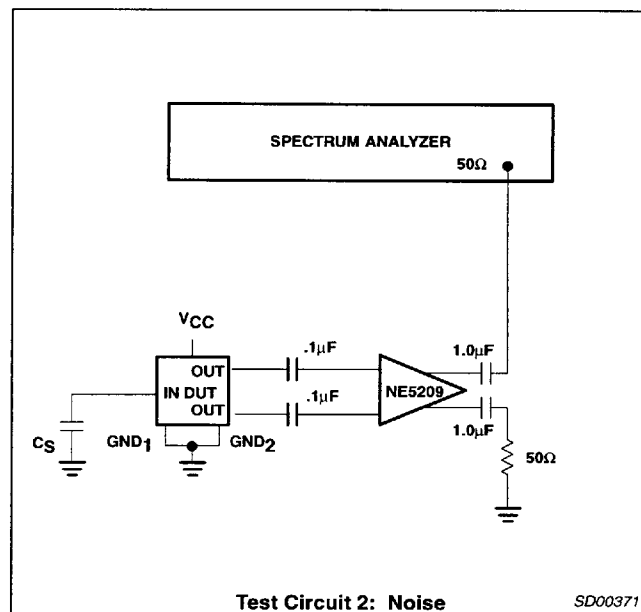
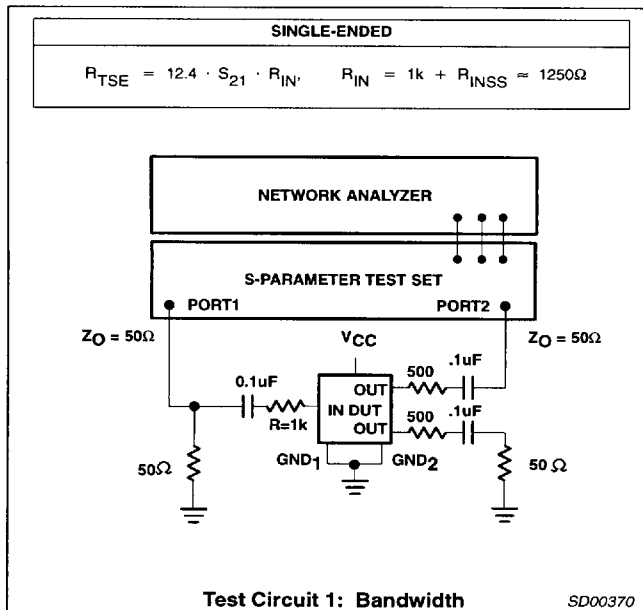
NOTES:

- Does not include Miller-multiplied capacitance of input device.
- Noise performance measured differential. Single-ended output noise is higher due to CM noise.
- PSRR is output referenced and is circuit board layout dependent at higher frequencies. For best performance use a RF filter in V_{CC} line.
- This implies that the SA5223 gain will change 1dB (10%) in the absence of data for 1ms (i.e., can handle bursty data without degrading Bit Error Rate (BER) for 100,000 cycles at 100MHz).

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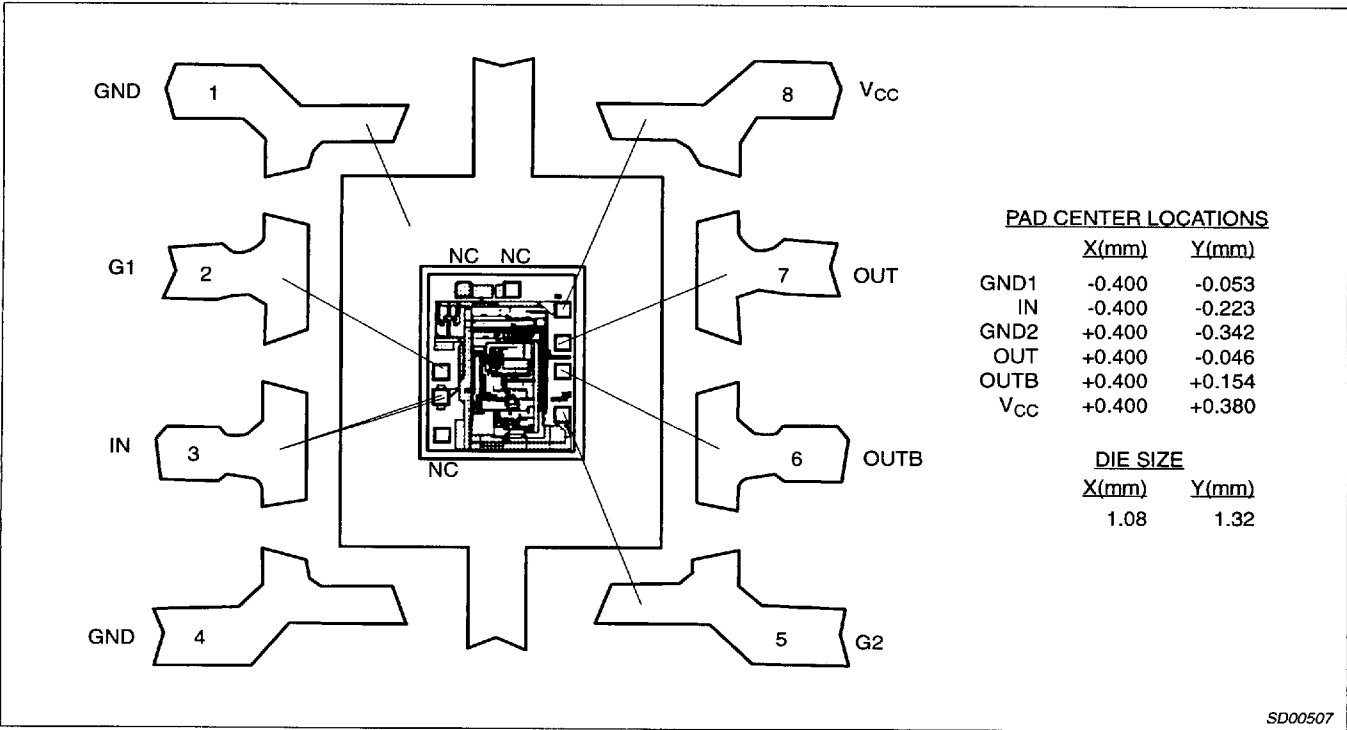
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TEST CIRCUITS



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SD00507

Figure 1. SA5223 Bonding Diagram

Die Sales Disclaimer

Due to the limitations in testing high frequency and other parameters at the die level, and the fact that die electrical characteristics may shift after packaging, die electrical parameters are not specified and die are not guaranteed to meet electrical characteristics (including temperature range) as noted in this data sheet which is intended only to specify electrical characteristics for a packaged device.

All die are 100% functional with various parametrics tested at the wafer level, at room temperature only (25°C), and are guaranteed to be 100% functional as a result of electrical testing to the point of wafer sawing only. Although the most modern processes are utilized for wafer sawing and die pick and place into waffle pack

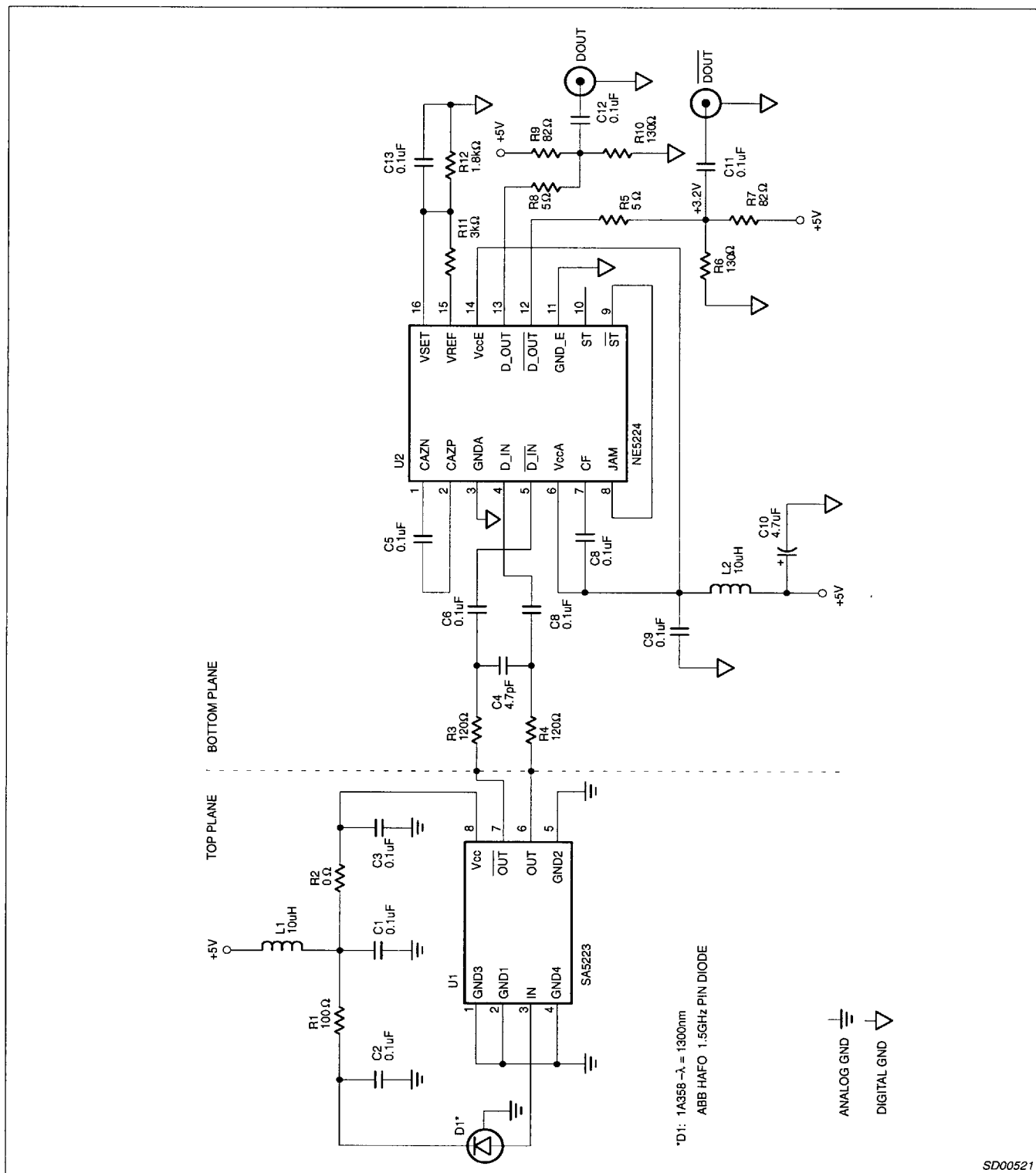
carriers, it is impossible to guarantee 100% functionality through this process. There is no post waffle pack testing performed on individual die.

Since Philips Semiconductors has no control of third party procedures in the handling or packaging of die, Philips Semiconductors assumes no liability for device functionality or performance of the die or systems on any die sales.

Although Philips Semiconductors typically realizes a yield of 85% after assembling die into their respective packages, with care customers should achieve a similar yield. However, for the reasons stated above, Philips Semiconductors cannot guarantee this or any other yield on any die sales.

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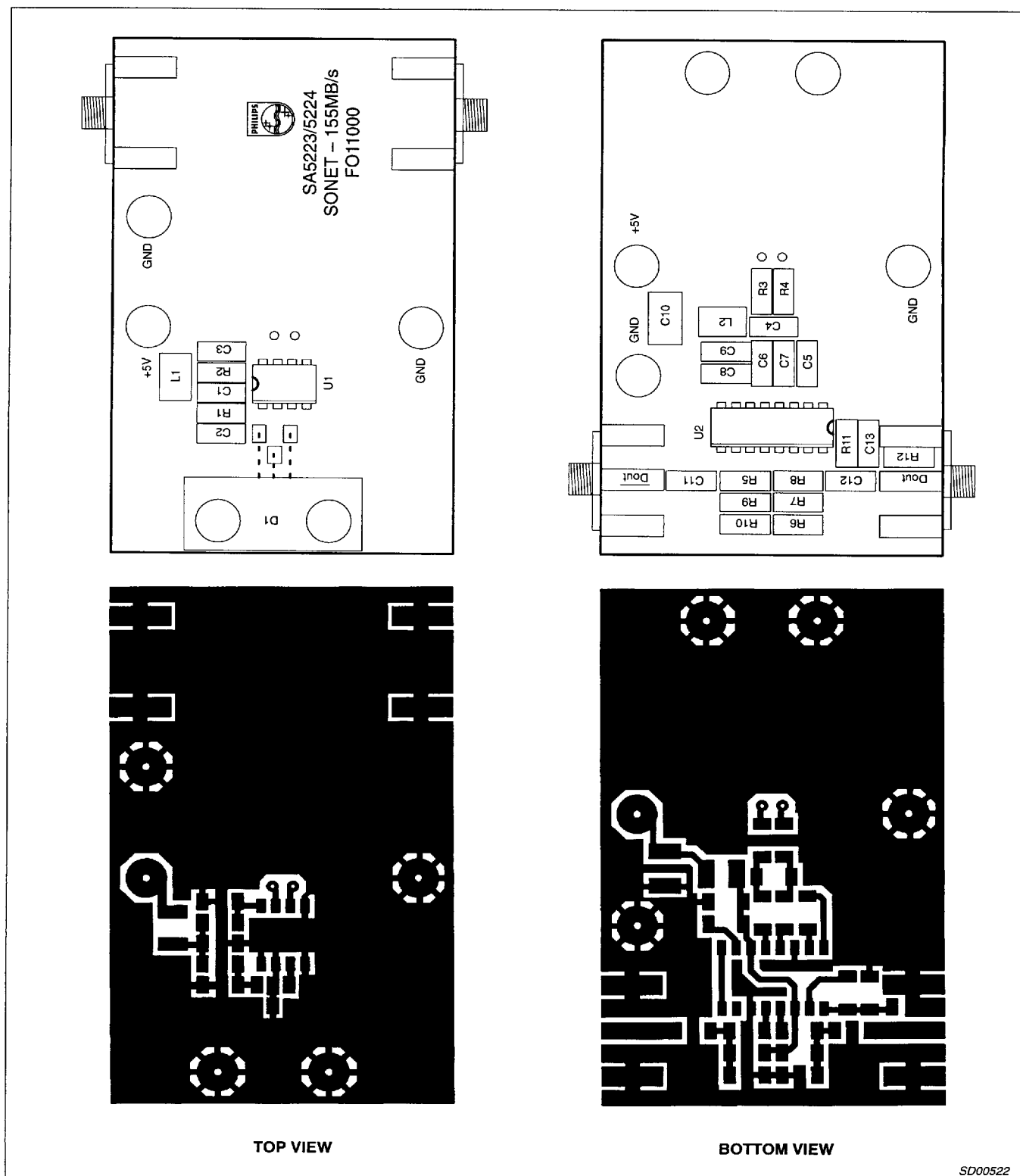
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SD00521

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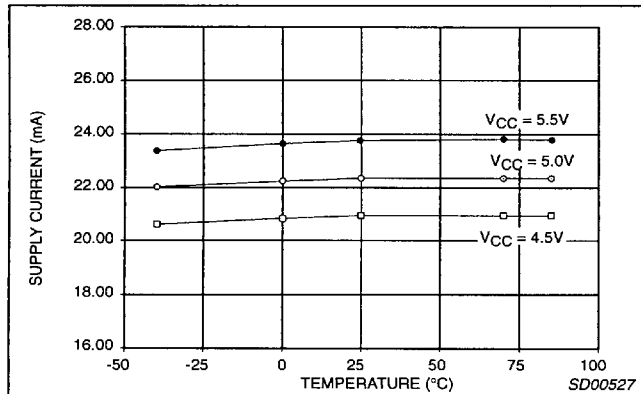
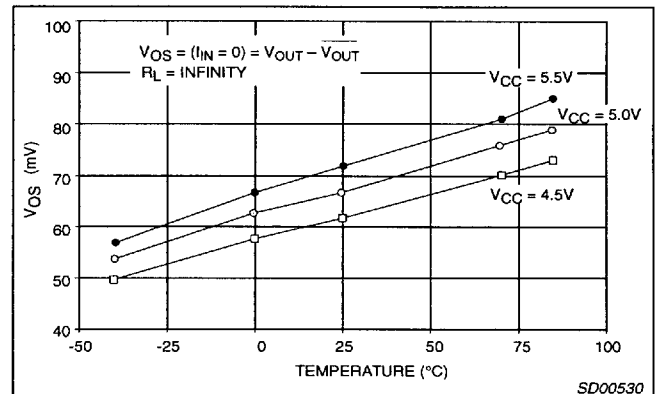
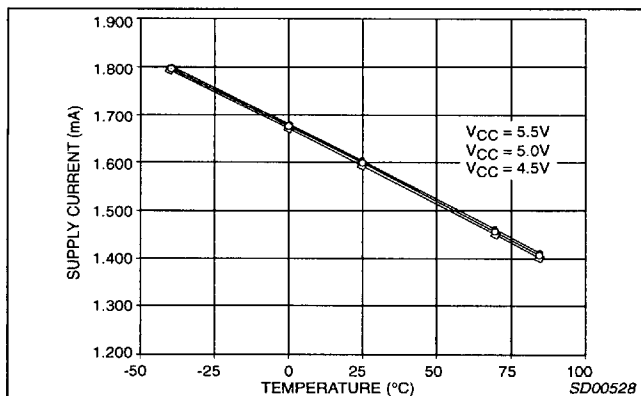
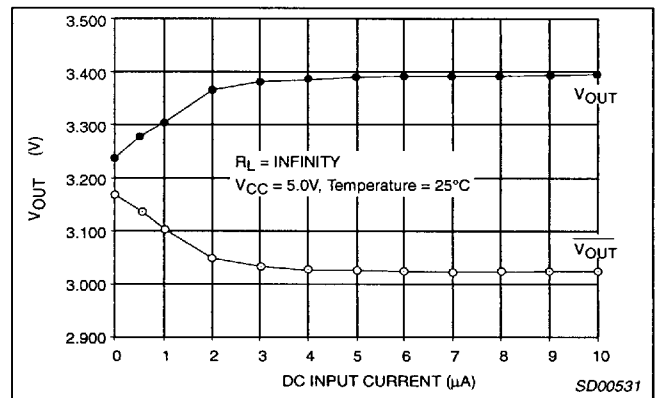
Figure 4. SA5223 I_{CC} vs TemperatureFigure 7. SA5223 Output V_{OS} vs TemperatureFigure 5. SA5223 Input V_{BIAS} vs Temperature

Figure 8. SA5223 Output Voltage vs DC Input Current (for small input current)

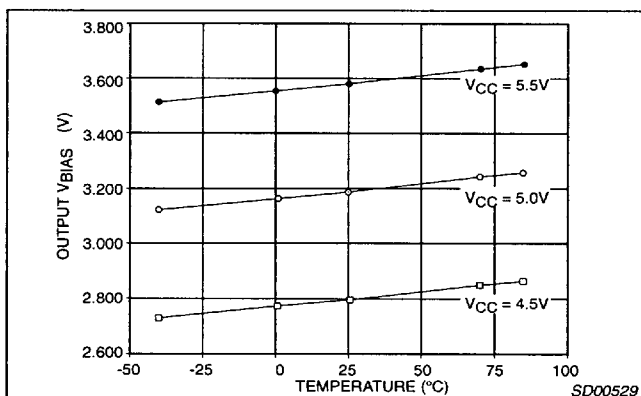
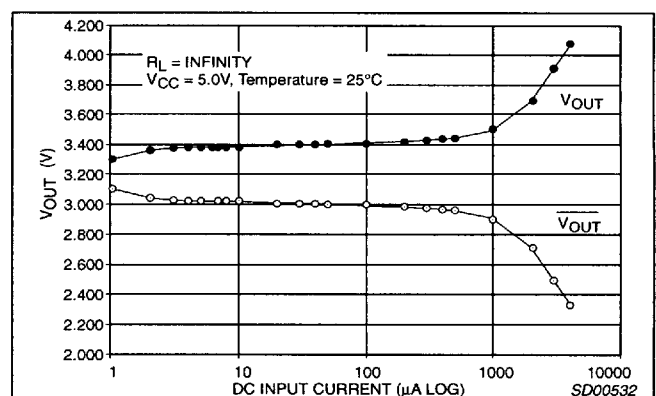
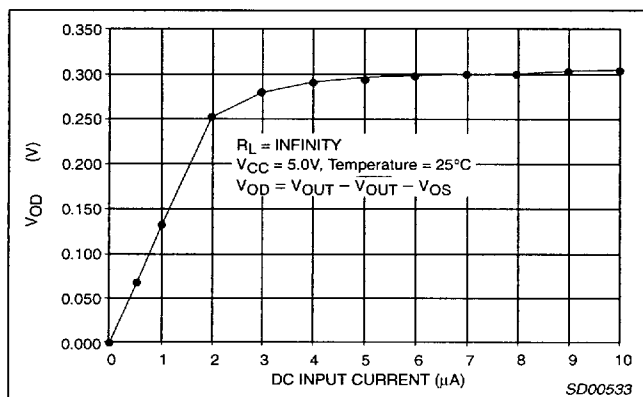
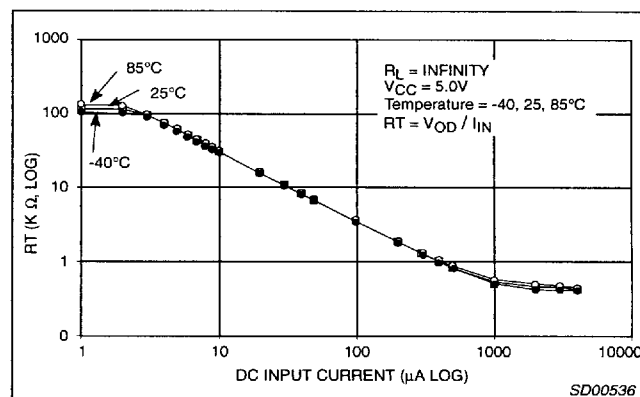
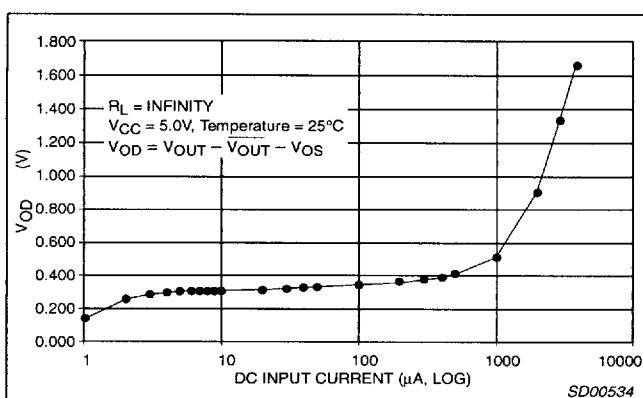
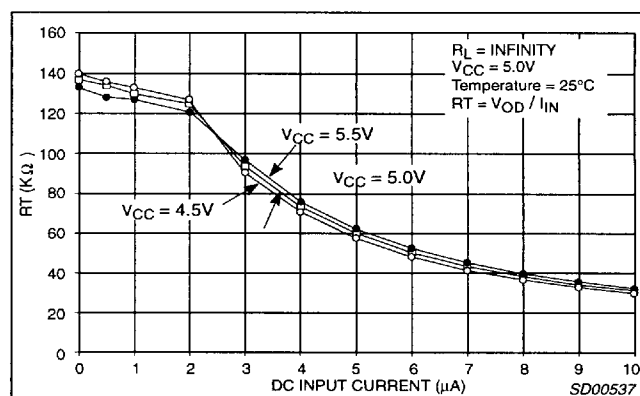
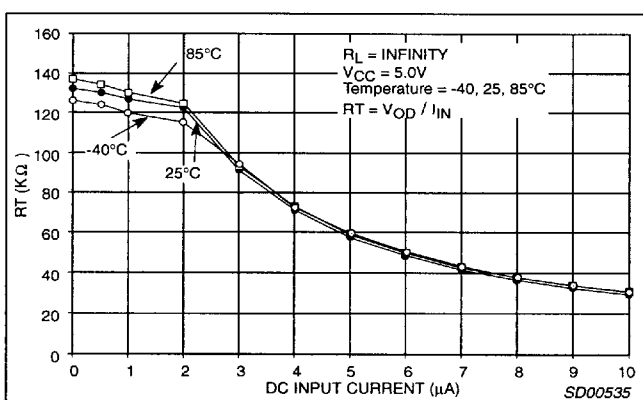
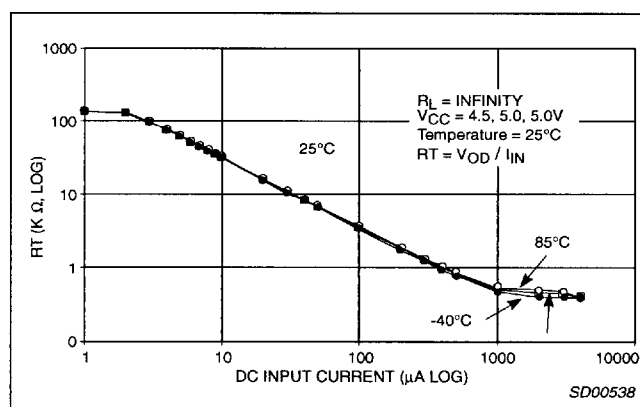
Figure 6. SA5223 Output V_{BIAS} vs Temperature

Figure 9. SA5223 Output Voltage vs DC Input Current (for large input current)

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Figure 10. SA5223 Differential Output vs DC I_{IN} (for small input current)Figure 13. SA5223 Differential RT vs DC I_{IN} (for large input current)Figure 11. SA5223 Differential Output vs DC I_{IN} Figure 14. SA5223 Differential RT vs DC I_{IN} (for small input current)Figure 12. SA5223 Differential RT vs DC I_{IN} (for small input current)Figure 15. SA5223 Differential RT vs DC I_{IN} (for large input current)

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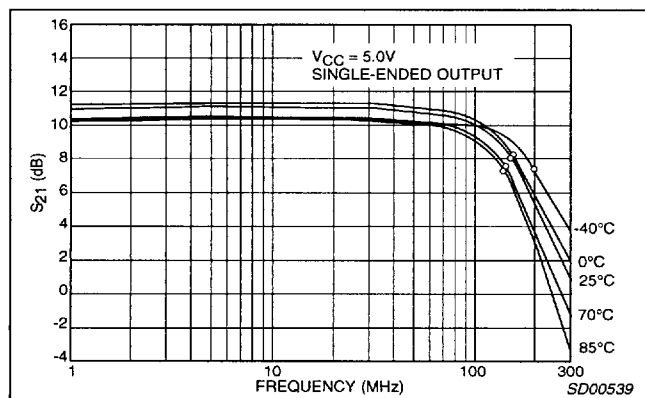


Figure 16. Insertion Gain vs Frequency

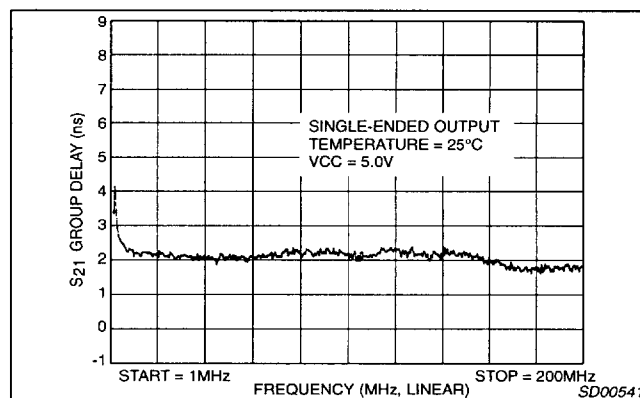


Figure 18. Group Delay vs Frequency

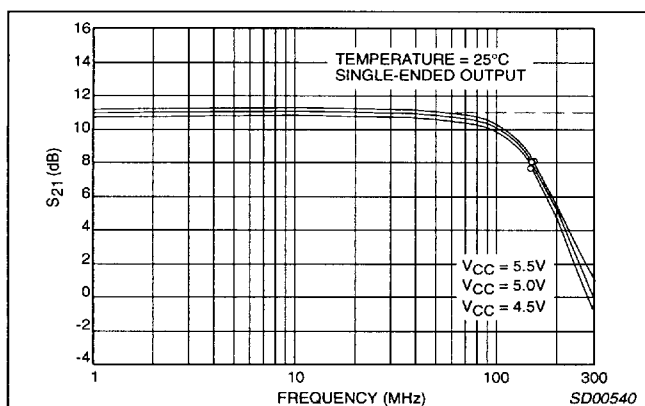


Figure 17. Insertion Gain vs Frequency

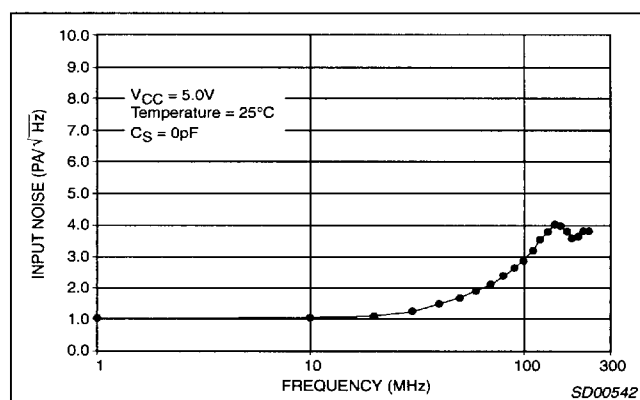


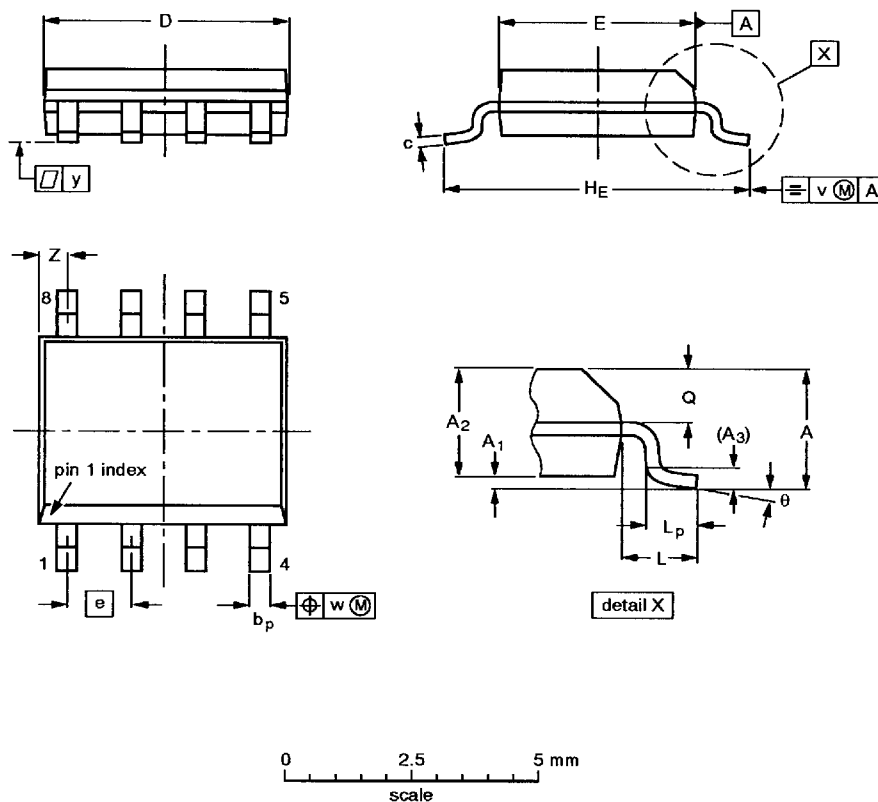
Figure 19. SA5223 Input Current RMS Noise Spectral Density

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SO8: plastic small outline package; 8 leads; body width 3.9mm

SOT96-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A _{max}	A ₁	A ₂	A ₃	b _p	c	D ⁽¹⁾	E ⁽²⁾	e	H _E	L	L _p	Q	v	w	y	z ⁽¹⁾	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	5.0 4.8	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8° 0°
inches	0.069	0.0098 0.0039	0.057 0.049	0.01	0.019 0.014	0.0098 0.0075	0.20 0.19	0.16 0.15	0.050	0.24 0.23	0.041	0.039 0.016	0.028 0.024	0.01	0.01	0.004	0.028 0.012	

Notes

1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
2. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT96-1	076E03S	MS-012AA				92-11-17 95-02-04