

General Description

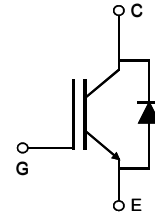
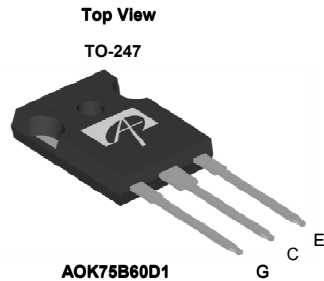
- AlphaIGBT (αIGBT) technology
- Low $V_{CE(SAT)}$ enables high efficiencies
- Smooth Switching waveforms reduce EMI
- Better thermal management
- Minimal gate spike under high dV/dt

Applications

- Welding Machines
- Solar Inverters
- Uninterruptible Power Supplies

Product Summary

V_{CE}	600V
I_C ($T_C=100^\circ\text{C}$)	75A
$V_{CE(sat)}$ ($T_J=25^\circ\text{C}$)	1.72V



Orderable Part Number		Package Type	Form	Minimum Order Quantity
AOK75B60D1		TO247	Tube	240
Absolute Maximum Ratings $T_A=25^\circ\text{C}$ unless otherwise noted				
Parameter	Symbol	AOK75B60D1	Units	
Collector-Emitter Voltage	V_{CE}	600	V	
Gate-Emitter Voltage	V_{GE}	± 20	V	
Continuous Collector Current	$T_C=25^\circ\text{C}$	150	A	
	$T_C=100^\circ\text{C}$	75		
Pulsed Collector Current, Limited by T_{Jmax}	I_{CM}	290	A	
Turn off SOA, $V_{CE} \leq 600\text{V}$, Limited by T_{Jmax}	I_{LM}	290	A	
Continuous Diode Forward Current	$T_C=25^\circ\text{C}$	75	A	
	$T_C=100^\circ\text{C}$	37.5		
Diode Pulsed Current, Limited by T_{Jmax}	I_{FM}	290	A	
Short circuit withstanding time $V_{GE} = 15\text{V}$, $V_{CE} \leq 400\text{V}$, Delay between short circuits $\geq 1.0\text{s}$, $T_C=150^\circ\text{C}$		t_{SC}	10	μs
Power Dissipation	$T_C=25^\circ\text{C}$	P_D	500	W
	$T_C=100^\circ\text{C}$		200	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^\circ\text{C}$
Maximum lead temperature for soldering purpose, 1/8" from case for 5 seconds		T_L	300	$^\circ\text{C}$
Thermal Characteristics				
Parameter	Symbol	AOK75B60D1	Units	
Maximum Junction-to-Ambient	$R_{\theta JA}$	40	$^\circ\text{C/W}$	
Maximum IGBT Junction-to-Case	$R_{\theta JC}$	0.25	$^\circ\text{C/W}$	
Maximum Diode Junction-to-Case	$R_{\theta JC}$	0.95	$^\circ\text{C/W}$	

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units	
STATIC PARAMETERS							
BV_{CES}	Collector-Emitter Breakdown Voltage	$I_C=1\text{mA}$, $V_{GE}=0\text{V}$, $T_J=25^\circ\text{C}$	600	-	-	V	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$V_{GE}=15\text{V}$, $I_C=75\text{A}$	$T_J=25^\circ\text{C}$	-	1.72	2.1	V
			$T_J=125^\circ\text{C}$	-	2	-	
			$T_J=150^\circ\text{C}$	-	2.1	-	
V_F	Diode Forward Voltage	$V_{GE}=0\text{V}$, $I_C=37.5\text{A}$	$T_J=25^\circ\text{C}$	-	1.44	2	V
			$T_J=125^\circ\text{C}$	-	1.43	-	
			$T_J=150^\circ\text{C}$	-	1.41	-	
$V_{GE(th)}$	Gate-Emitter Threshold Voltage	$V_{CE}=5\text{V}$, $I_C=1\text{mA}$	-	5.35	-	V	
I_{CES}	Zero Gate Voltage Collector Current	$V_{CE}=600\text{V}$, $V_{GE}=0\text{V}$	$T_J=25^\circ\text{C}$	-	-	10	μA
			$T_J=125^\circ\text{C}$	-	-	1250	
			$T_J=150^\circ\text{C}$	-	-	6250	
I_{GES}	Gate-Emitter leakage current	$V_{CE}=0\text{V}$, $V_{GE}=\pm 20\text{V}$	-	-	± 100	nA	
g_{FS}	Forward Transconductance	$V_{CE}=20\text{V}$, $I_C=75\text{A}$	-	36	-	S	
DYNAMIC PARAMETERS							
C_{ies}	Input Capacitance	$V_{GE}=0\text{V}$, $V_{CE}=25\text{V}$, $f=1\text{MHz}$	-	4750	-	pF	
C_{oes}	Output Capacitance		-	470	-	pF	
C_{res}	Reverse Transfer Capacitance		-	16	-	pF	
Q_g	Total Gate Charge	$V_{GE}=15\text{V}$, $V_{CE}=480\text{V}$, $I_C=75\text{A}$	-	118	-	nC	
Q_{ge}	Gate to Emitter Charge		-	48	-	nC	
Q_{gc}	Gate to Collector Charge		-	36	-	nC	
$I_{C(SC)}$	Short circuit collector current, Max. 1000 short circuits, Delay between short circuits $\geq 1.0\text{s}$	$V_{GE}=15\text{V}$, $V_{CE}=400\text{V}$, $R_G=25\Omega$	-	290	-	A	
R_g	Gate resistance	$V_{GE}=0\text{V}$, $V_{CE}=0\text{V}$, $f=1\text{MHz}$	-	1.5	-	Ω	
SWITCHING PARAMETERS, (Load Inductive, $T_J=25^\circ\text{C}$)							
$t_{D(on)}$	Turn-On DelayTime	$T_J=25^\circ\text{C}$ $V_{GE}=15\text{V}$, $V_{CE}=400\text{V}$, $I_C=75\text{A}$, $R_G=4\Omega$, Parasitic Inductance=150nH	-	33	-	ns	
t_r	Turn-On Rise Time		-	69	-	ns	
$t_{D(off)}$	Turn-Off Delay Time		-	84	-	ns	
t_f	Turn-Off Fall Time		-	18.4	-	ns	
E_{on}	Turn-On Energy		-	3.7	-	mJ	
E_{off}	Turn-Off Energy		-	1.3	-	mJ	
E_{total}	Total Switching Energy		-	5	-	mJ	
t_{rr}	Diode Reverse Recovery Time	$T_J=25^\circ\text{C}$ $I_F=37.5\text{A}$, $dI/dt=200\text{A}/\mu\text{s}$, $V_{CE}=400\text{V}$	-	147	-	ns	
Q_{rr}	Diode Reverse Recovery Charge		-	0.9	-	μC	
I_{rm}	Diode Peak Reverse Recovery Current		-	10	-	A	
SWITCHING PARAMETERS, (Load Inductive, $T_J=150^\circ\text{C}$)							
$t_{D(on)}$	Turn-On DelayTime	$T_J=150^\circ\text{C}$ $V_{GE}=15\text{V}$, $V_{CE}=400\text{V}$, $I_C=75\text{A}$, $R_G=4\Omega$, Parasitic Inductance=150nH	-	30	-	ns	
t_r	Turn-On Rise Time		-	75	-	ns	
$t_{D(off)}$	Turn-Off Delay Time		-	105	-	ns	
t_f	Turn-Off Fall Time		-	16	-	ns	
E_{on}	Turn-On Energy		-	4.2	-	mJ	
E_{off}	Turn-Off Energy		-	1.9	-	mJ	
E_{total}	Total Switching Energy		-	6.1	-	mJ	
t_{rr}	Diode Reverse Recovery Time	$T_J=150^\circ\text{C}$ $I_F=37.5\text{A}$, $dI/dt=200\text{A}/\mu\text{s}$, $V_{CE}=400\text{V}$	-	235	-	ns	
Q_{rr}	Diode Reverse Recovery Charge		-	1.9	-	μC	
I_{rm}	Diode Peak Reverse Recovery Current		-	15	-	A	

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

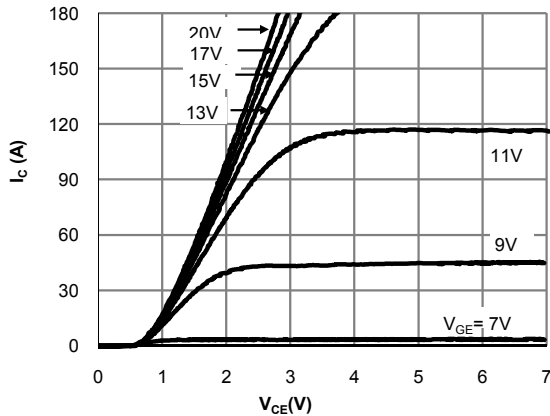


Fig 1: Output Characteristic
($T_J = 25^\circ\text{C}$)

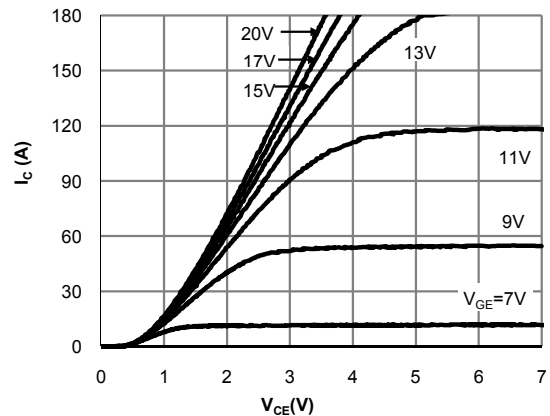


Fig 2: Output Characteristic
($T_J = 150^\circ\text{C}$)

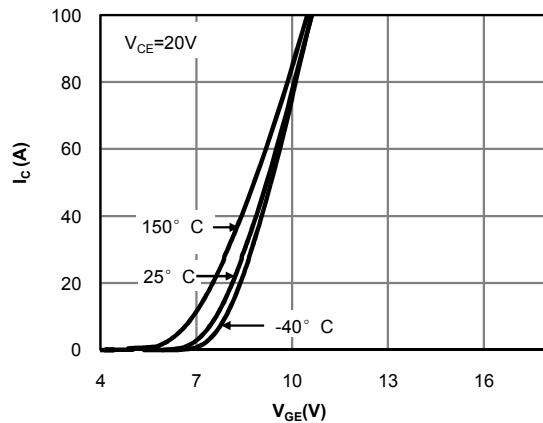


Fig 3: Transfer Characteristic

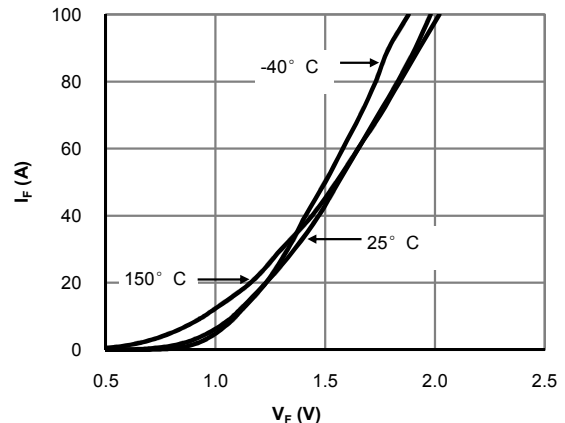


Fig 4: Diode Characteristic

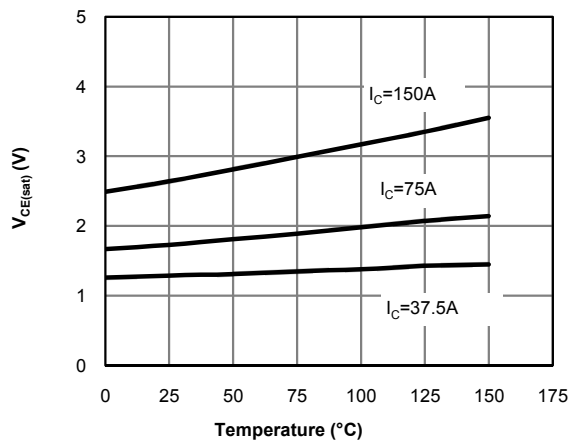


Fig 5: Collector-Emitter Saturation Voltage vs.
Junction Temperature

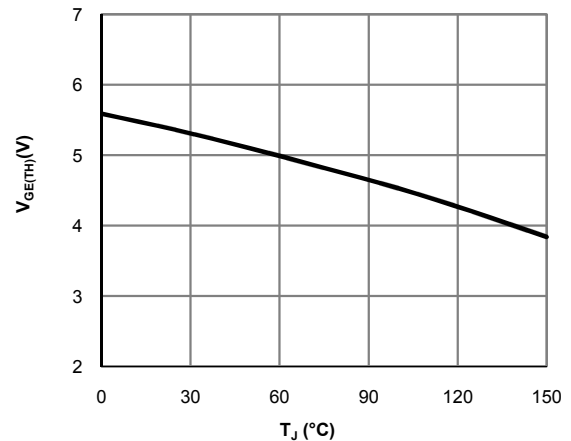
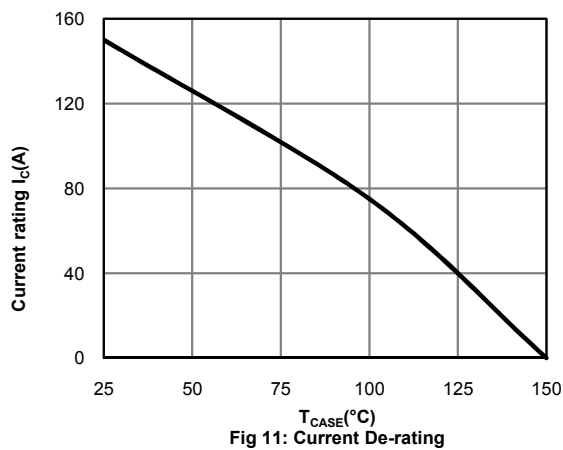
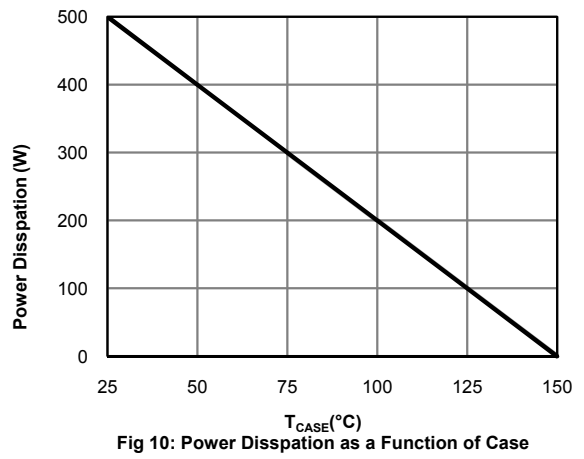
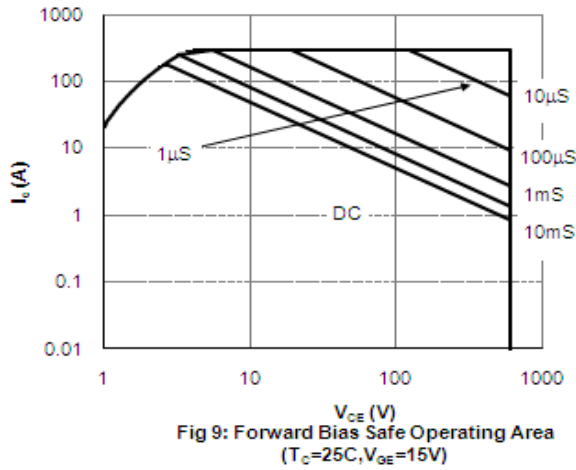
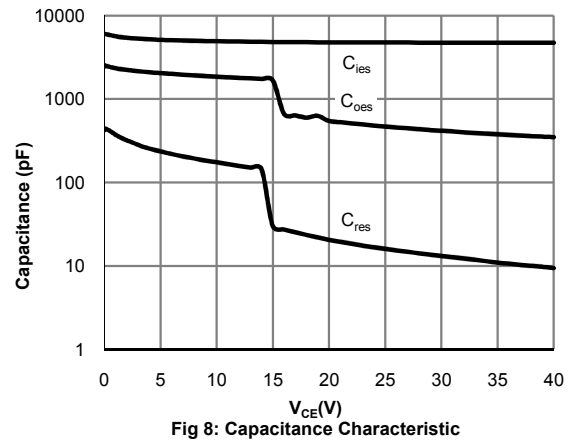
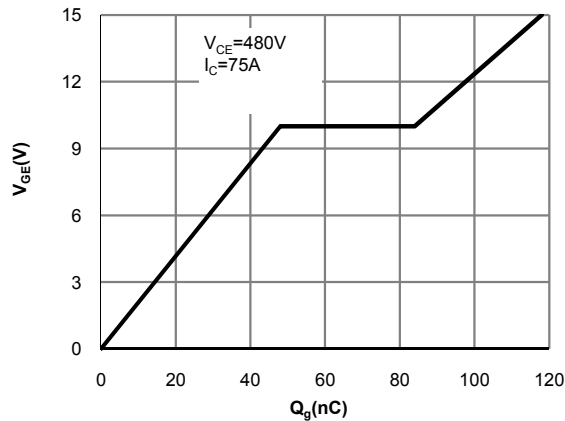
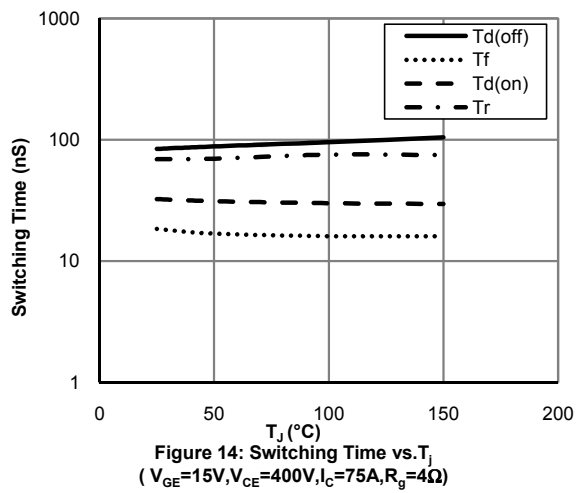
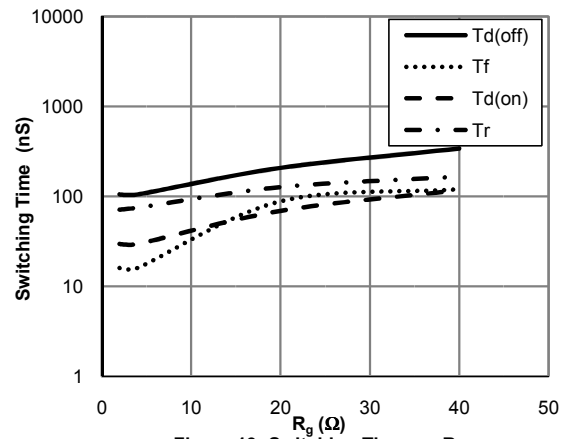
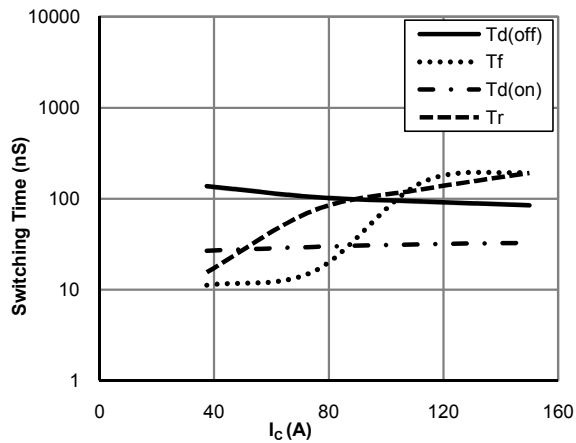


Figure 6: $V_{GE(TH)}$ vs. T_J

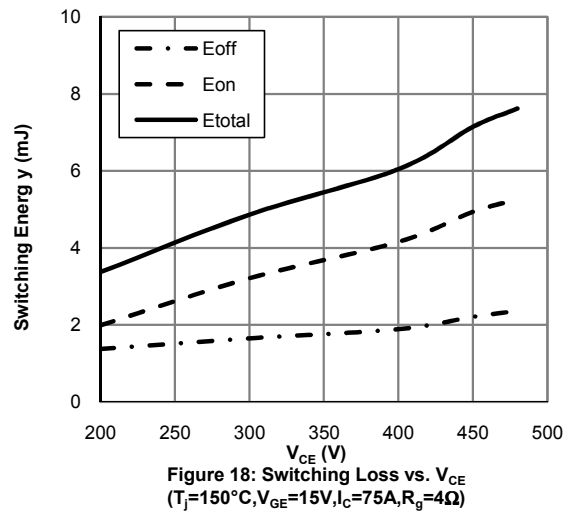
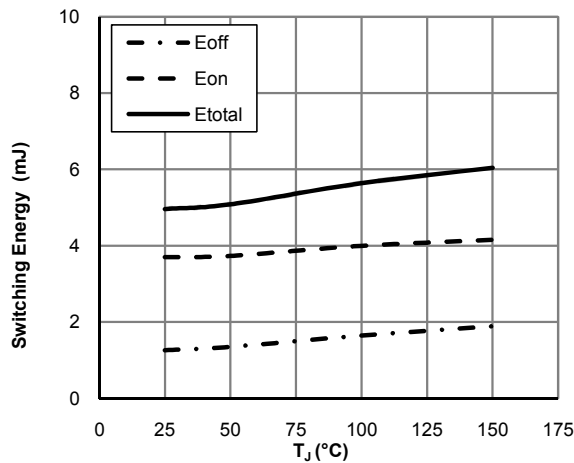
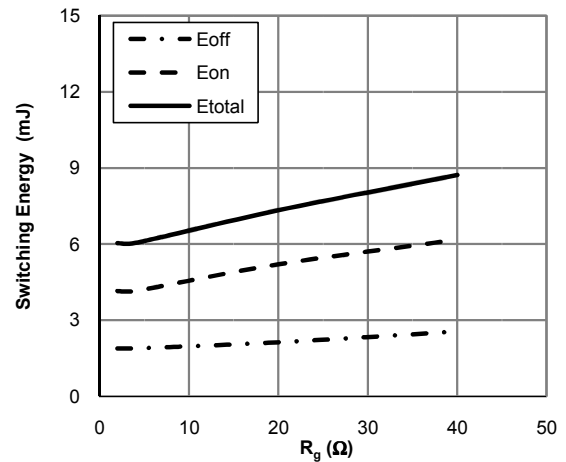
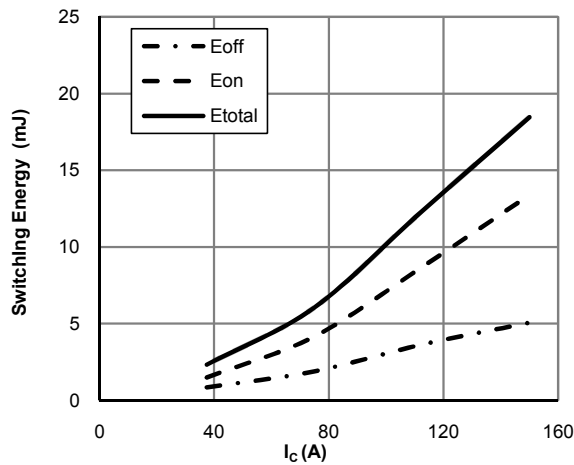
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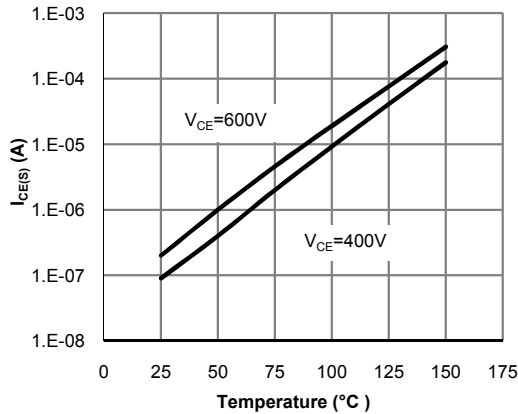


Fig 19: Diode Reverse Leakage Current vs. Junction Temperature

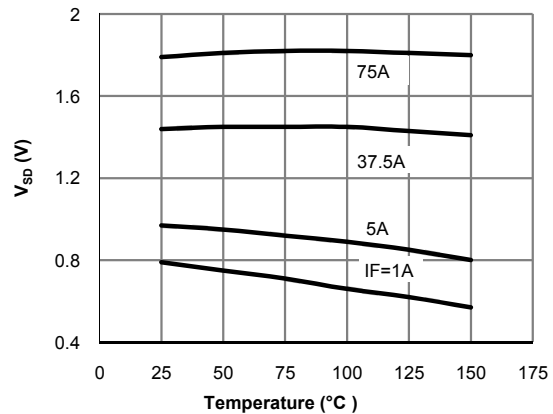


Fig 20: Diode Forward Voltage vs. Junction Temperature

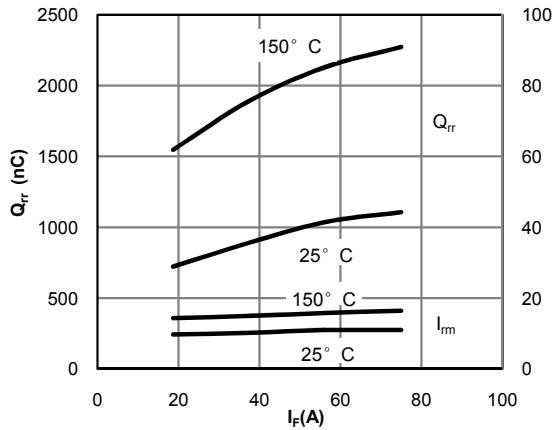


Fig 21: Diode Reverse Recovery Charge and Peak Current vs. Conduction Current
($V_{GE}=15V, V_{CE}=400V, di/dt=200A/\mu s$)

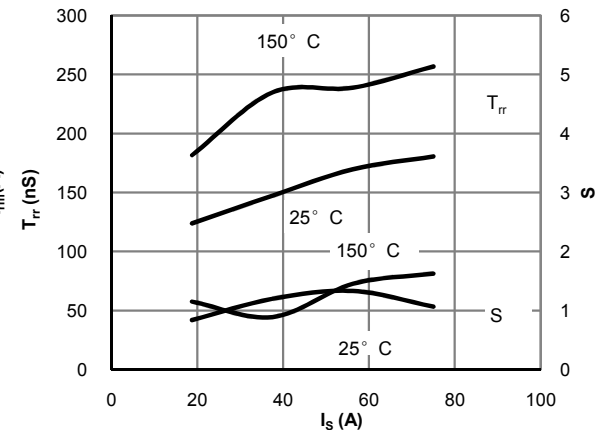


Fig 22: Diode Reverse Recovery Time and Softness Factor vs. Conduction Current
($V_{GE}=15V, V_{CE}=400V, di/dt=200A/\mu s$)

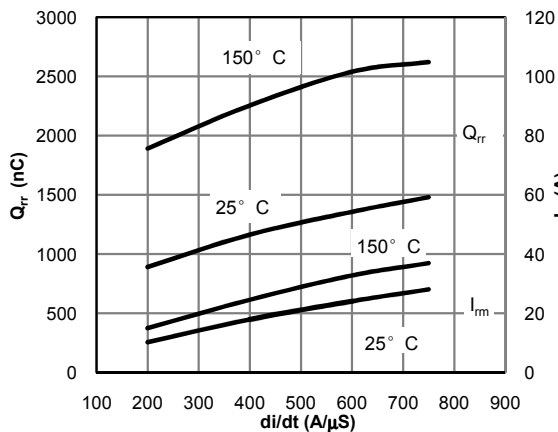


Fig 23: Diode Reverse Recovery Charge and Peak Current vs. di/dt
($V_{GE}=15V, V_{CE}=400V, I_F=37.5A$)

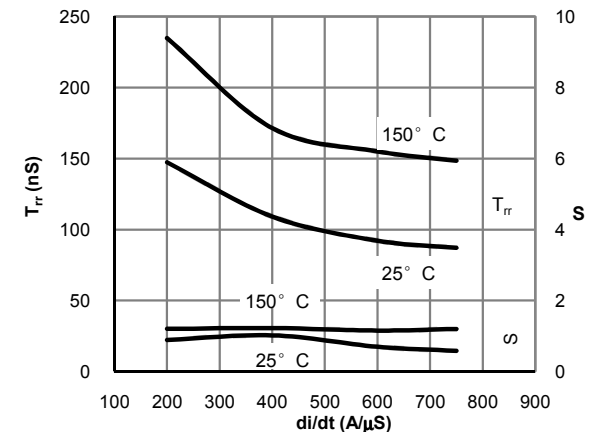


Fig 24: Diode Reverse Recovery Time and Softness Factor vs. di/dt
($V_{GE}=15V, V_{CE}=400V, I_F=37.5A$)

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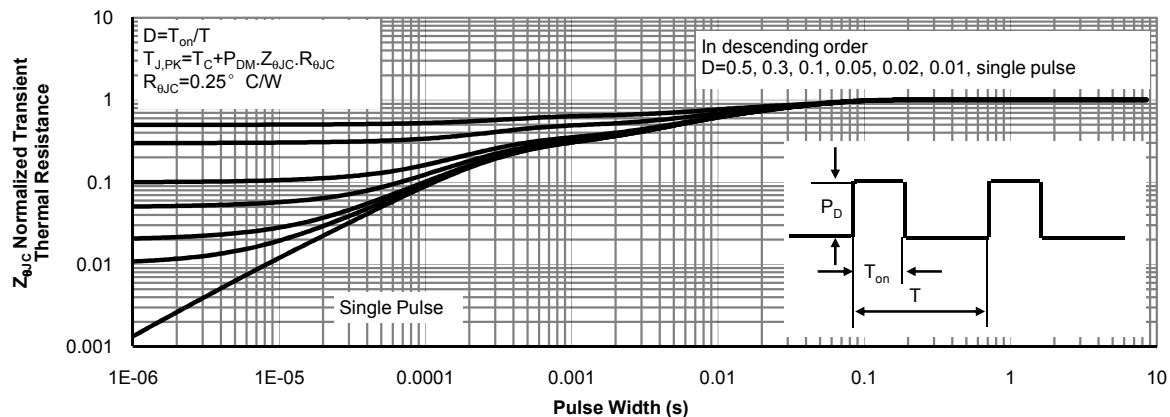


Figure 25: Normalized Maximum Transient Thermal Impedance for IGBT

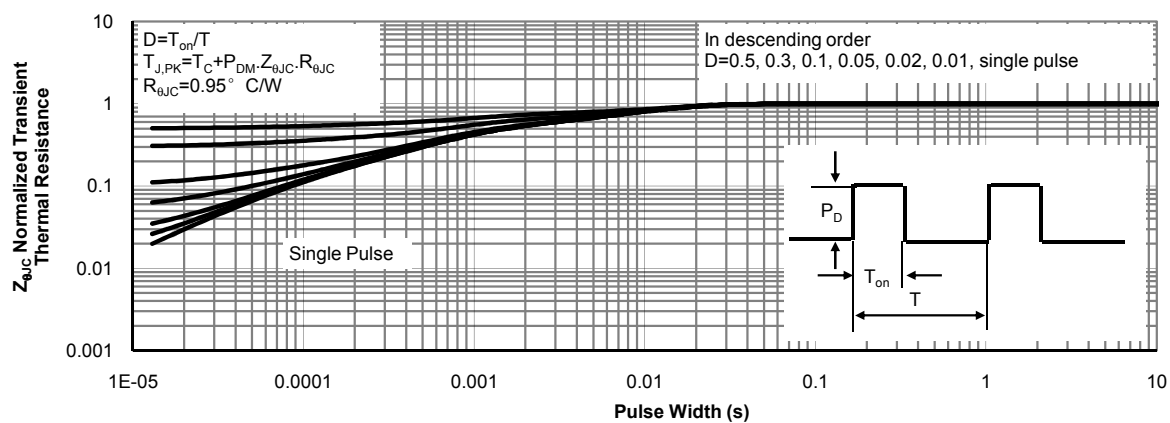
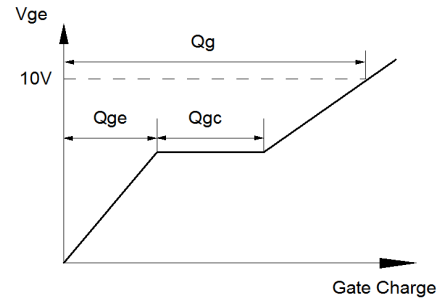
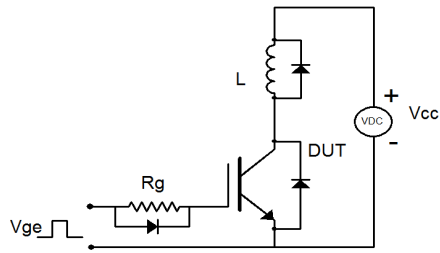
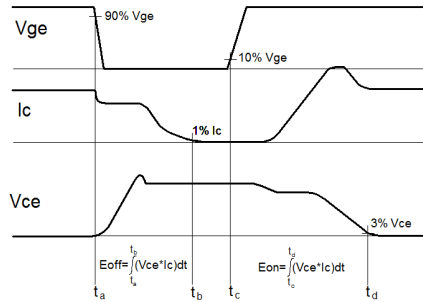
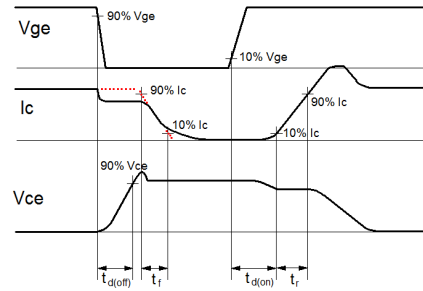
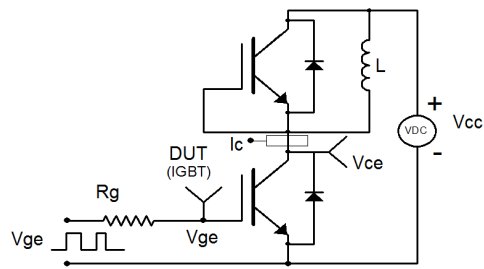


Figure 26: Normalized Maximum Transient Thermal Impedance for Diode

Gate Charge Test Circuit & Waveform



Inductive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

