

Silicon Controlled Rectifiers Reverse Blocking Thyristors

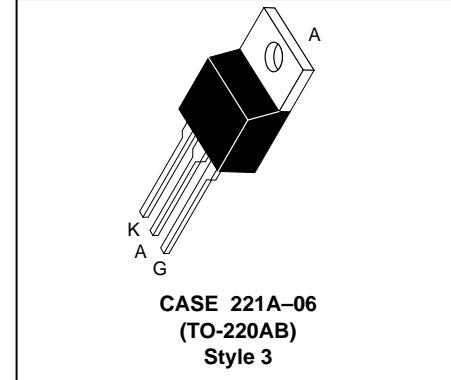
MCR25 SERIES*

*Motorola preferred devices

Designed primarily for half-wave ac control applications, such as motor controls, heating controls, and power supplies; or wherever half-wave, silicon gate-controlled devices are needed.

- Blocking Voltage to 800 Volts
- On-State Current Rating of 25 Amperes RMS
- High Surge Current Capability — 300 Amperes
- Industry Standard TO-220AB Package for Ease of Design
- Glass Passivated Junctions for Reliability and Uniformity

SCRs
25 AMPERES RMS
400 thru 800
VOLTS



MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak Repetitive Off-State Voltage (1)	V_{DRM}		Volts
Peak Repetitive Reverse Voltage ($T_J = -40$ to 125°C)	V_{RRM}		
MCR25D		400	
MCR25M		600	
MCR25N		800	
On-State RMS Current (All Conduction Angles)	$I_{\text{T(RMS)}}$	25	A
Peak Non-repetitive Surge Current (One Half Cycle, 60 Hz, $T_J = 125^\circ\text{C}$)	I_{TSM}	300	A
Circuit Fusing Consideration ($t = 8.3$ ms)	I^2t	373	A^2sec
Peak Gate Power (Pulse Width ≤ 1.0 μs , $T_C = 80^\circ\text{C}$)	P_{GM}	20.0	Watts
Average Gate Power ($t = 8.3$ ms, $T_C = 80^\circ\text{C}$)	$P_{\text{G(AV)}}$	0.5	Watts
Peak Gate Current (Pulse Width ≤ 1.0 μs , $T_C = 80^\circ\text{C}$)	I_{GM}	2.0	A
Operating Junction Temperature Range	T_J	-40 to +125	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-40 to +150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Thermal Resistance — Junction to Case — Junction to Ambient	$R_{\theta\text{JC}}$ $R_{\theta\text{JA}}$	1.5 62.5	$^\circ\text{C/W}$
Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 5 Seconds	T_L	260	$^\circ\text{C}$

(1) V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

Preferred devices are Motorola recommended choices for future use and best overall value.

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MCR25 SERIES

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Peak Forward Blocking Current	I_{DRM}	—	—	0.01	mA
Peak Reverse Blocking Current (V_{AK} = Rated V_{DRM} or V_{RRM} , Gate Open)	I_{RRM}	—	—	2.0	
$T_J = 25^\circ\text{C}$					
$T_J = 125^\circ\text{C}$					

ON CHARACTERISTICS

Peak On-State Voltage* ($I_{TM} = 50\text{ A}$)	V_{TM}	—	—	1.8	Volts
Gate Trigger Current (Continuous dc) ($V_D = 12\text{ V}$, $R_L = 100\text{ }\Omega$)	I_{GT}	4.0	10	30	mA
Gate Trigger Voltage (Continuous dc) ($V_D = 12\text{ V}$, $R_L = 100\text{ }\Omega$)	V_{GT}	0.5	0.65	1.0	Volts
Hold Current (Anode Voltage = 12 V)	I_H	5.0	25	40	mA

DYNAMIC CHARACTERISTICS

Critical Rate of Rise of Off-State Voltage (V_D = Rated V_{DRM} , Exponential Waveform, Gate Open, $T_J = 125^\circ\text{C}$)	dv/dt	50	200	—	$\text{V}/\mu\text{s}$
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*Indicates Pulse Test: Pulse Width $\leq 2.0\text{ ms}$, Duty Cycle $\leq 2\%$.

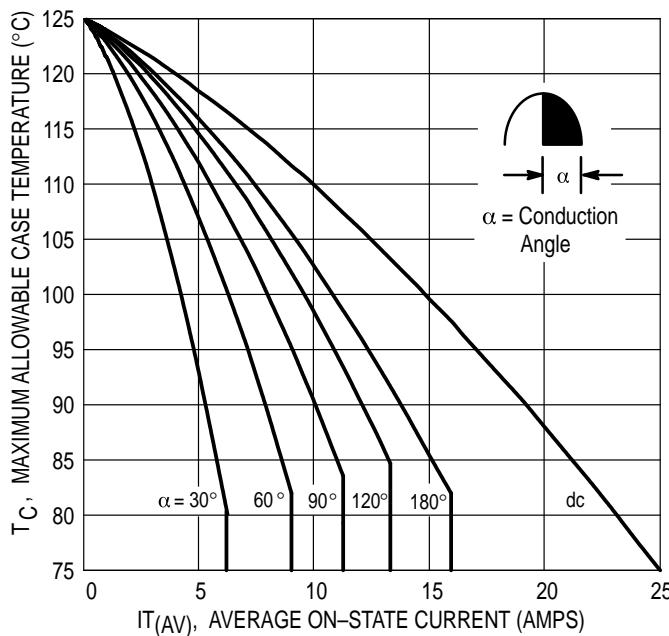


Figure 1. Average Current Derating

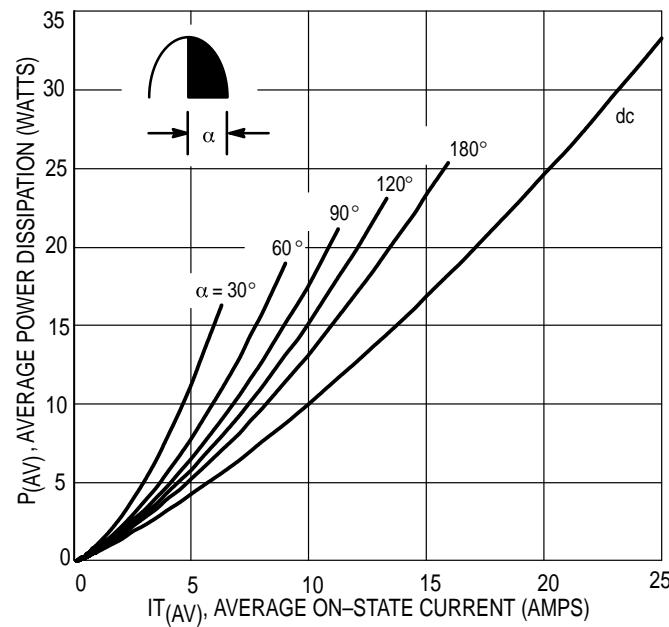
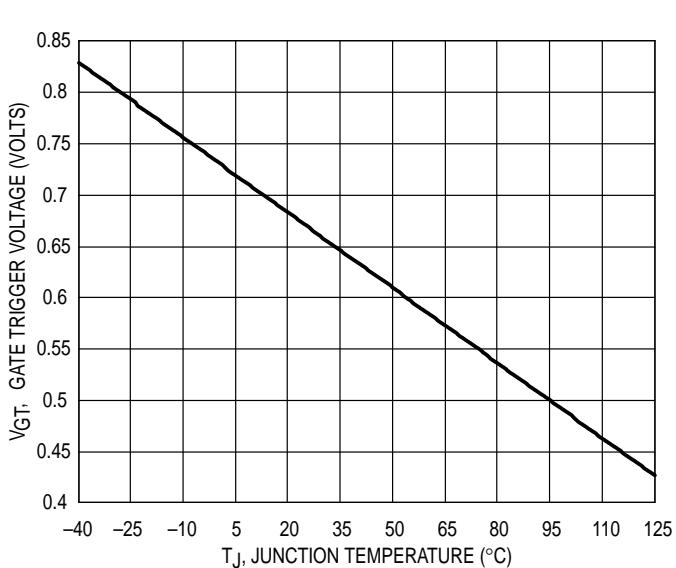
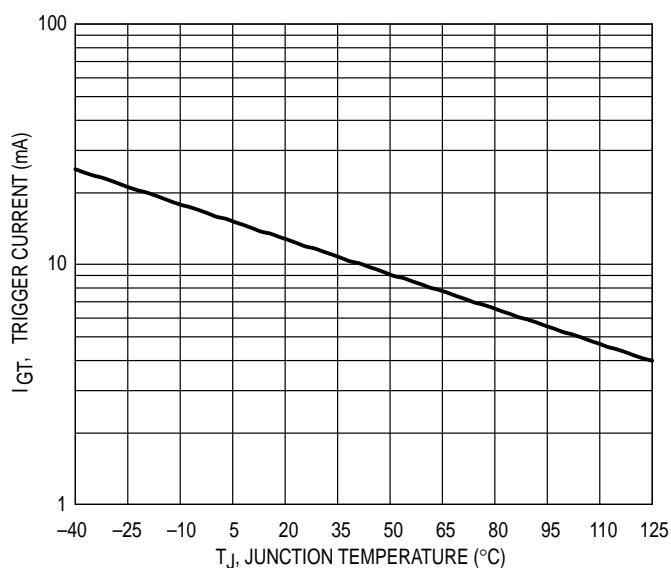
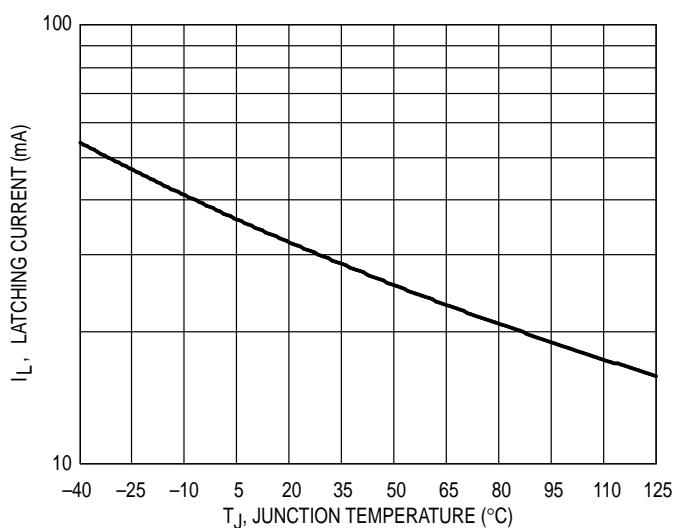
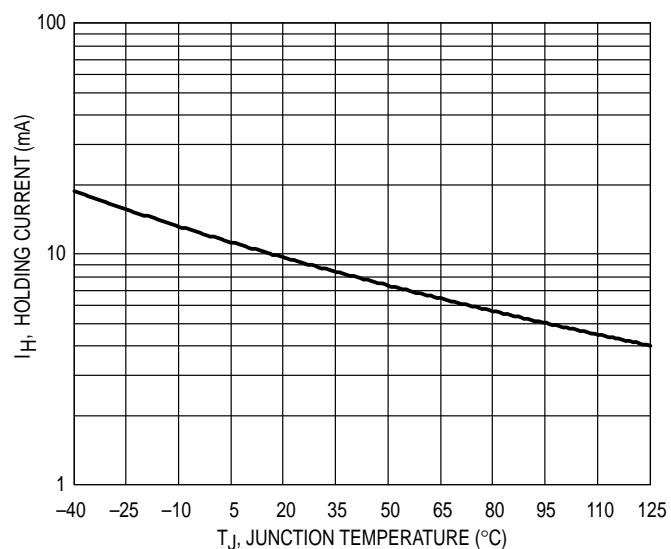
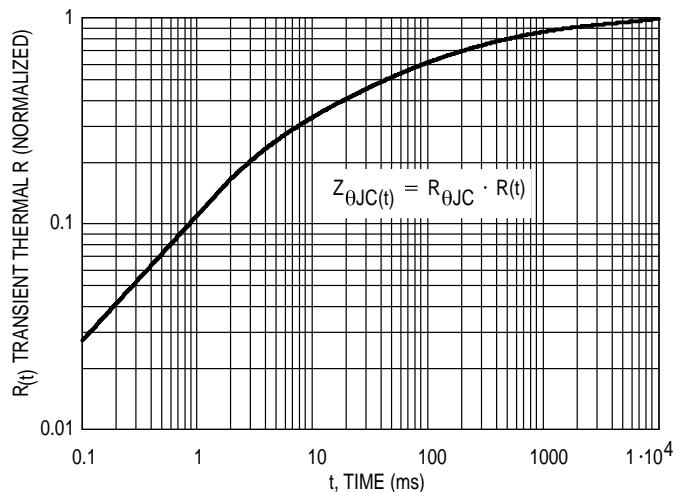
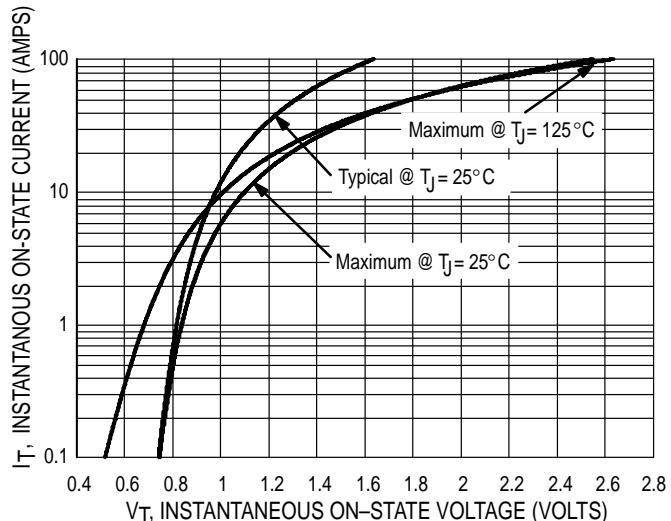


Figure 2. Maximum On-State Power Dissipation



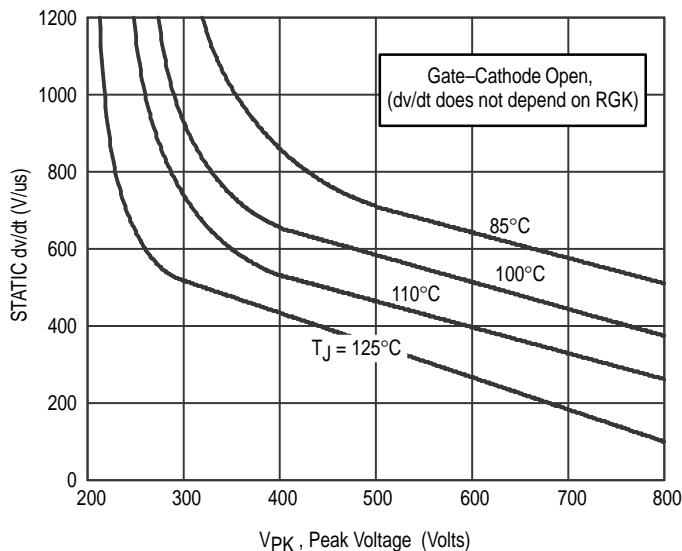


Figure 9. Typical Exponential Static dv/dt Versus Peak Voltage.

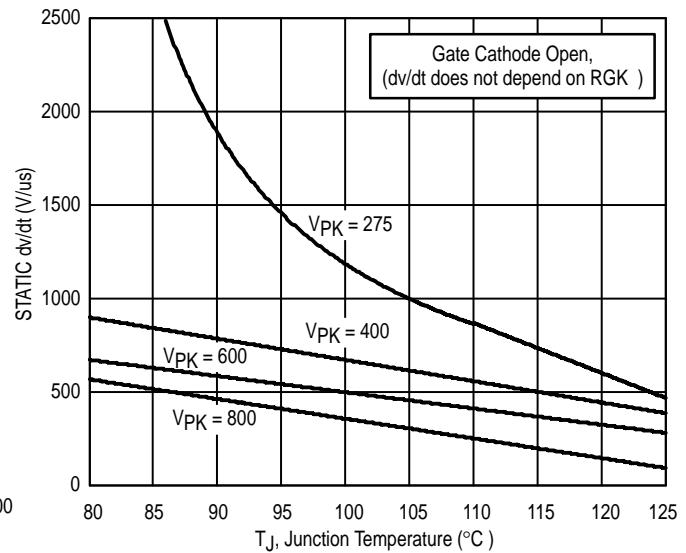


Figure 10. Typical Exponential Static dv/dt Versus Junction Temperature.

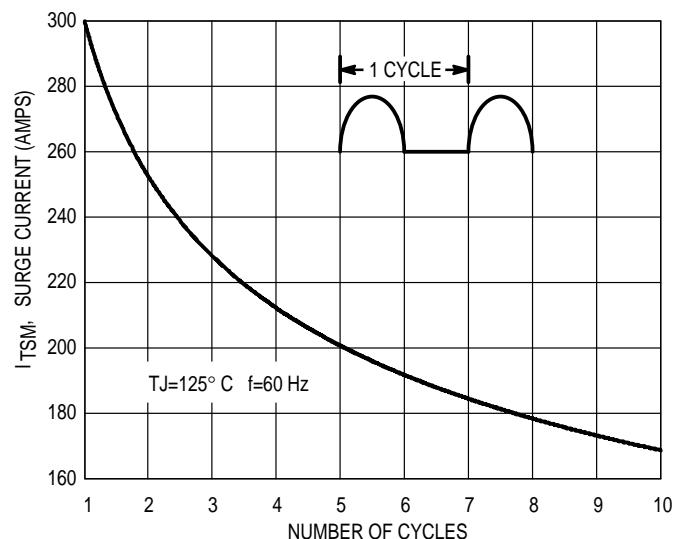
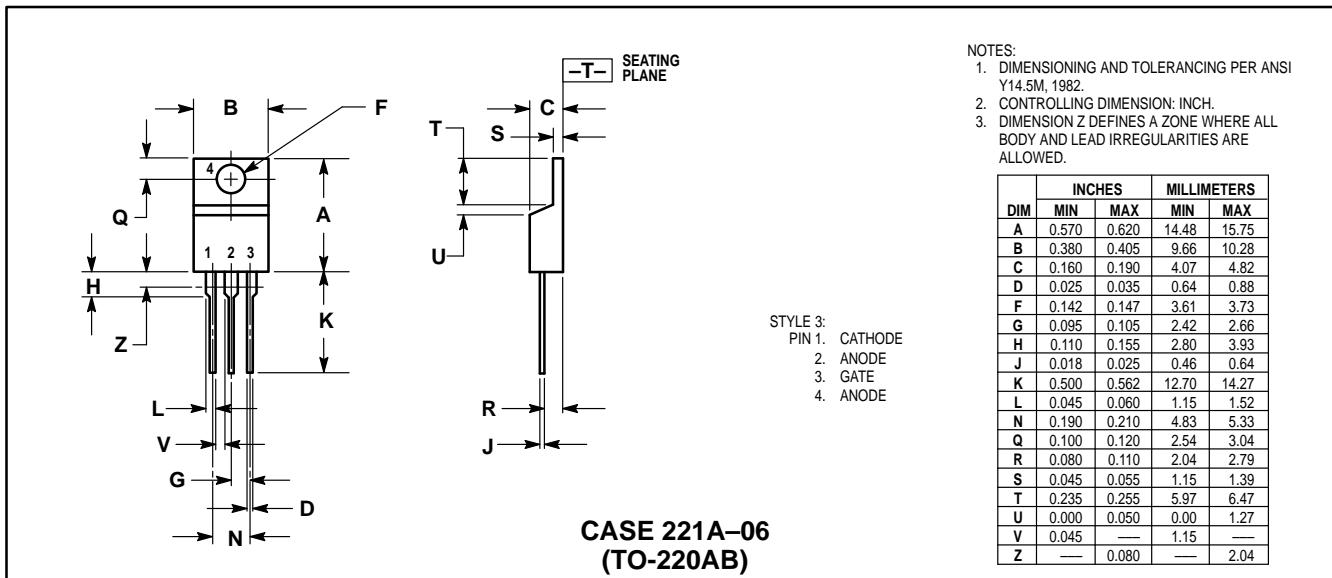


Figure 11. Maximum Non-Repetitive Surge Current

PACKAGE DIMENSIONS



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