



South Sea Semiconductor

SSM6912

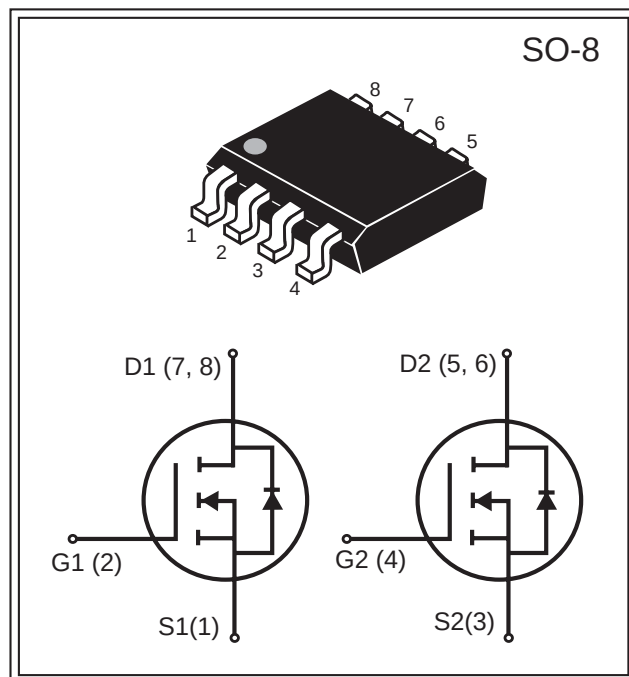
Dual N-Channel Enhancement Mode MOSFET

Product Summary

V _{DS} (V)	I _D (A)	R _{DS(ON)} (mΩ) Max
30V	6A	32 @V _{GS} = 10V
		57 @V _{GS} = 4.5V

FEATURES

- ◆ Super high dense cell design for low R_{DS(ON)}.
- ◆ Rugged and reliable.
- ◆ Surface Mount package.



ABSOLUTE MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous @ T _C = 25°C	I _D	6	A
-Pulsed ^b	I _{DM}	30	A
Drain-Source Diode Forward Current ^a	I _S	1.7	A
Maximum Power Dissipation ^a	P _D	2	W
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55 to 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient ^a	R _{θJA}	62.5	°C/W
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South Sea Semiconductor reserves the right to make changes to improve reliability or manufacturability without advance notice.

South Sea Semiconductor, August 2005 (Rev 2.0)

**P-Channel Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=24V, V_{GS}=0V$			1	μA
Gate-Body Leakage	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	1	1.5	2.5	V
Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=6A$		27	32	m Ω
		$V_{GS}=4.5V, I_D=5A$		51	57	
On-State Drain Current	$I_{D(ON)}$	$V_{DS}=5V, V_{GS}=10V$	16			A
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_D=6A$		5.5		S
Input Capacitance	C_{ISS}	$V_{DS}=15V$ $V_{GS}=0V$ $f=1.0MHz$		575		pF
Output Capacitance	C_{OSS}			110		
Reverse Transfer Capacitance	C_{RSS}			80		
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD}=10V,$ $I_D=1A,$ $V_{GEN}=10V,$ $R_{GEN}=6\Omega,$ $R_L=15\Omega$		8		ns
Rise Time	t_r			11.5		
Turn-Off Delay Time	$t_{D(OFF)}$			15		
Fall Time	t_f			16		
Total Gate Charge	Q_g	$V_{DS}=15V, I_D=2A, V_{GS}=10V$		12		nC
		$V_{DS}=15V, I_D=2A, V_{GS}=4.5V$		6		
Gate-Source Charge	Q_{gs}	$V_{DS}=15V,$ $I_D=2A,$ $V_{GS}=4.5V$		2		
Gate-Drain Charge	Q_{gd}			3		
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_D=1.7A$		0.8	1.2	V

Notes :

- Surface Mounted on FR4 Board, $t \leq 10$ sec.
- Pulse Test : Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
- Guaranteed by design, not subject to production testing.

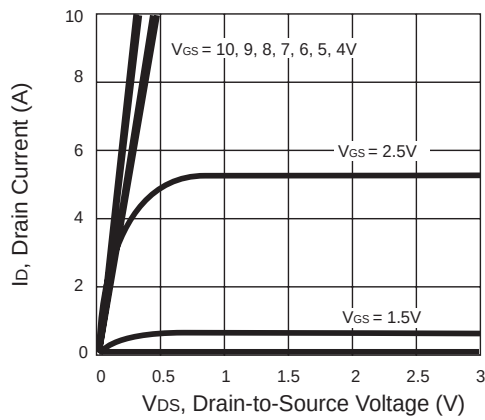


Figure 1. Output Characteristics

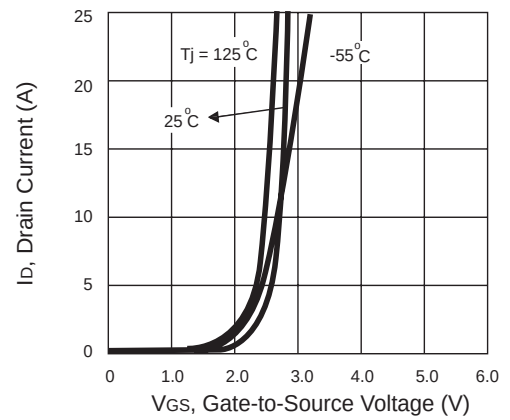


Figure 2. Transfer Characteristics

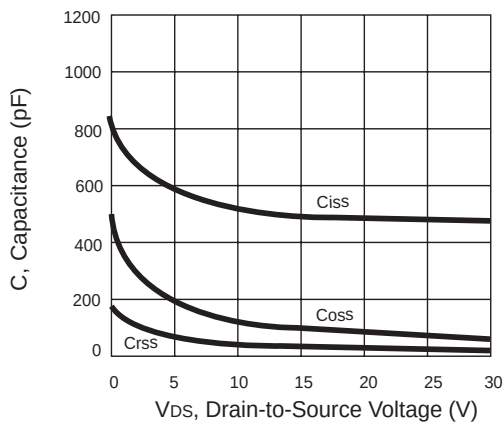


Figure 3. Capacitance

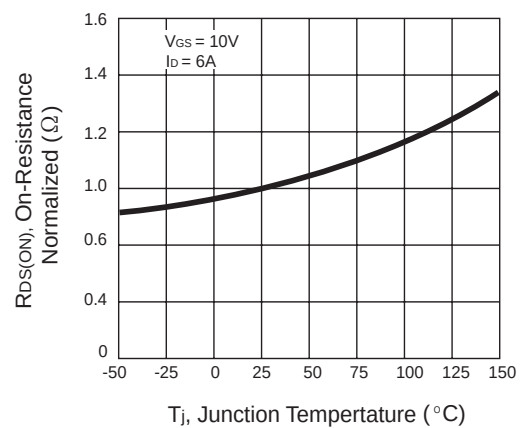


Figure 4. On-Resistance Variation with Temperature

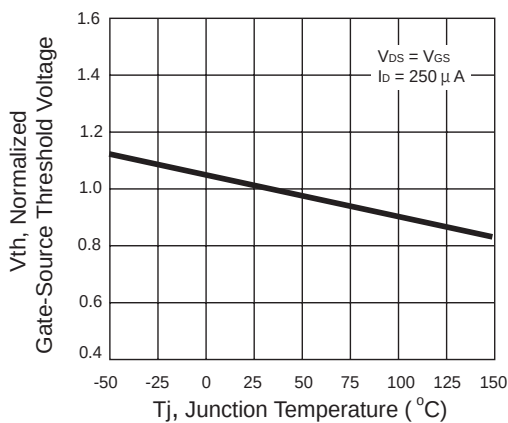


Figure 5. Gate Threshold Variation with Temperature

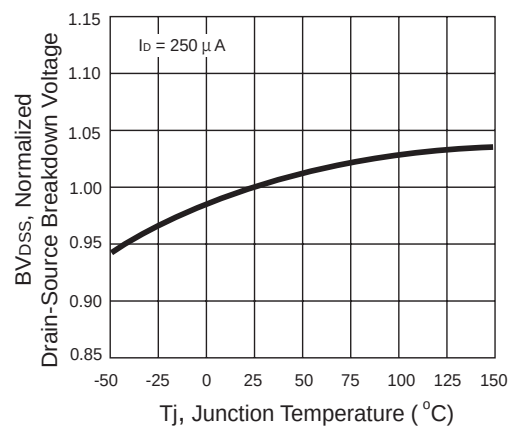


Figure 6. Breakdown Voltage Variation with Temperature



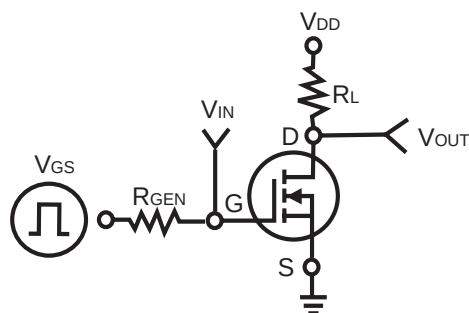


Figure 11. Switching Test Circuit

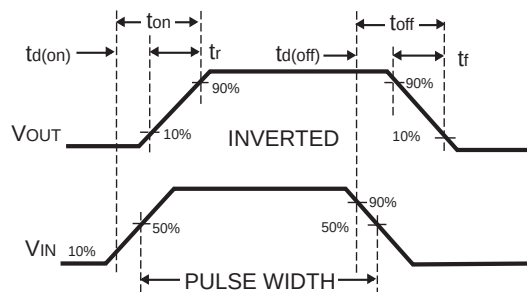


Figure 12. Switching Waveforms

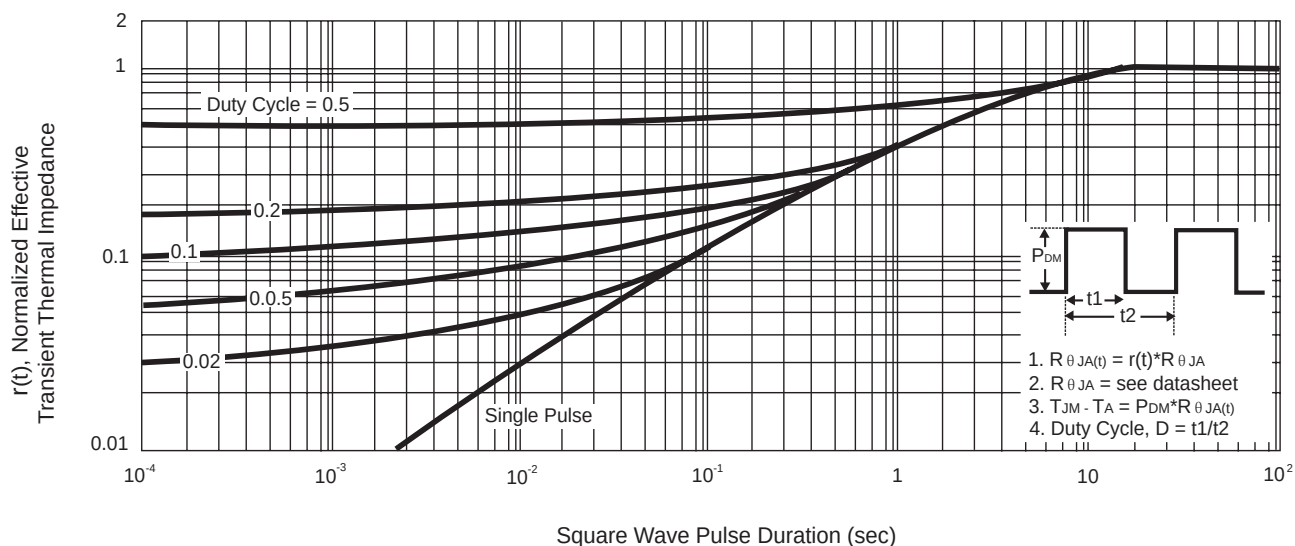
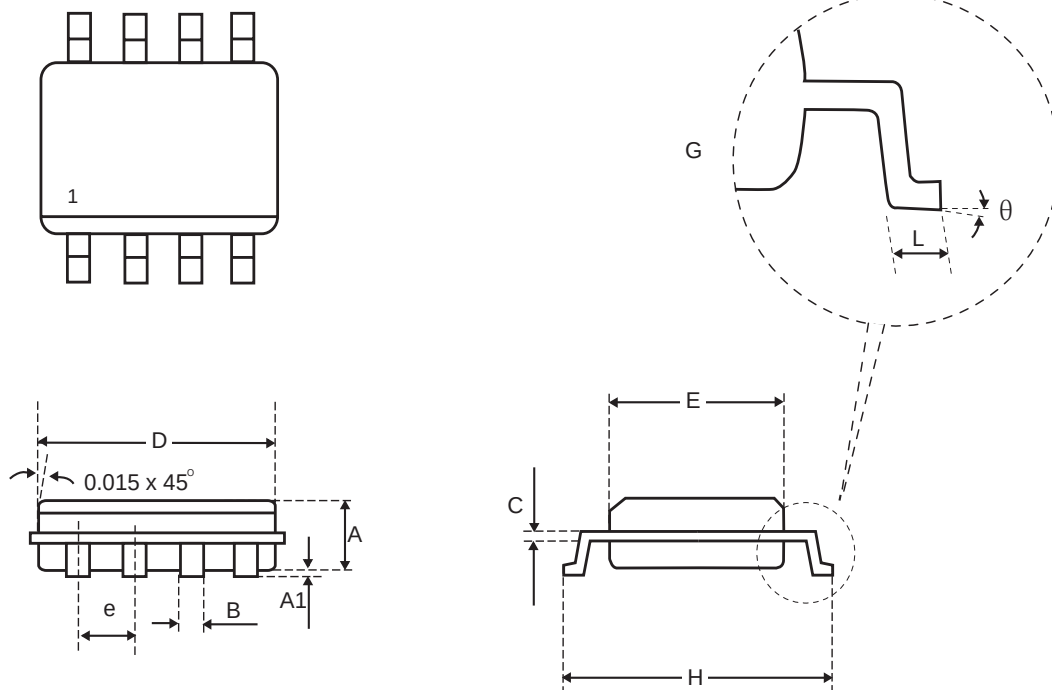


Figure 13. Normalized Thermal Transient Impedance Curve



Package Outline Dimensions

SO-8

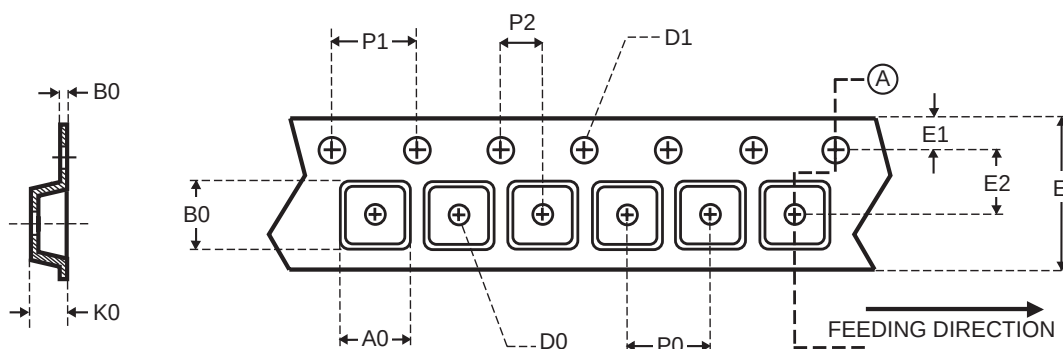


SYMBOLS	MILLIMETERS		INCHES	
	Min.	Max.	Min.	Max.
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
B	0.41 Typ.		0.016 Typ.	
C	0.20 Typ.		0.008 Typ.	
D	4.80	4.98	0.189	0.196
E	3.81	3.99	0.150	0.157
e	1.25 Typ.		0.05 Typ.	
H	5.79	6.20	0.228	0.244
L	0.41	1.27	0.016	0.050
θ	0°	8°	0°	8°



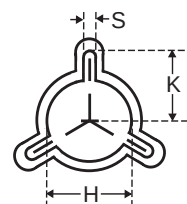
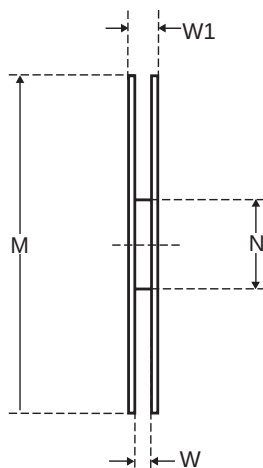
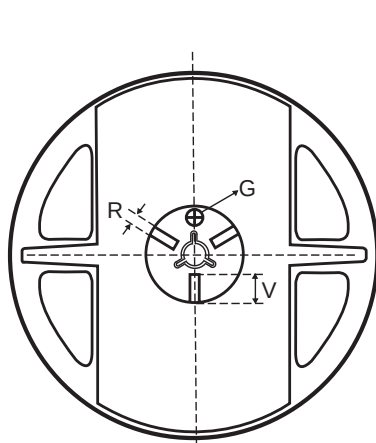
Carrier Tape & Reel Dimensions

SO-8



Package	A0	B0	K0	D0	D1	E	E1	E2	P0	P1	P2	T
SOP 8N 150 mil	6.40	5.20	2.10	$\psi 1.50$ (Min.)	$\psi 1.50$ $+0.10$ -0.10	12.00 ± 0.30	1.75	5.50 ± 0.05	8.00	4.00	2.00 ± 0.05	0.30 ± 0.05

UNIT : mm



Tape size	Reel Size	M	N	W	W1	H	K	S	G	R	V
12mm	$\psi 330$	330 ± 1	62 ± 1.5	12.4 ± 0.2	16.8 -0.4	$\psi 12.75$ ± 0.15	-	2.0 ± 0.15	-	-	-

UNIT : mm