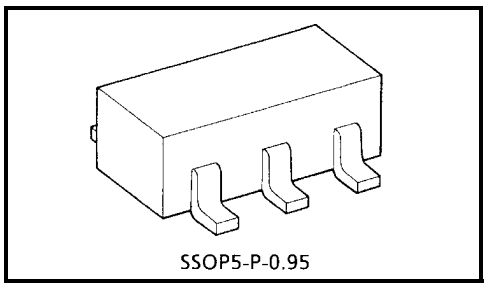
TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TAR5S15, TAR5S18, TAR5S20, TAR5S21, TAR5S22, TAR5S23, TAR5S25, TAR5S27, TAR5S28, TAR5S29, TAR5S30, TAR5S31, TAR5S32, TAR5S33, TAR5S35, TAR5S45, TAR5S48, TAR5S50

Point Regulators (Low-Dropout Regulator)

The TAR5Sxx Series is comprised of general-purpose bipolar single-power-supply devices incorporating a control pin which can be used to turn them ON/OFF.

Overtemperature and overcurrent protection circuits are built into the devices' output circuit.

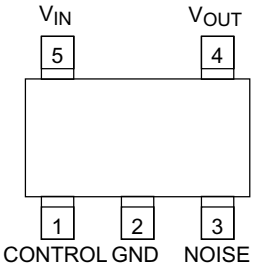


Weight: 0.014 g (typ.)

Features

- Low stand-by current
- Overtemperature/overcurrent protection
- Operation voltage range is wide.
- Maximum output current is high.
- Difference between input voltage and output voltage is low.
- Small package.
- Ceramic capacitors can be used.

Pin Assignments (top view)



Overtemperature protection and overcurrent protection functions are not necessary guarantee of operating ratings below the maximum ratings.

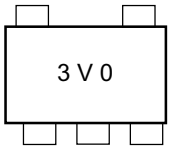
Do not use devices under conditions in which their maximum ratings will be exceeded.

List of Products Number and Marking

Products No.	Marking
TAR5S15	1V5
TAR5S18	1V8
TAR5S20	2V0
TAR5S21	2V1
TAR5S22	2V2
TAR5S23	2V3
TAR5S25	2V5
TAR5S27	2V7
TAR5S28	2V8
TAR5S29	2V9
TAR5S30	3V0
TAR5S31	3V1
TAR5S32	3V2
TAR5S33	3V3
TAR5S35	3V5
TAR5S45	4V5
TAR5S48	4V8
TAR5S50	5V0

Marking on the Product

Example: TAR5S30 (3.0 V output)



Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{IN}	15	V
Output current	I_{OUT}	200	mA
Power dissipation	P_D	200 (Note1)	mW
		380 (Note2)	
Operation temperature range	T_{opr}	−40 to 85	°C
Storage temperature range	T_{stg}	−55 to 150	°C

Note1: Unit Ratintg

Note2: Mounted on a glass epoxy circuit board of 30 × 30 mm. Pad dimension of 50 mm^2

TAR5S15~TAR5S22

Electrical Characteristic (unless otherwise specified, $V_{IN} = V_{OUT} + 1$ V, $I_{OUT} = 50$ mA, $C_{IN} = 1$ μF, $C_{OUT} = 10$ μF, $C_{NOISE} = 0.01$ μF, $T_j = 25°C$)

Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Output voltage	V_{OUT}	Please refer to the Output Voltage Accuracy table.				
Line regulation	Reg·line	$V_{OUT} + 1$ V ≦ V_{IN} ≦ 15 V, $I_{OUT} = 1$ mA	—	3	15	mV
Load regulation	Reg·load	1 mA ≦ I_{OUT} ≦ 150 mA	—	25	75	mV
Quiescent current	I_{B1}	$I_{OUT} = 0$ mA	—	170	—	μA
	I_{B2}	$I_{OUT} = 50$ mA	—	550	850	
Stand-by current	$I_{B\ (OFF)}$	$V_{CT} = 0$ V	—	—	0.1	μA
Output noise voltage	V_{NO}	$V_{IN} = V_{OUT} + 1$ V, $I_{OUT} = 10$ mA, 10 Hz ≦ f ≦ 100 kHz, $C_{NOISE} = 0.01$ μF, Ta = 25°C	—	30	—	μV_{rms}
Temperature coefficient	T_{CVO}	−40°C ≦ T_{opr} ≦ 85°C	—	100	—	ppm/°C
Input voltage	V_{IN}	—	2.4	—	15	V
Ripple rejection	R.R.	$V_{IN} = V_{OUT} + 1$ V, $I_{OUT} = 10$ mA, $C_{NOISE} = 0.01$ μF, f = 1 kHz, $V_{Ripple} = 500\ mV_{p-p}$, Ta = 25°C	—	70	—	dB
Control voltage (ON)	$V_{CT\ (ON)}$	—	1.5	—	V_{IN}	V
Control voltage (OFF)	$V_{CT\ (OFF)}$	—	—	—	0.4	V
Control current (ON)	$I_{CT\ (ON)}$	$V_{CT} = 1.5$ V	—	3	10	μA
Control current (OFF)	$I_{CT\ (OFF)}$	$V_{CT} = 0$ V	—	0	0.1	μA

TAR5S23~TAR5S50

Electrical Characteristic (unless otherwise specified, $V_{IN} = V_{OUT} + 1$ V, $I_{OUT} = 50$ mA, $C_{IN} = 1$ μF, $C_{OUT} = 10$ μF, $C_{NOISE} = 0.01$ μF, $T_j = 25°C$)

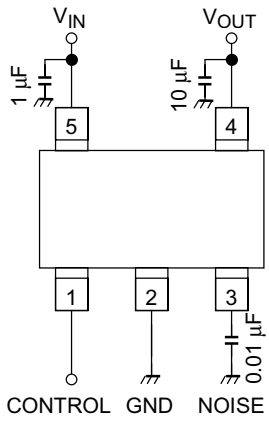
Characteristics	Symbol	Test Condition	Min	Typ.	Max	Unit
Output voltage	V_{OUT}	Please refer to the Output Voltage Accuracy table.				
Line regulation	Reg·line	$V_{OUT} + 1$ V ≦ V_{IN} ≦ 15 V, $I_{OUT} = 1$ mA	—	3	15	mV
Load regulation	Reg·load	1 mA ≦ I_{OUT} ≦ 150 mA	—	25	75	mV
Quiescent current	I_{B1}	$I_{OUT} = 0$ mA	—	170	—	μA
	I_{B2}	$I_{OUT} = 50$ mA	—	550	850	
Stand-by current	$I_{B\ (OFF)}$	$V_{CT} = 0$ V	—	—	0.1	μA
Output noise voltage	V_{NO}	$V_{IN} = V_{OUT} + 1$ V, $I_{OUT} = 10$ mA, 10 Hz ≦ f ≦ 100 kHz, $C_{NOISE} = 0.01$ μF, Ta = 25°C	—	30	—	μV_{rms}
Dropout volatge	$V_{IN} - V_{OUT}$	$I_{OUT} = 50$ mA	—	130	200	mV
Temperature coefficient	T_{CVO}	−40°C ≦ T_{opr} ≦ 85°C	—	100	—	ppm/°C
Input voltage	V_{IN}	—	V_{OUT} + 0.2 V	—	15	V
Ripple rejection	R.R.	$V_{IN} = V_{OUT} + 1$ V, $I_{OUT} = 10$ mA, $C_{NOISE} = 0.01$ μF, f = 1 kHz, $V_{Ripple} = 500\ mV_{p-p}$, Ta = 25°C	—	70	—	dB
Control voltage (ON)	$V_{CT\ (ON)}$	—	1.5	—	V_{IN}	V
Control voltage (OFF)	$V_{CT\ (OFF)}$	—	—	—	0.4	V
Control current (ON)	$I_{CT\ (ON)}$	$V_{CT} = 1.5$ V	—	3	10	μA
Control current (OFF)	$I_{CT\ (OFF)}$	$V_{CT} = 0$ V	—	0	0.1	μA

Output Voltage Accuracy
(V_{IN} = V_{OUT} + 1 V, I_{OUT} = 50 mA, C_{IN} = 1 μF, C_{OUT} = 10 μF, C_{NOISE} = 0.01 μF, T_j = 25°C)

Product No.	Symbol	Min	Typ.	Max	Unit
TAR5S15	V_{OUT}	1.44	1.5	1.56	V
TAR5S18		1.74	1.8	1.86	
TAR5S20		1.94	2.0	2.06	
TAR5S21		2.04	2.1	2.16	
TAR5S22		2.14	2.2	2.26	
TAR5S23		2.24	2.3	2.36	
TAR5S25		2.43	2.5	2.57	
TAR5S27		2.63	2.7	2.77	
TAR5S28		2.73	2.8	2.87	
TAR5S29		2.83	2.9	2.97	
TAR5S30		2.92	3.0	3.08	
TAR5S31		3.02	3.1	3.18	
TAR5S32		3.12	3.2	3.28	
TAR5S33		3.21	3.3	3.39	
TAR5S35		3.41	3.5	3.59	
TAR5S45		4.38	4.5	4.62	
TAR5S48		4.68	4.8	4.92	
TAR5S50		4.87	5.0	5.13	

Application Note

1. Recommended Application Circuit



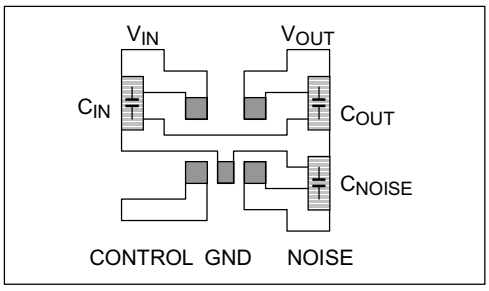
Control Level	Operation
HIGH	ON
LOW	OFF

The figure above shows the recommended configuration for using a point regulator. Insert a capacitor for stable input/output operation. If the control function is not to be used, Toshiba recommend that the control pin (pin 1) be connected to the V_{CC} pin.

2. Power Dissipation

The power dissipation for board-mounted TAR5Sxx Series devices (rated at 380 mW) is measured using a board whose size and pattern are as shown below. When incorporating a device belonging to this series into your design, derate the power dissipation as far as possible by reducing the levels of parameters such as input voltage, output current and ambient temperature. Toshiba recommend that these devices should typically be derated to 70%~80% of their absolute maximum power dissipation value.

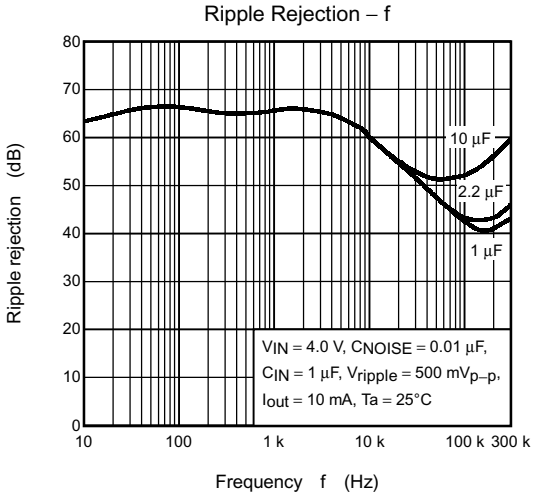
Thermal Resistance Evaluation Board

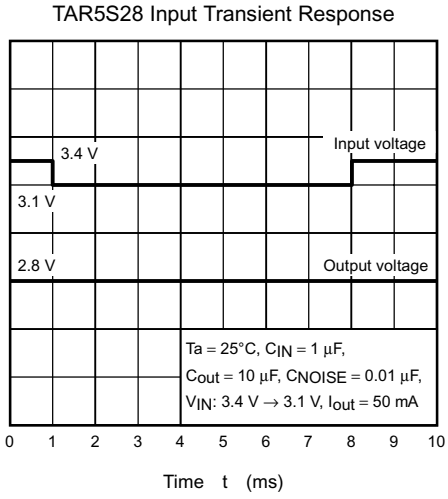


Circuit board material: glass epoxy,
Circuit board dimension:30 mm × 30 mm,
Copper foil pad area: 50 mm^2 (t = 0.8 mm)

3. Ripple Rejection

The devices of the TAR5Sxx Series feature a circuit with an excellent ripple rejection characteristic. Because the circuit also features an excellent output fluctuation characteristic for sudden supply voltage drops, the circuit is ideal for use in the RF blocks incorporated in all mobile telephones.

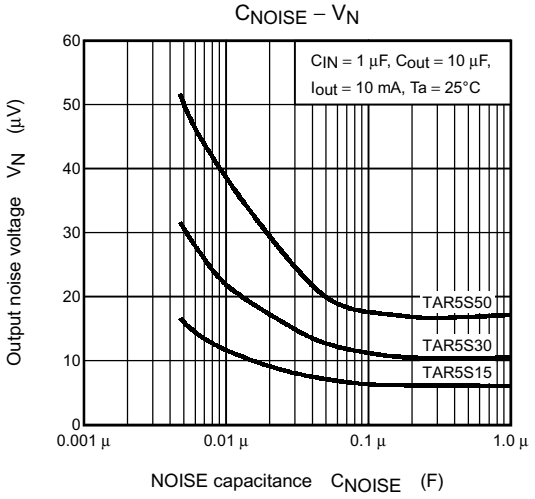


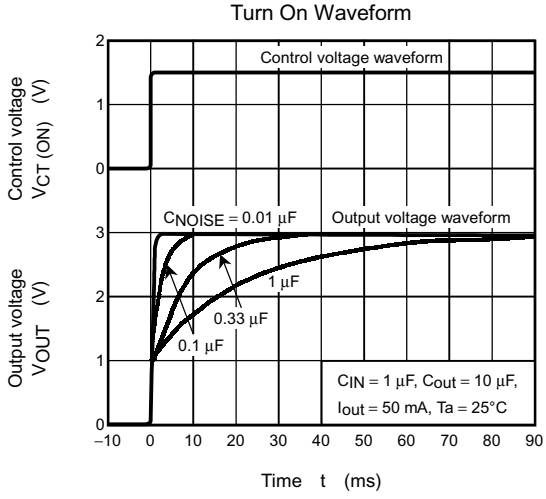


4. NOISE Pin

TAR5Sxx Series devices incorporate a NOISE pin to reduce output noise voltage. Inserting a capacitor between the NOISE pin and GND reduces output noise. To ensure stable operation, insert a capacitor of 0.0047 µF or more between the NOISE pin and GND.

The output voltage rise time varies according to the capacitance of the capacitor connected to the NOISE pin.





5. Example of Characteristics when Ceramic Capacitor is Used

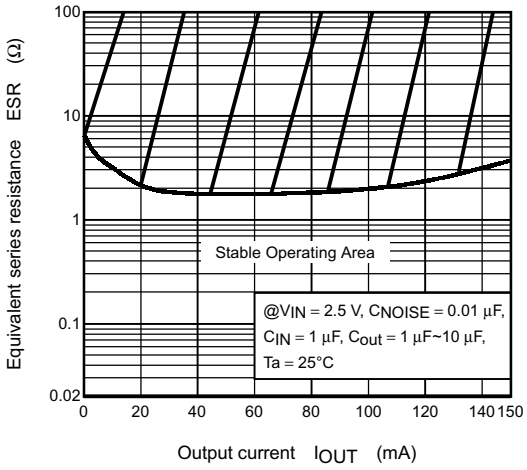
Shown below is the stable operation area, where the output voltage does not oscillate, evaluated using a Toshiba evaluation circuit. The equivalent series resistance (ESR) of the output capacitor and output current determines this area. TAR5Sxx Series devices operate stably even when a ceramic capacitor is used as the output capacitor.

If a ceramic capacitor is used as the output capacitor and the ripple frequency is 30 kHz or more, the ripple rejection differs from that when a tantalum capacitor is used. This is shown below.

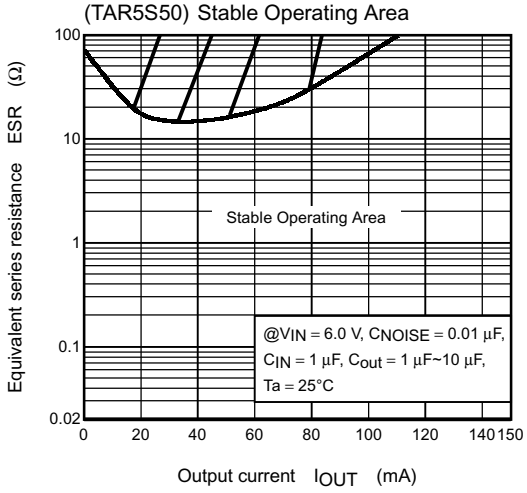
Toshiba recommend that users check that devices operate stably under the intended conditions of use.

Examples of safe operating area characteristics

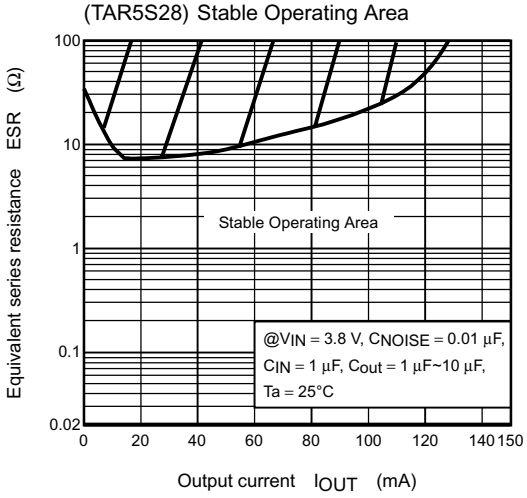
(TAR5S15) Stable Operating Area



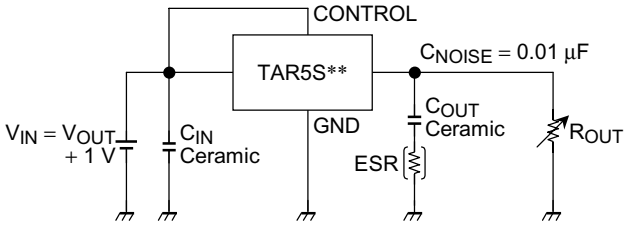
(TAR5S50) Stable Operating Area



(TAR5S28) Stable Operating Area



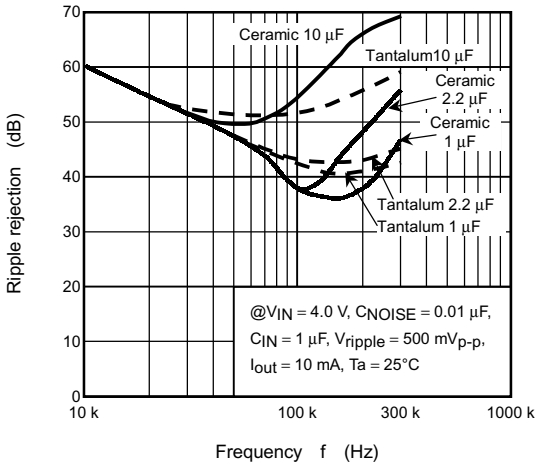
Evaluation Circuit for Stable Operating Area



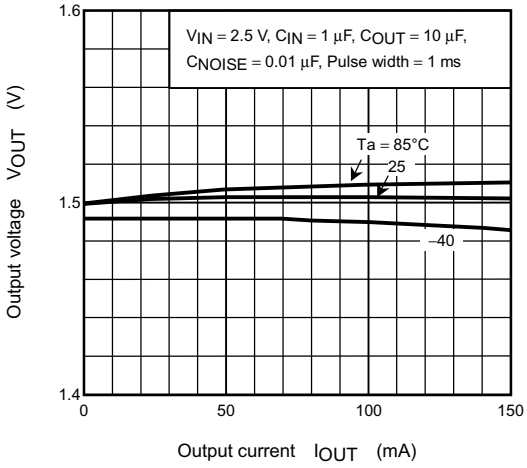
Capacitors used for evaluation
Made by Murata C_{IN}: GRM40B105K
C_{OUT}: GRM40B105K/GRM40B106K

Ripple Rejection Characteristic (f = 10 kHz~300 kHz)

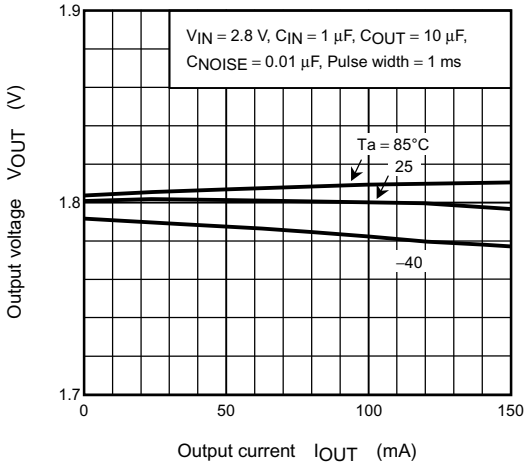
(TAR5S30) Ripple Rejection – f



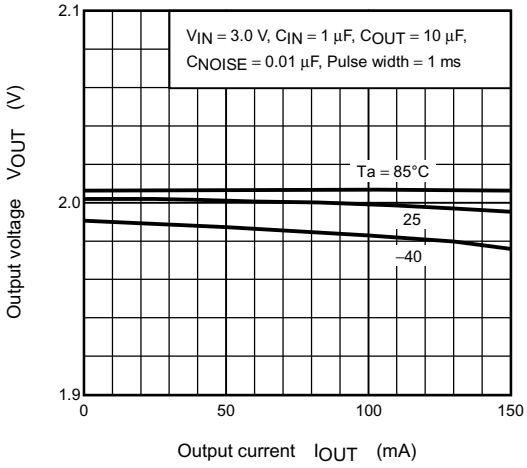
(TAR5S15) $I_{OUT} - V_{OUT}$



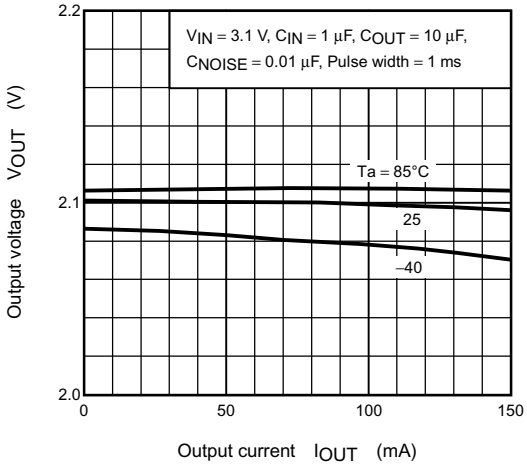
(TAR5S18) $I_{OUT} - V_{OUT}$



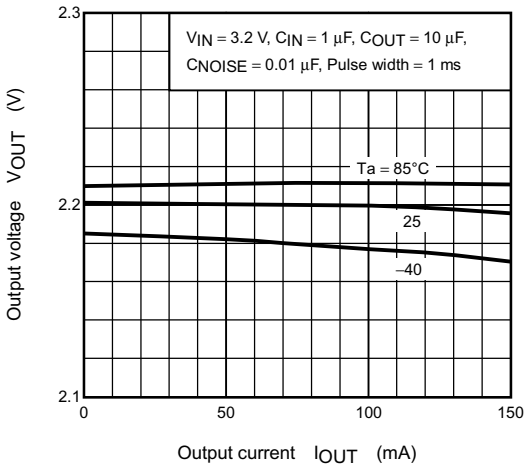
(TAR5S20) $I_{OUT} - V_{OUT}$



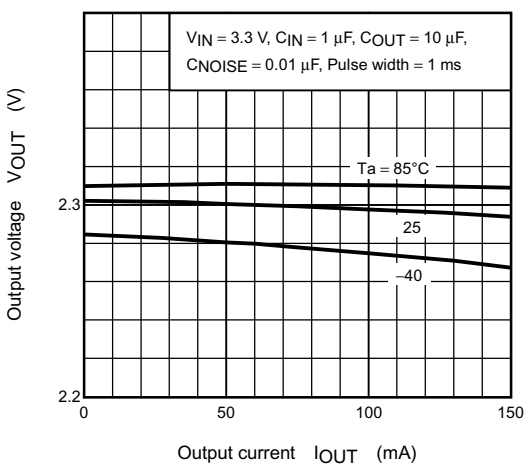
(TAR5S21) $I_{OUT} - V_{OUT}$



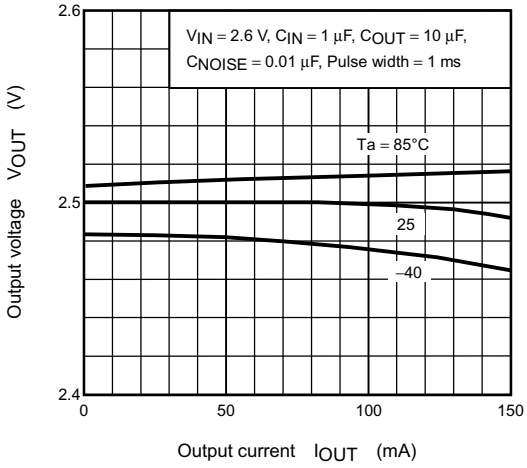
(TAR5S22) $I_{OUT} - V_{OUT}$



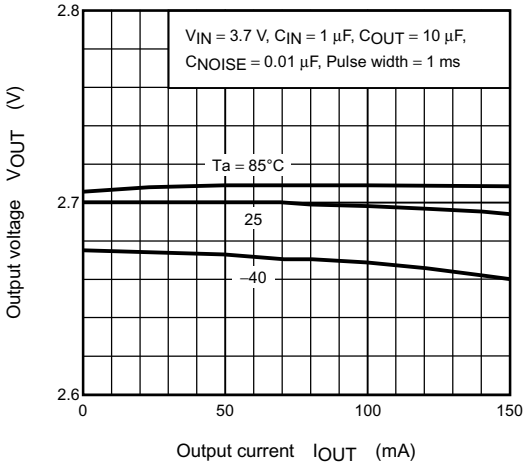
(TAR5S23) $I_{OUT} - V_{OUT}$



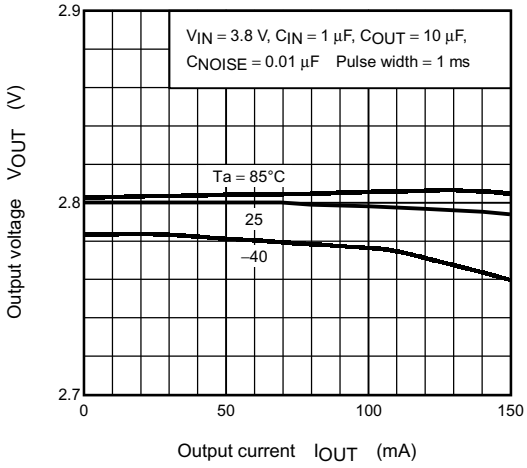
(TAR5S25) $I_{OUT} - V_{OUT}$



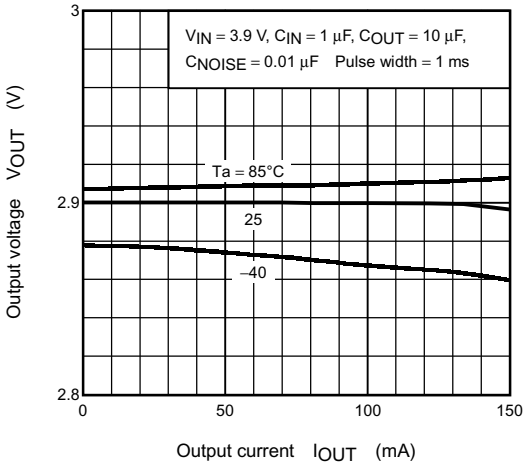
(TAR5S27) $I_{OUT} - V_{OUT}$



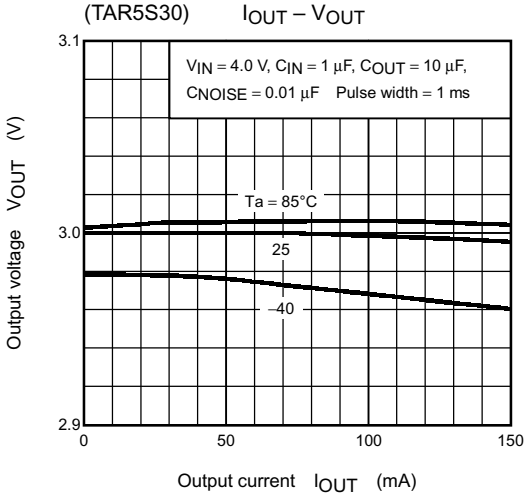
(TAR5S28) $I_{OUT} - V_{OUT}$



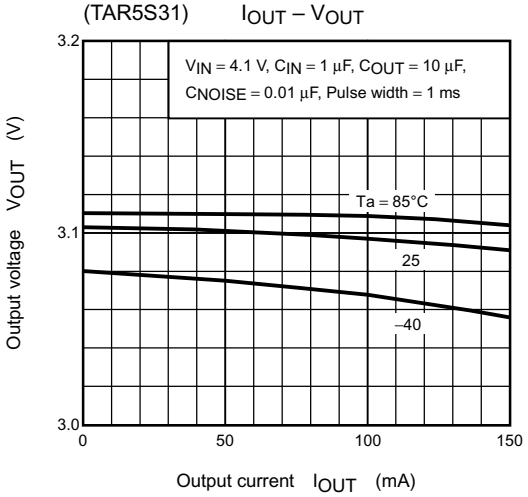
(TAR5S29) $I_{OUT} - V_{OUT}$



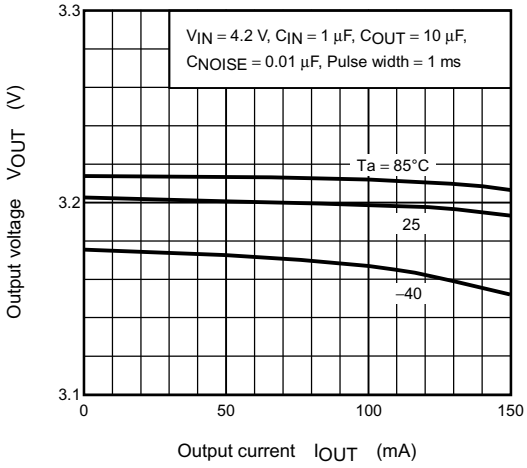
(TAR5S30) $I_{OUT} - V_{OUT}$



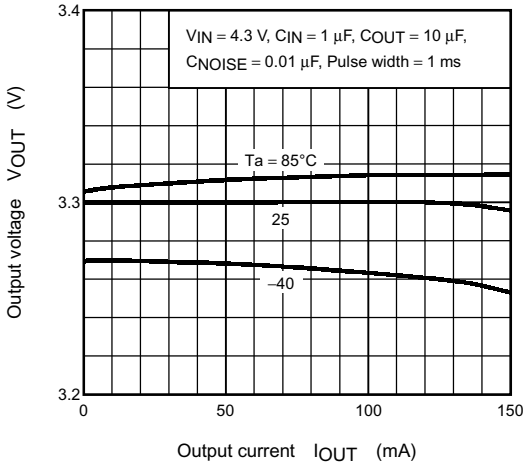
(TAR5S31) $I_{OUT} - V_{OUT}$



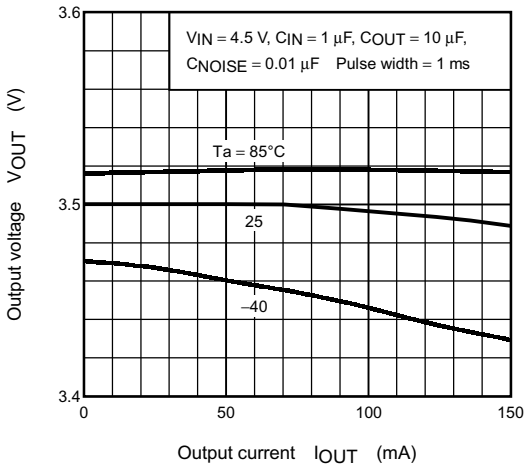
(TAR5S32) $I_{OUT} - V_{OUT}$



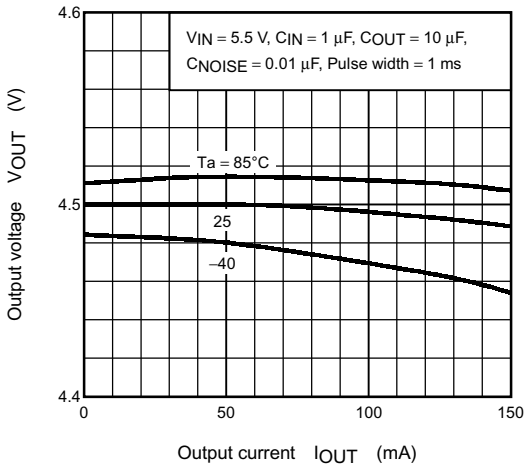
(TAR5S33) $I_{OUT} - V_{OUT}$



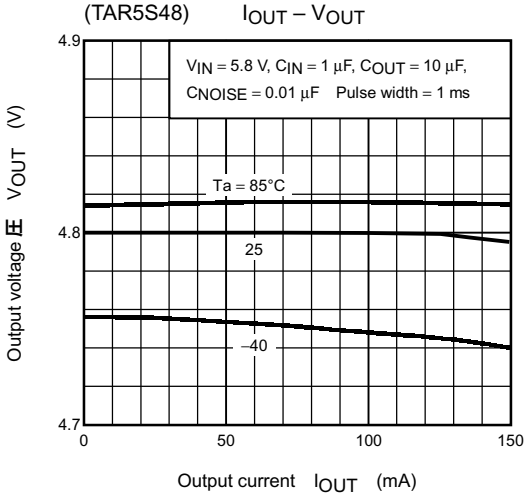
(TAR5S35) $I_{OUT} - V_{OUT}$



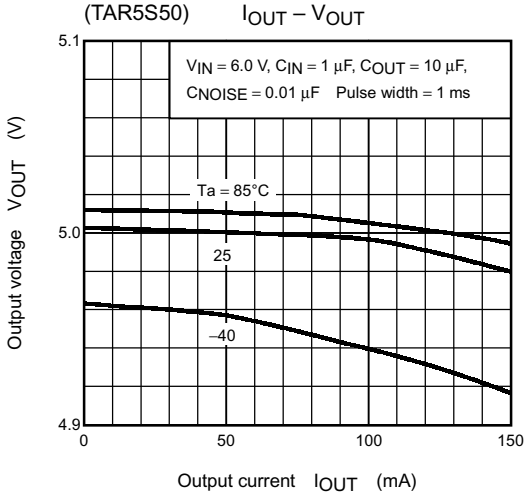
(TAR5S45) $I_{OUT} - V_{OUT}$



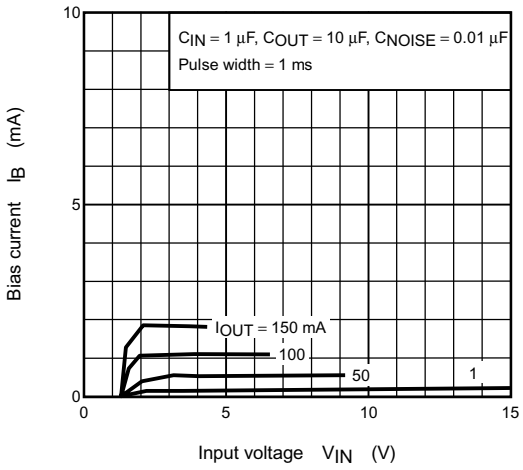
(TAR5S48) $I_{OUT} - V_{OUT}$



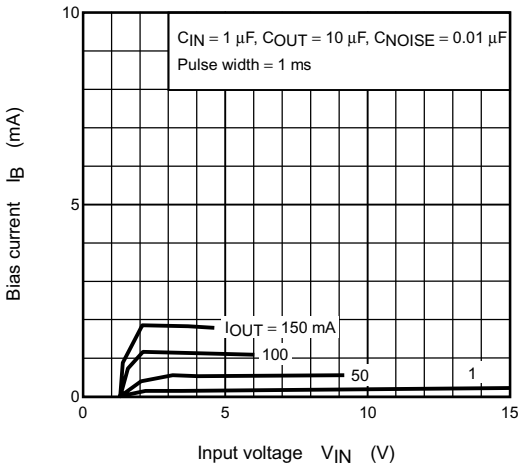
(TAR5S50) $I_{OUT} - V_{OUT}$



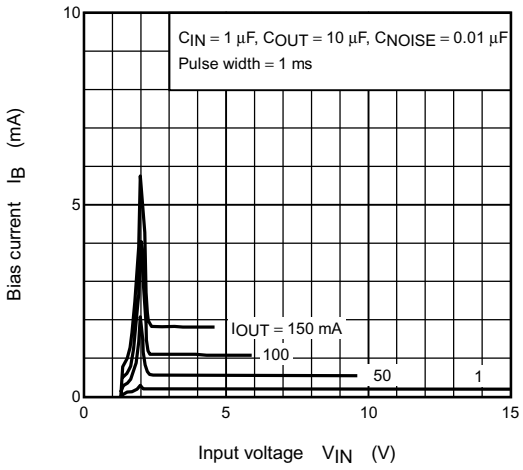
(TAR5S15) $I_B - V_{IN}$



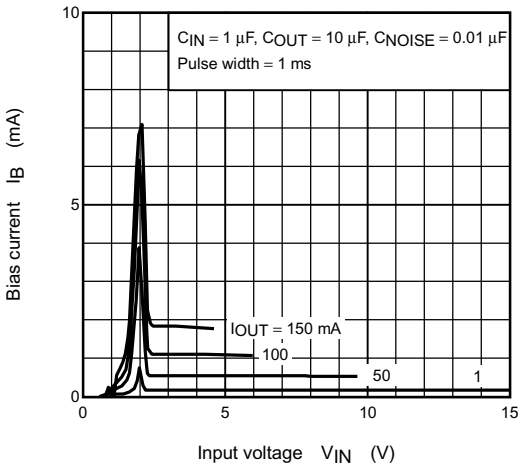
(TAR5S18) $I_B - V_{IN}$



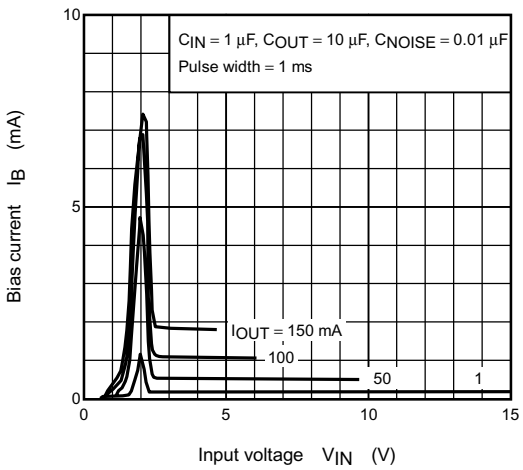
(TAR5S20) $I_B - V_{IN}$



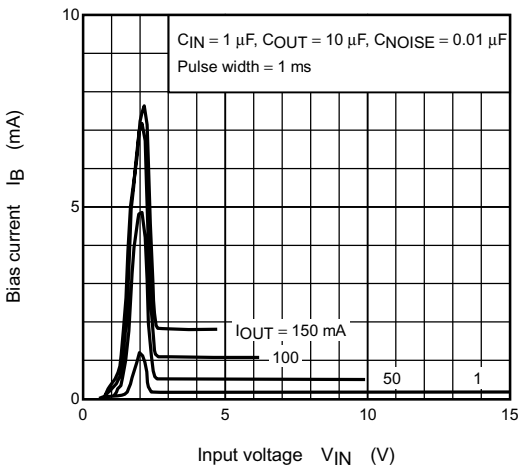
(TAR5S21) $I_B - V_{IN}$



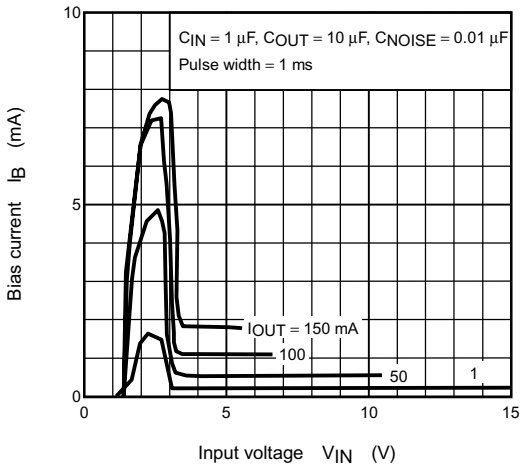
(TAR5S22) $I_B - V_{IN}$



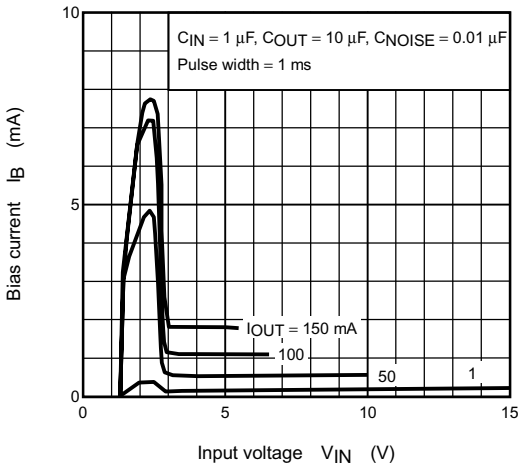
(TAR5S23) $I_B - V_{IN}$



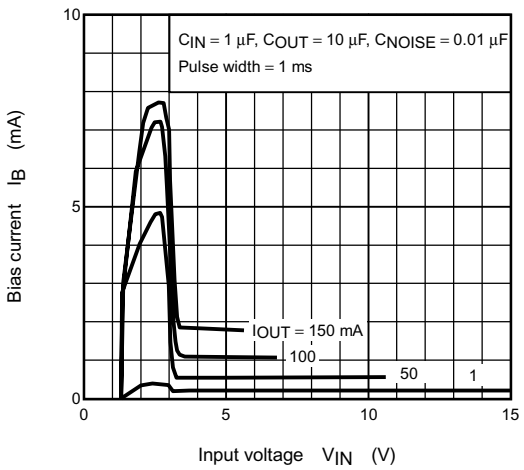
(TAR5S25) $I_B - V_{IN}$



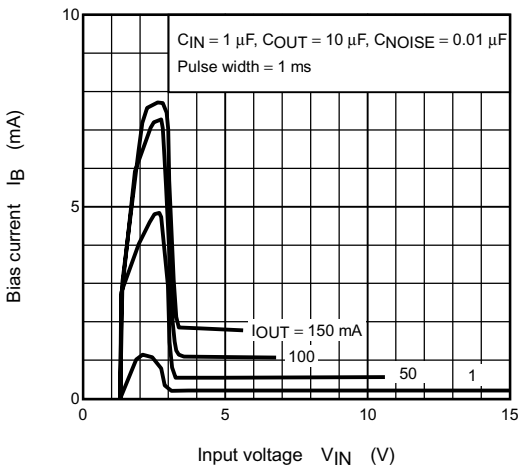
(TAR5S27) $I_B - V_{IN}$



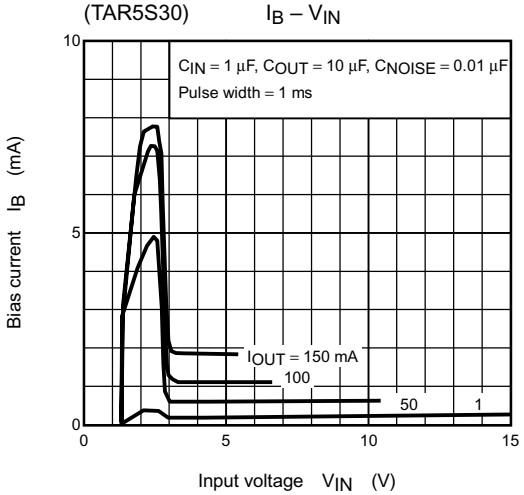
(TAR5S28) $I_B - V_{IN}$



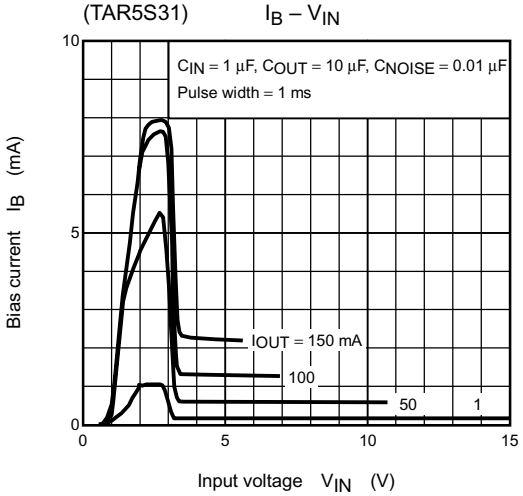
(TAR5S29) $I_B - V_{IN}$



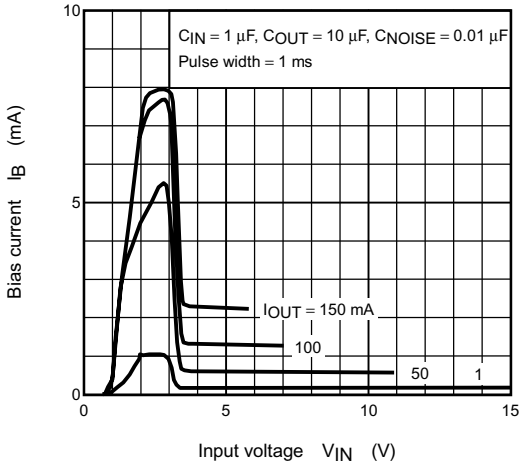
(TAR5S30) $I_B - V_{IN}$



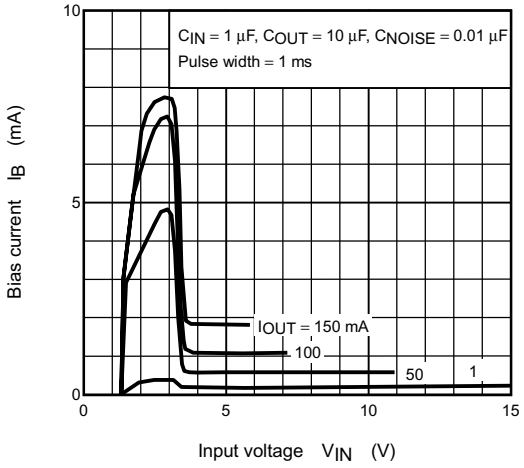
(TAR5S31) $I_B - V_{IN}$



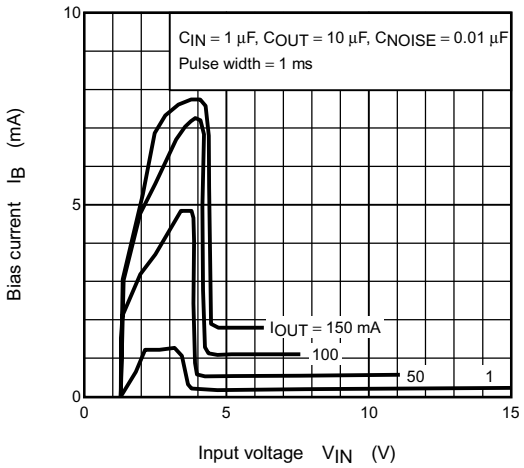
(TAR5S32) $I_B - V_{IN}$



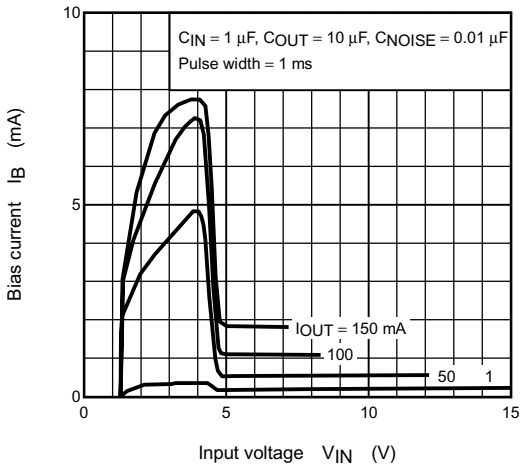
(TAR5S33) $I_B - V_{IN}$



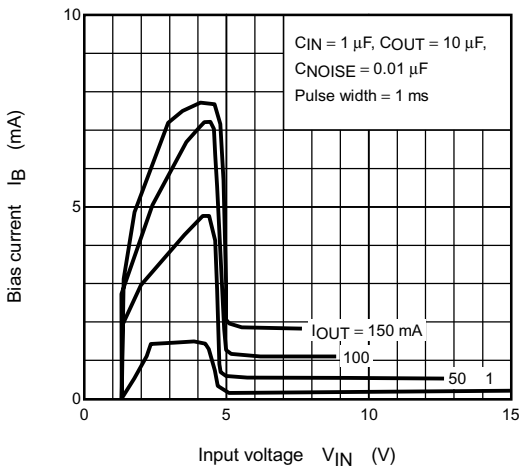
(TAR5S35) $I_B - V_{IN}$



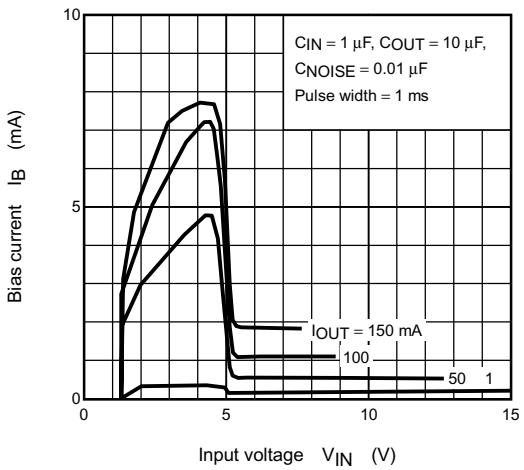
(TAR5S45) $I_B - V_{IN}$



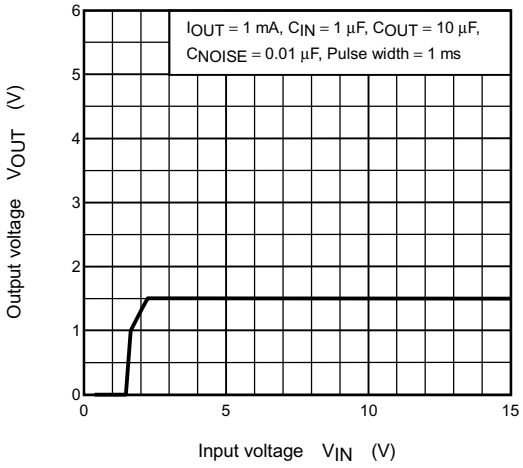
(TAR5S48) $I_B - V_{IN}$



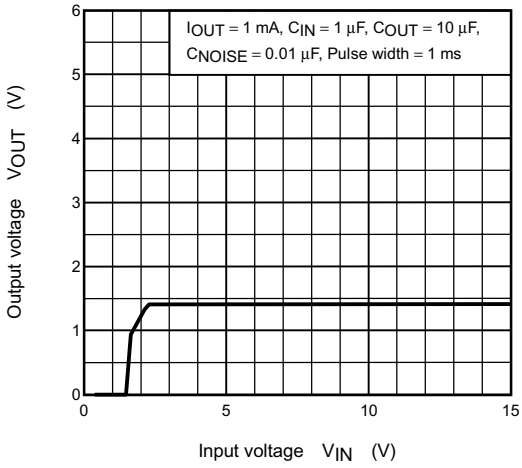
(TAR5S50) $I_B - V_{IN}$



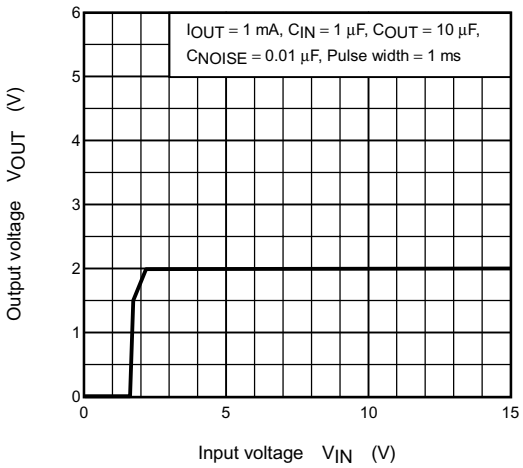
(TAR5S15) $V_{OUT} - V_{IN}$



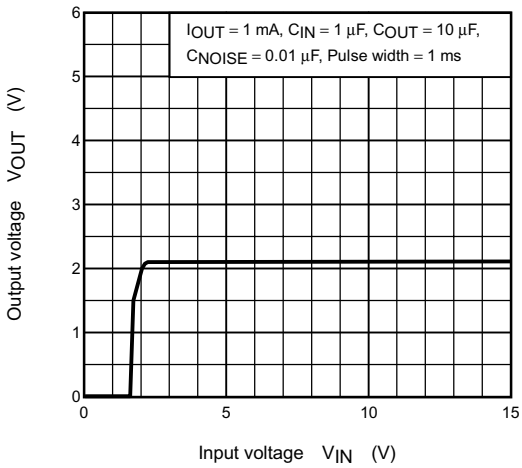
(TAR5S18) $V_{OUT} - V_{IN}$



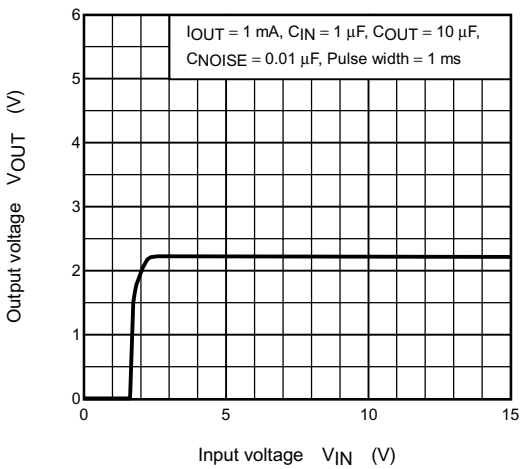
(TAR5S20) $V_{OUT} - V_{IN}$



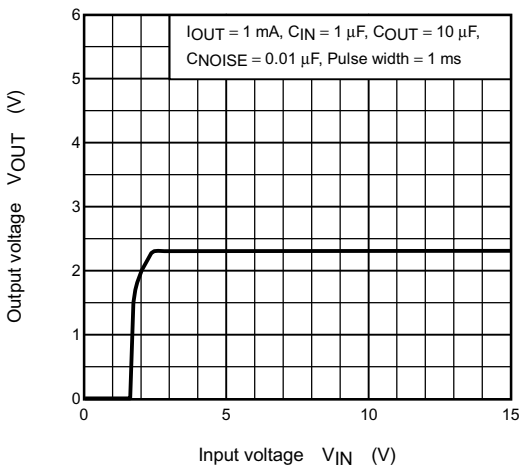
(TAR5S21) $V_{OUT} - V_{IN}$



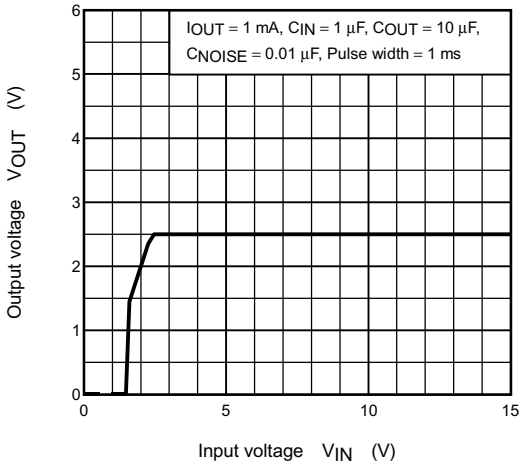
(TAR5S22) $V_{OUT} - V_{IN}$



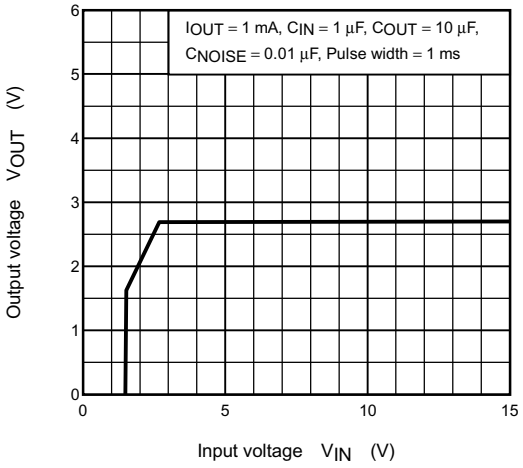
(TAR5S23) $V_{OUT} - V_{IN}$



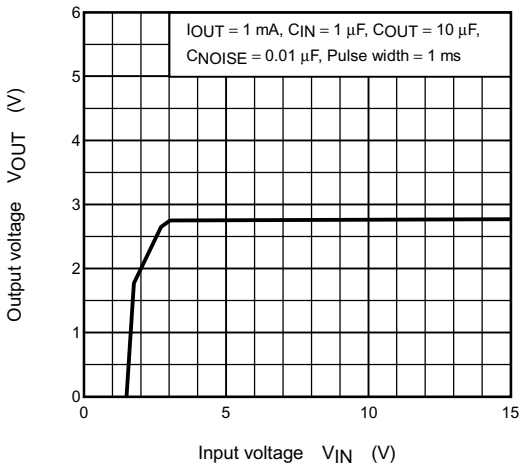
(TAR5S25) $V_{OUT} - V_{IN}$



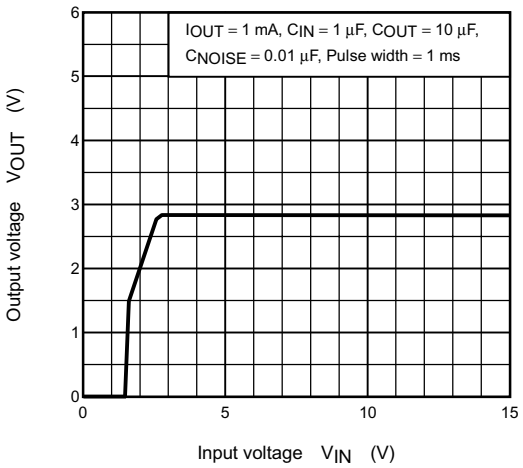
(TAR5S27) $V_{OUT} - V_{IN}$



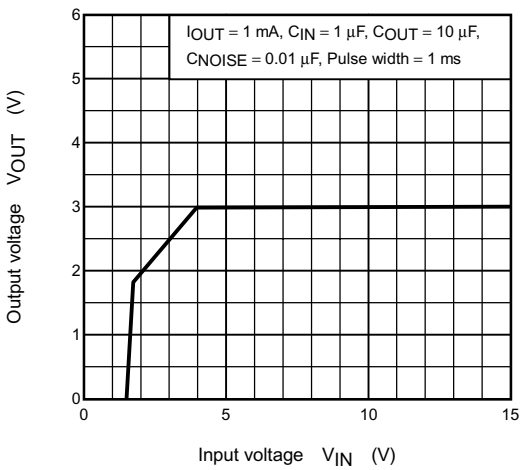
(TAR5S28) $V_{OUT} - V_{IN}$



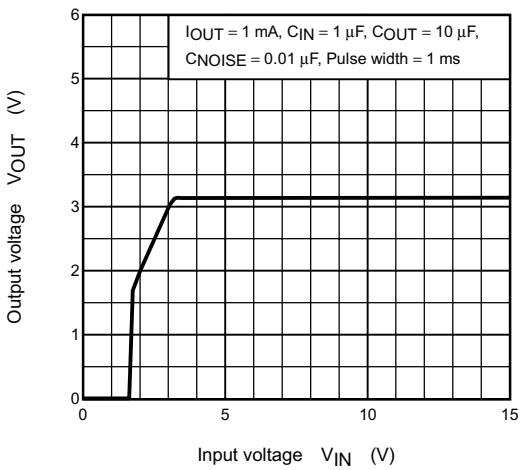
(TAR5S29) $V_{OUT} - V_{IN}$



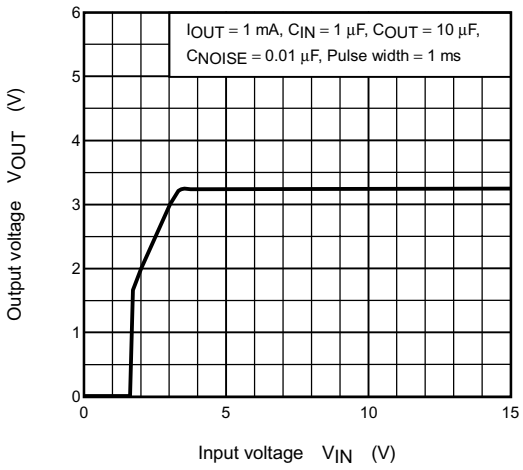
(TAR5S30) $V_{OUT} - V_{IN}$



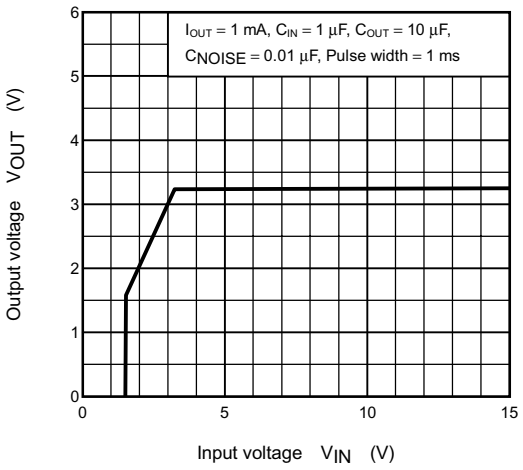
(TAR5S31) $V_{OUT} - V_{IN}$



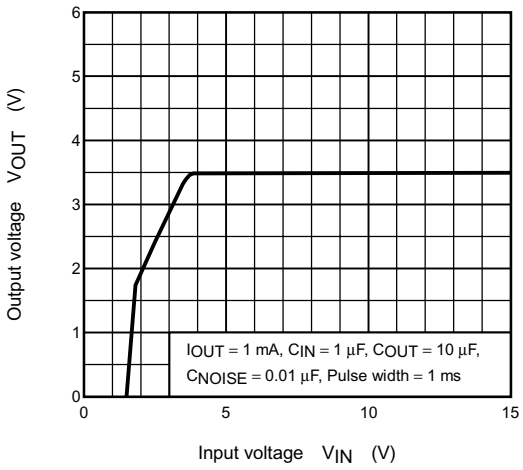
(TAR5S32) $V_{OUT} - V_{IN}$



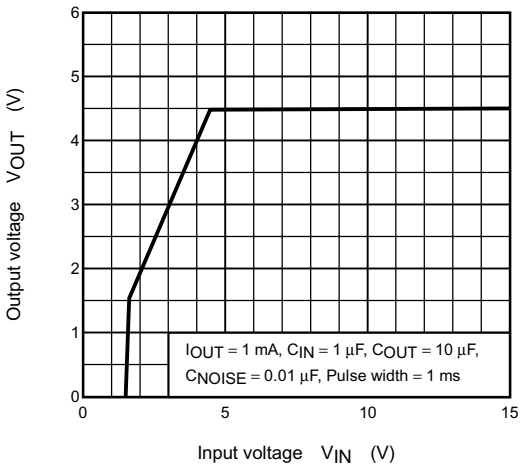
(TAR5S33) $V_{OUT} - V_{IN}$



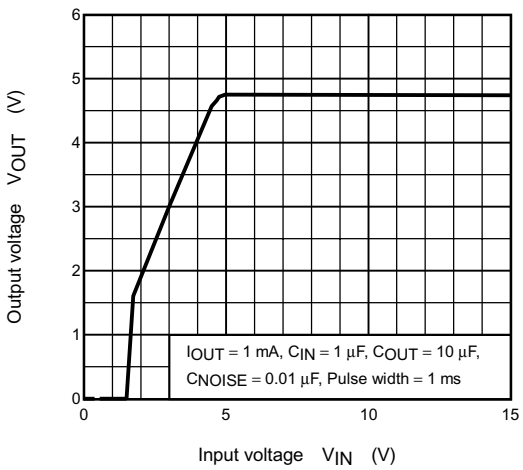
(TAR5S35) $V_{OUT} - V_{IN}$



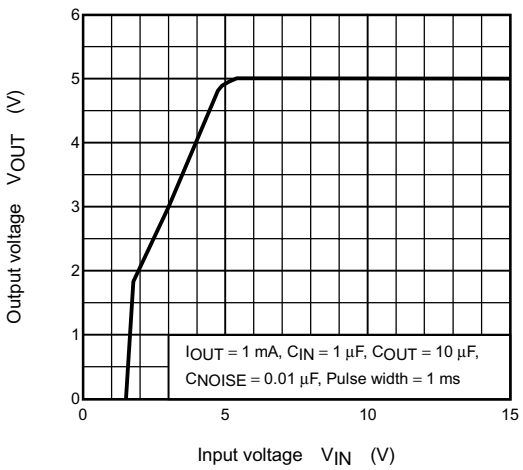
(TAR5S45) $V_{OUT} - V_{IN}$



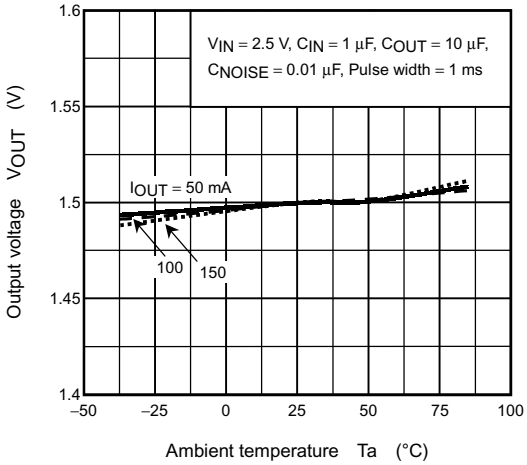
(TAR5S48) $V_{OUT} - V_{IN}$



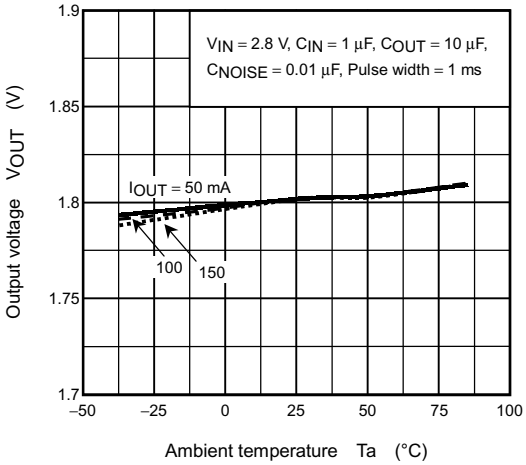
(TAR5S50) $V_{OUT} - V_{IN}$



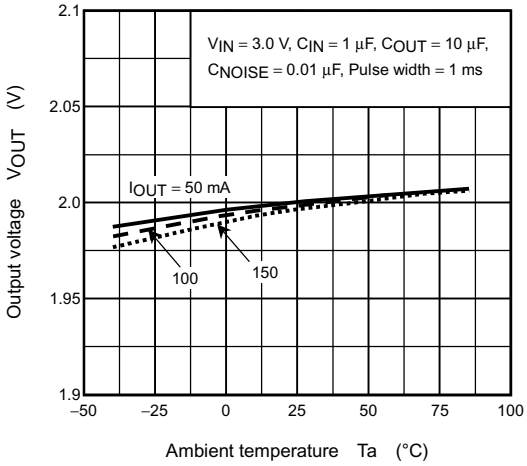
(TAR5S15) V_{OUT} – Ta



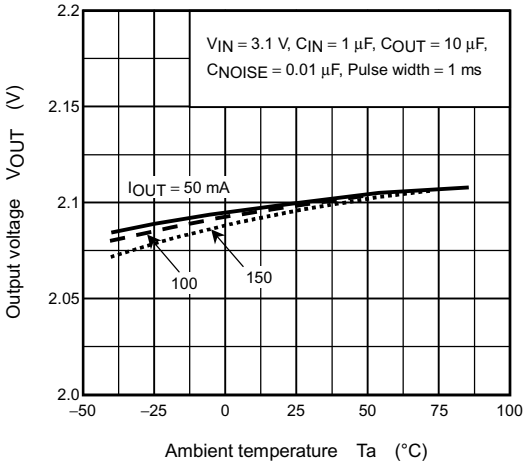
(TAR5S18) V_{OUT} – Ta



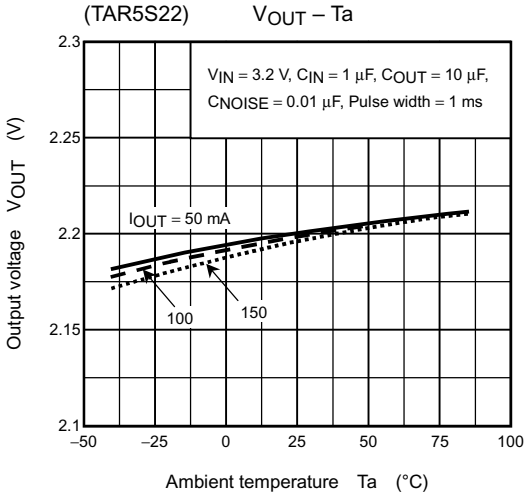
(TAR5S20) V_{OUT} – Ta



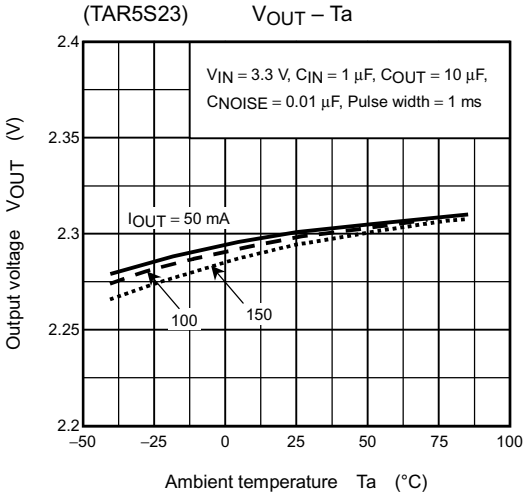
(TAR5S21) V_{OUT} – Ta



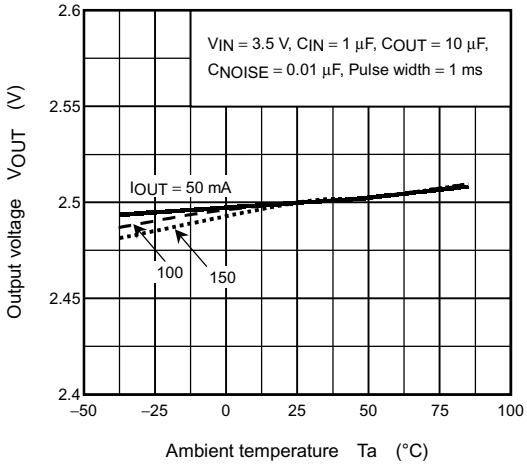
(TAR5S22) V_{OUT} – Ta



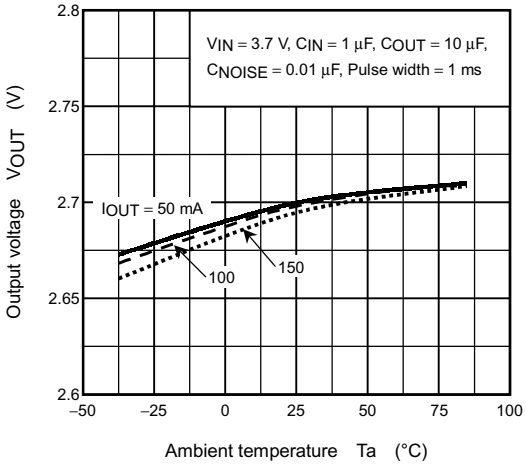
(TAR5S23) V_{OUT} – Ta



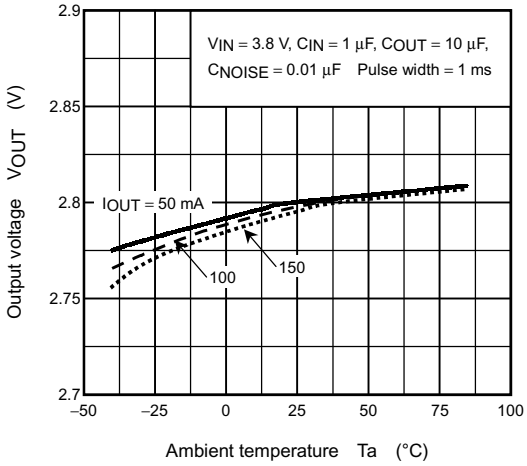
(TAR5S25) V_{OUT} – Ta



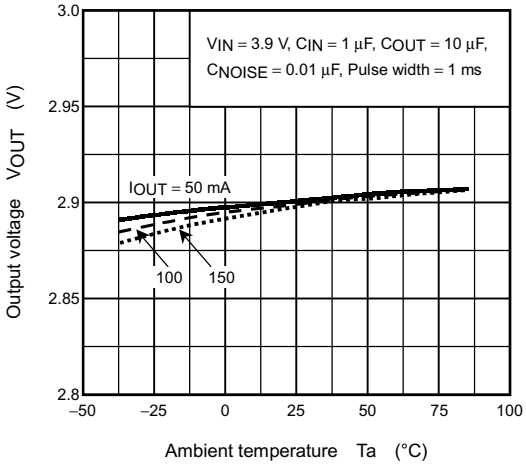
(TAR5S27) V_{OUT} – Ta



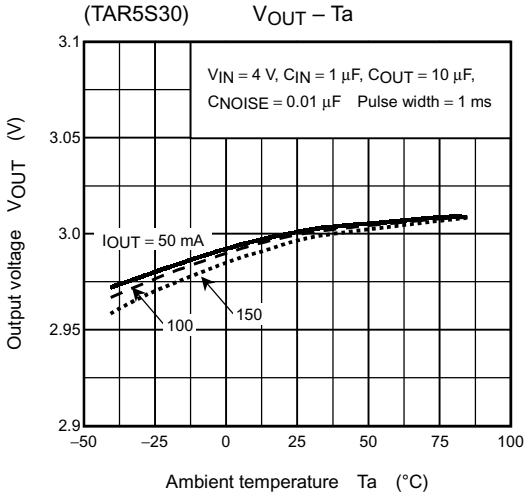
(TAR5S28) V_{OUT} – Ta



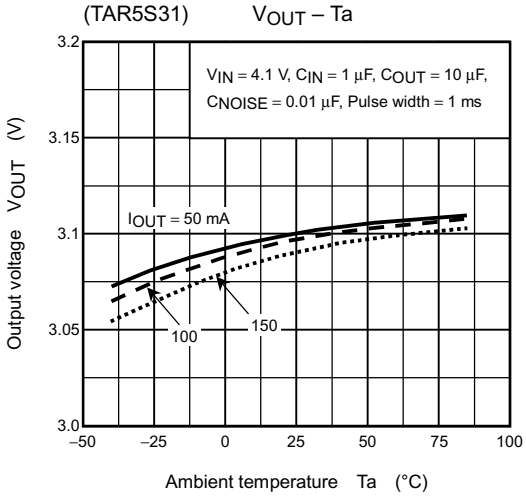
(TAR5S29) V_{OUT} – Ta



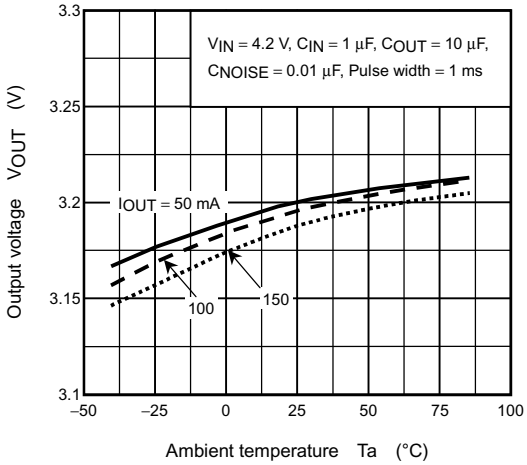
(TAR5S30) V_{OUT} – Ta



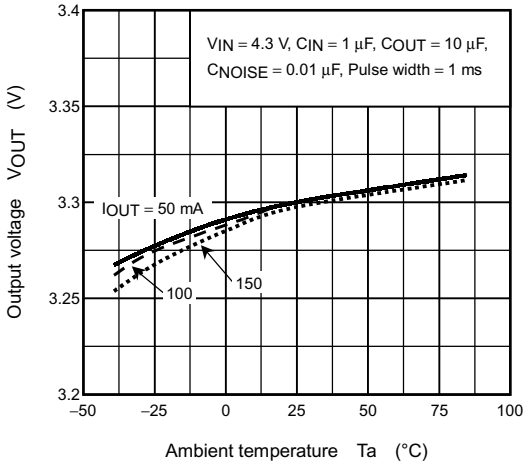
(TAR5S31) V_{OUT} – Ta



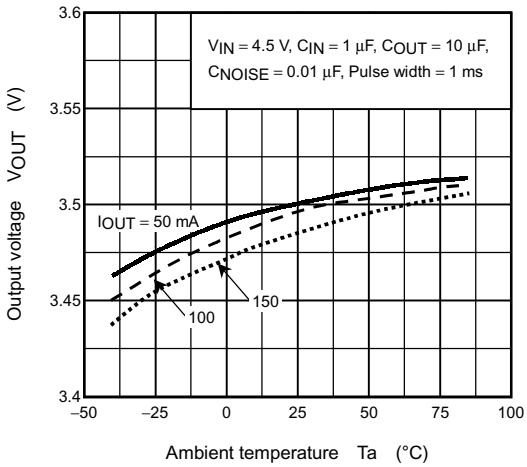
(TAR5S32) V_{OUT} – Ta



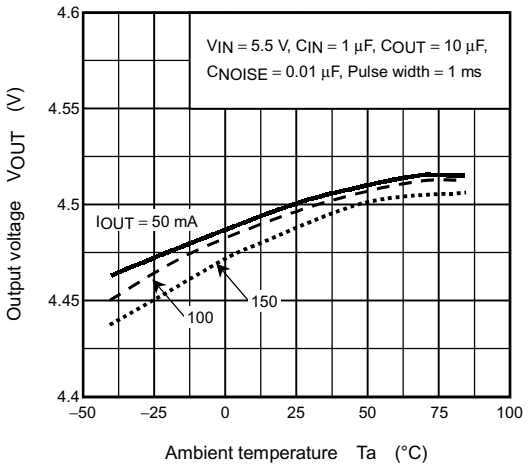
(TAR5S33) V_{OUT} – Ta



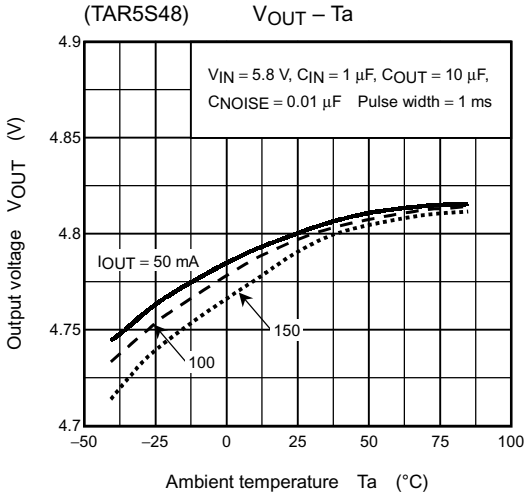
(TAR5S35) V_{OUT} – Ta



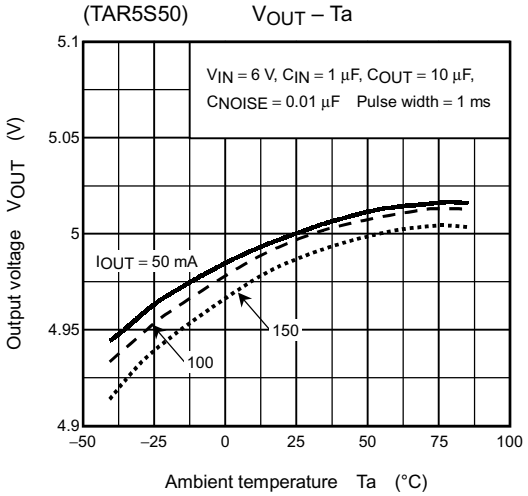
(TAR5S45) V_{OUT} – Ta



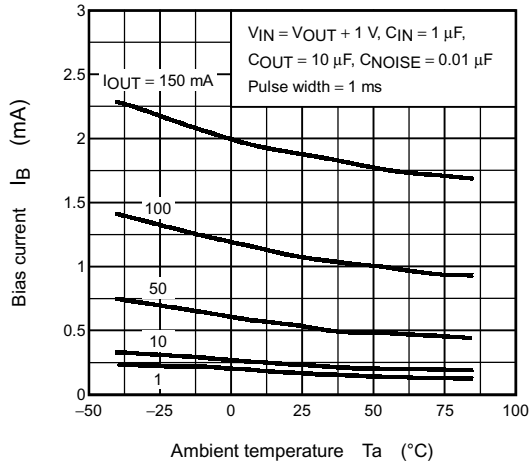
(TAR5S48) V_{OUT} – Ta



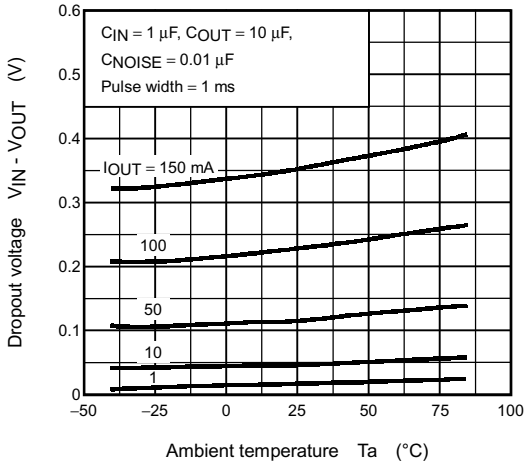
(TAR5S50) V_{OUT} – Ta



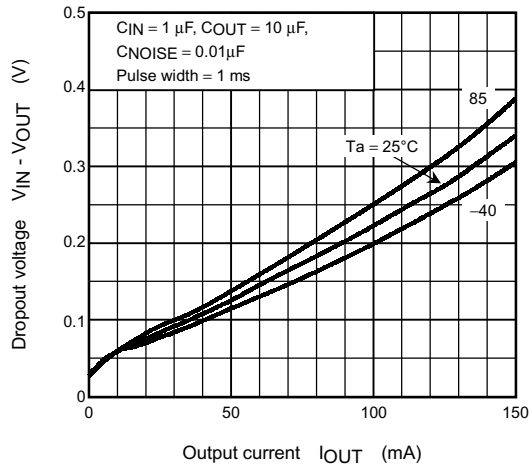
I_B – Ta



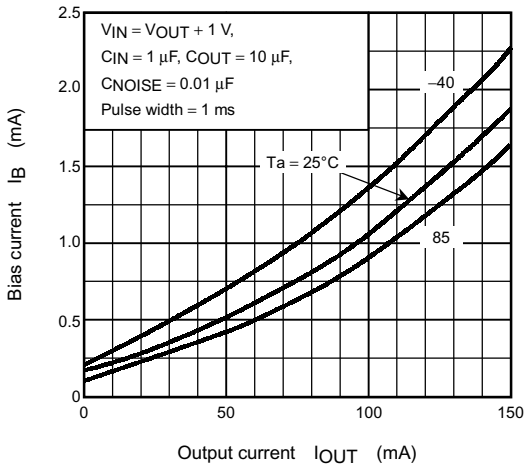
(TAR5S23~TAR5S50) V_{IN} - V_{OUT} – Ta



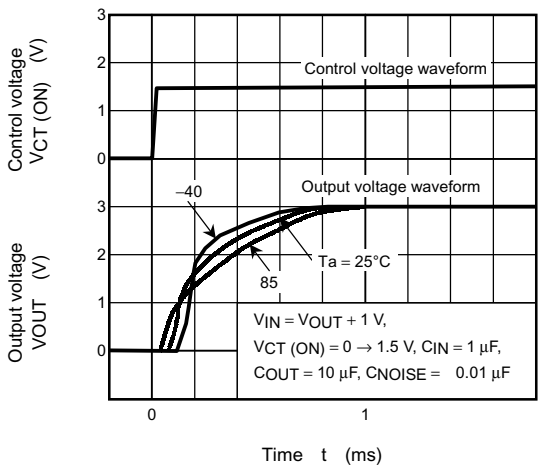
(TAR5S23~TAR5S50) V_{IN} - V_{OUT} – I_{OUT}



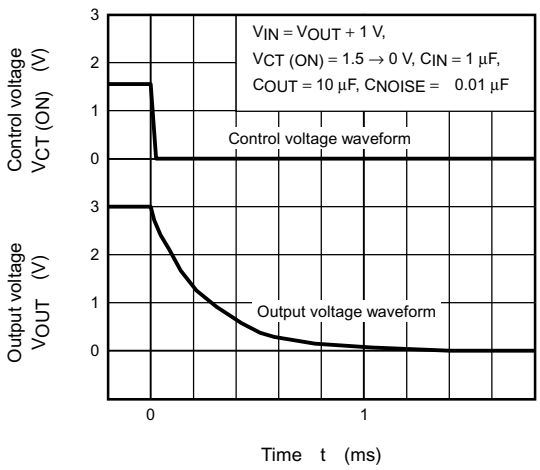
I_B – I_{OUT}



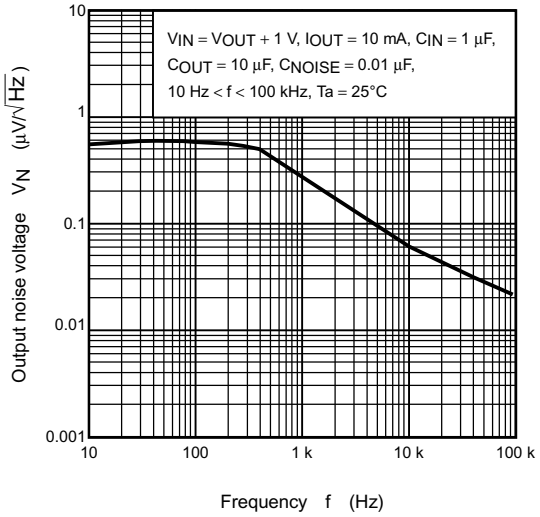
Turn On Waveform



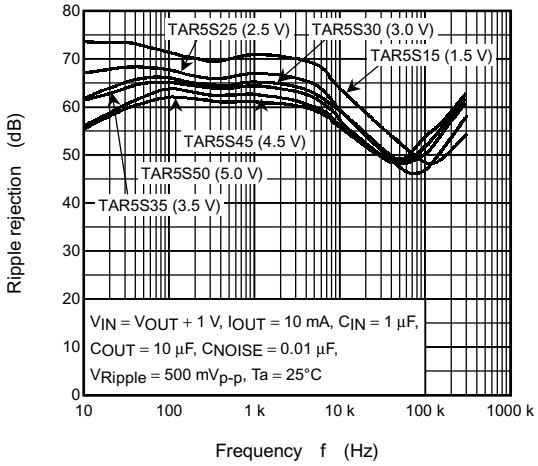
Turn Off Waveform



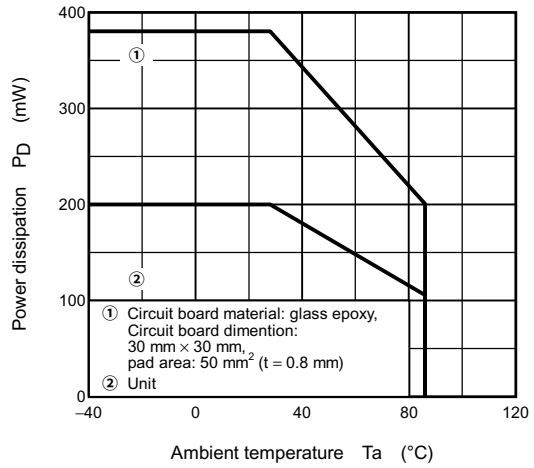
V_N – f



Ripple Rejection – f

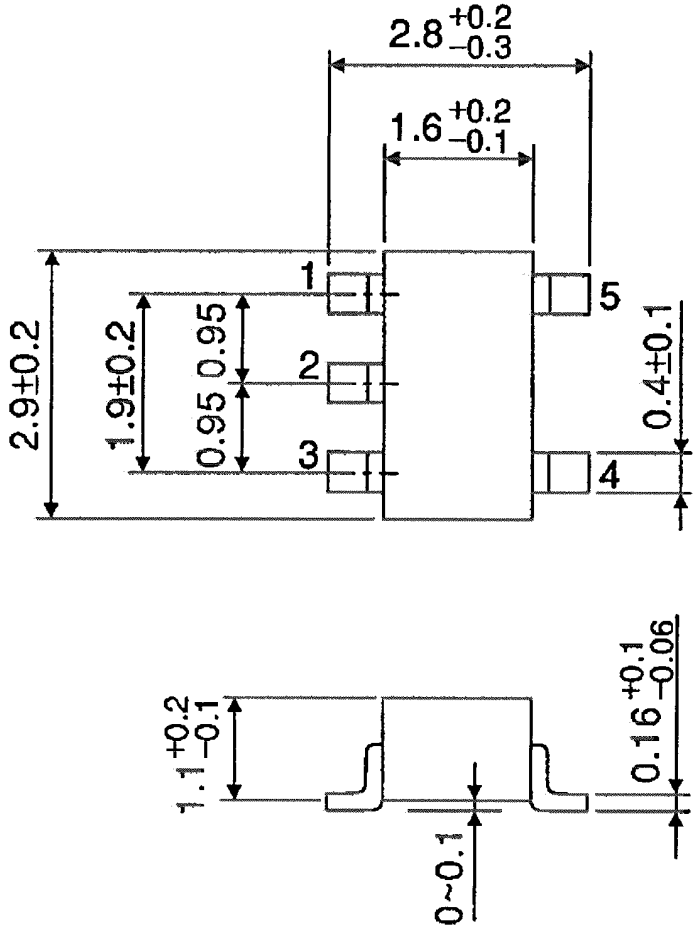


P_D – Ta



Package Dimensions

SSOP5-P-0.95 Unit : mm



Weight: 0.014 g (typ.)