

## LMH6714/6720/6722

# Wideband Video Op Amp; Single, Single with Shutdown and Quad

### General Description

The LMH6714/6720/6722 series combine National's VIP10™ high speed complementary bipolar process with National's current feedback topology to produce a very high speed op amp. These amplifiers provide a 400MHz small signal bandwidth at a gain of +2V/V and a 1800V/ $\mu$ s slew rate while consuming only 5.6mA from  $\pm 5$ V supplies.

The LMH6714/6720/6722 series offer exceptional video performance with its 0.01% and 0.01° differential gain and phase errors for NTSC and PAL video signals while driving a back terminated  $75\Omega$  load. They also offer a flat gain response of 0.1dB to 120MHz. Additionally, they can deliver 70mA continuous output current. This level of performance makes them an ideal op amp for broadcast quality video systems.

The LMH6714/6720/6722's small packages (SOIC & SOT23), low power requirement, low noise and distortion allow the LMH6714/6720/6722 to serve portable RF applications. The high impedance state during shutdown makes the LMH6720 suitable for use in multiplexing multiple high speed signals onto a shared transmission line. The LMH6720 is also ideal for portable applications where current draw can be reduced with the shutdown function.

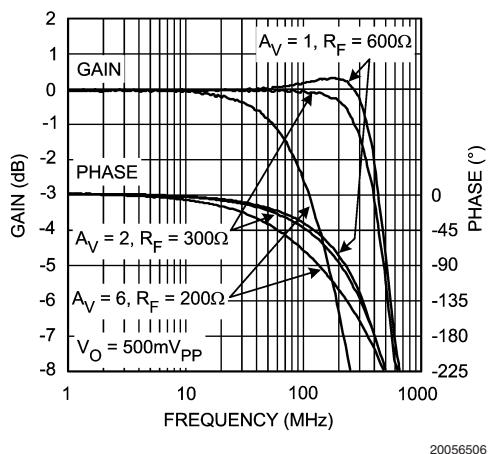
### Features

- 400MHz ( $A_V = +2V/V$ ,  $V_{OUT} = 500mV_{PP}$ ) -3dB BW
- 250MHz ( $A_V = +2V/V$ ,  $V_{OUT} = 2V_{PP}$ ) -3dB BW
- 0.1dB gain flatness to 120MHz
- Low power: 5.6mA
- TTL compatible shutdown pin (LMH6720)
- Very low diff. gain, phase: 0.01%, 0.01° (LMH6714)
- -58 HD2 / -70 HD3 at 20MHz
- Fast slew rate: 1800V/ $\mu$ s
- Low shutdown current: 500uA (LMH6720)
- 11ns turn on time (LMH6720)
- 7ns shutdown time (LMH6720)
- Unity gain stable
- Improved replacement for CLC400,401,402,404,406 and 446 (LMH6714)
- Improved replacement for CLC405 (LMH6720)
- Improved replacement for CLC415 (LMH6722)

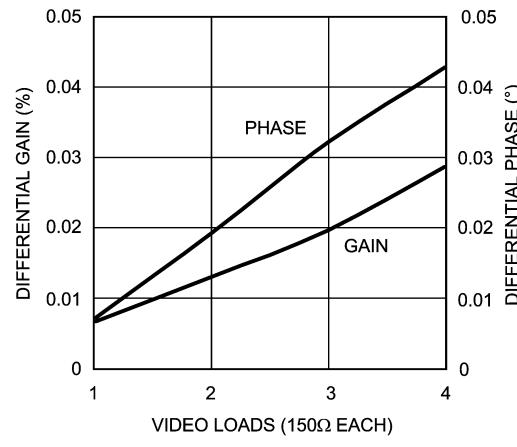
### Applications

- HDTV, NTSC & PAL video systems
- Video switching and distribution
- Wideband active filters
- Cable drivers
- High speed multiplexer (LMH6720)
- Programmable gain amplifier (LMH6720)

Non-Inverting Small Signal Frequency Response



Differential Gain and Phase vs. Number of Video Loads (LMH6714)



**Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

ESD Tolerance (Note 4)

Human Body Model	2000V
Machine Model	200V
$V_{CC}$	$\pm 6.75V$
$I_{OUT}$	(Note 3)
Common Mode Input Voltage	$\pm V_{CC}$
Differential Input Voltage	2.2V
Maximum Junction Temperature	+150°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (soldering 10 sec)	+300°C

Storage Temperature Range

-65°C to +150°C

Shutdown Pin Voltage (Note 5)

 $+V_{CC}$  to  $V_{CC}/2-1V$ **Operating Ratings** (Note 3)

Thermal Resistance

Package	$(\theta_{JA})$
5-Pin SOT23	232°C/W
6-Pin SOT23	198°C/W
8-Pin SOIC	145°C/W
14-Pin SOIC	130°C/W
Operating Temperature	-40°C to +85°C
Nominal Supply Voltage	$\pm 5V$ to $\pm 6V$

**Electrical Characteristics**

Unless specified,  $A_V = +2$ ,  $R_F = 300\Omega$ ;  $V_{CC} = \pm 5V$ ,  $R_L = 100\Omega$ , LMH6714/6720/6722. **Boldface** limits apply at temperature extremes.

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Frequency Domain Response</b>						
SSBW	-3dB Bandwidth	$V_{OUT} = 0.5V_{PP}$	345	400		MHz
LSBW	-3dB Bandwidth	$V_{OUT} = 2.0V_{PP}$	200	250		MHz
	Gain Flatness	$V_{OUT} = 2V_{PP}$				
GFP	Peaking	DC to 120MHz		0.1		dB
GFR	Rolloff	DC to 120MHz		0.1		dB
LPD	Linear Phase Deviation	DC to 120MHz		0.5		deg
DG	Differential Gain	$R_L = 150\Omega$ , 4.43MHz (LMH6714)		0.01		%
DG	Differential Gain	$R_L = 150\Omega$ , 4.43MHz (LMH6720)		0.02		%
DP	Differential Phase	$R_L = 150\Omega$ , 4.43MHz		0.01		deg
<b>Time Domain Response</b>						
TRS	Rise and Fall Time	.5V Step		1.5		ns
TRL		2V Step		2.6		ns
$t_s$	Settling Time to 0.05%	2V Step		12		ns
SR	Slew Rate	6V Step	1200	1800		V/μs
<b>Distortion and Noise Response</b>						
HD2	2nd Harmonic Distortion	$2V_{PP}$ , 20MHz		-58		dBc
HD3	3rd Harmonic Distortion	$2V_{PP}$ , 20MHz		-70		dBc
IMD	3rd Order Intermodulation Products	10MHz, $P_{OUT} = 0\text{dBm}$		-78		dBc
	Equivalent Input Noise					
VN	Non-Inverting Voltage	>1MHz		3.4		nV/√Hz
NICN	Inverting Current	>1MHz		10		pA/√Hz
ICN	Non-Inverting Current	>1MHz		1.2		pA/√Hz
<b>Static, DC Performance</b>						
$V_{IO}$	Output Offset Voltage		$\pm 0.2$	$\pm 6$	$\pm 8$	mV
DVIO	Average Drift		8			μV/°C
$I_{BN}$	Input Bias Current	Non-Inverting	$\pm 1$	$\pm 10$	$\pm 15$	μA
DIBN	Average Drift		4			nA/°C

## Electrical Characteristics (Continued)

Unless specified,  $A_V = +2$ ,  $R_F = 300\Omega$ ;  $V_{CC} = \pm 5V$ ,  $R_L = 100\Omega$ , LMH6714/6720/6722. **Boldface** limits apply at temperature extremes.

Symbol	Parameter	Conditions	Min	Typ	Max	Units
$I_{BI}$	Input Bias Current	Inverting		-4	$\pm 12$ <b><math>\pm 20</math></b>	$\mu A$
DIBI	Average Drift			41		$nA/^\circ C$
PSRR	Power Supply Rejection Ratio	DC	48 <b>47</b>	58		dB
CMRR	Common Mode Rejection Ratio	DC	48 <b>45</b>	54		dB
$I_{CC}$	Supply Current	$R_L = \infty$	4.5 <b>3</b>	5.6	7.5 <b>8</b>	$mA$
$I_{CCI}$	Supply Current During Shutdown	LMH6720		500	670	$\mu A$
<b>Miscellaneous Performance</b>						
$R_{IN}$	Input Resistance	Non-Inverting		2		$M\Omega$
$C_{IN}$	Input Capacitance	Non-Inverting		1.0		$pF$
$R_{OUT}$	Output Resistance	Closed Loop		0.06		$\Omega$
$V_O$	Output Voltage Range	$R_L = \infty$	$\pm 3.5$ <b><math>\pm 3.4</math></b>	$\pm 3.9$		$V$
$V_{OL}$		$R_L = 100\Omega$	$\pm 3.6$ <b><math>\pm 3.4</math></b>	$\pm 3.8$		$V$
CMIR	Input Voltage Range	Common Mode		$\pm 2.2$		$V$
$I_O$	Output Current (Note 3)	$V_{IN} = 0V$ , Max Linear Current	50	70		$mA$
OFFMAX	Voltage for Shutdown	LMH6720			<b>0.8</b>	$V$
ONMIN	Voltage for Turn On	LMH6720	<b>2.0</b>			$V$
IIH	Current Turn On	LMH6720, $\overline{SD} = 2.0V$	-20 <b>-30</b>	2	20 <b>30</b>	$\mu A$
IIL	Current Shutdown	LMH6720, $\overline{SD} = .8V$	<b>-600</b>	-400	<b>-100</b>	$\mu A$
IOZ	$R_{OUT}$ Shutdown	LMH6720, $\overline{SD} = .8V$	0.2	1.8		$M\Omega$
$t_{on}$	Turn on Time	LMH6720		11		$ns$
$t_{off}$	Turn off Time	LMH6720		7		$ns$

**Note 1:** Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is intended to be functional, but specific performance is not guaranteed. For guaranteed specifications, see the Electrical Characteristics tables.

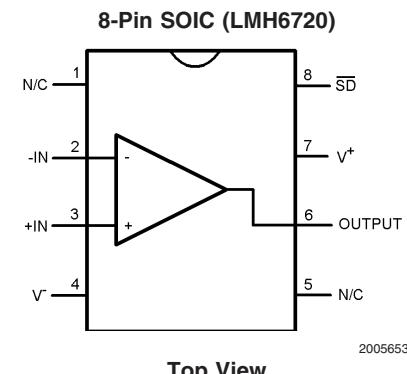
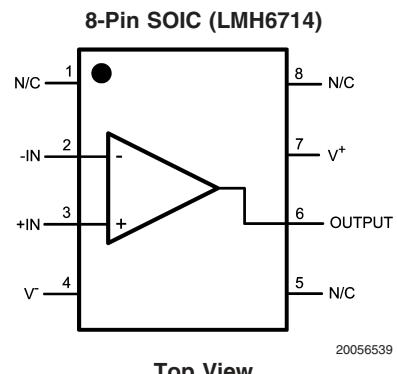
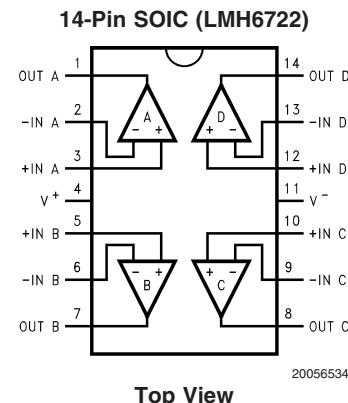
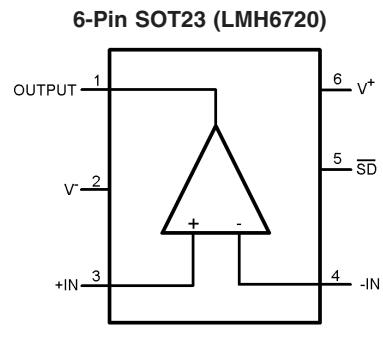
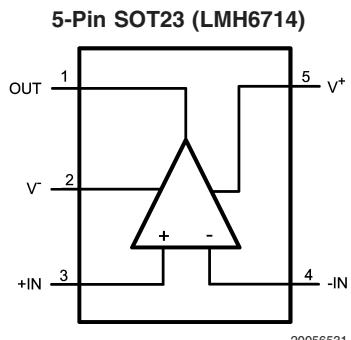
**Note 2:** Electrical Table values apply only for factory testing conditions at the temperature indicated. Factory testing conditions result in very limited self-heating of the device such that  $T_J = T_A$ . No guarantee of parametric performance is indicated in the electrical tables under conditions of internal self heating where  $T_J > T_A$ . See Applications Section for information on temperature derating of this device.<sup>6</sup> Min/Max ratings are based on product characterization and simulation. Individual parameters are tested as noted.

**Note 3:** The maximum output current ( $I_{OUT}$ ) is determined by device power dissipation limitations. See the Power Dissipation section of the Application Division for more details.

**Note 4:** Human body model,  $1.5k\Omega$  in series with  $100pF$ . Machine model,  $0\Omega$  in series with  $200pF$ .

**Note 5:** The shutdown pin is designed to work between 0 and  $V_{CC}$  with split supplies ( $V_{CC} = -V_{EE}$ ). With single supplies ( $V_{EE} = \text{ground}$ ) the shutdown pin should not be taken below  $V_{CC}/2$ .

## Connection Diagrams

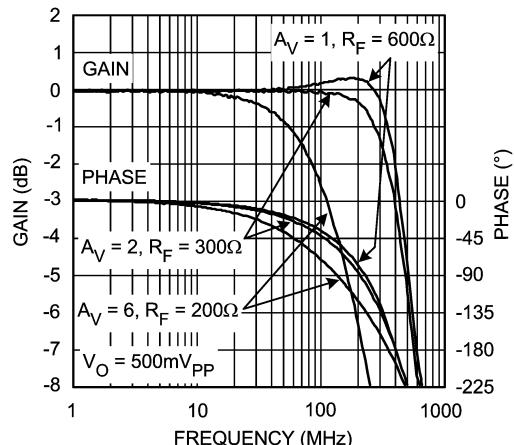


## Ordering Information

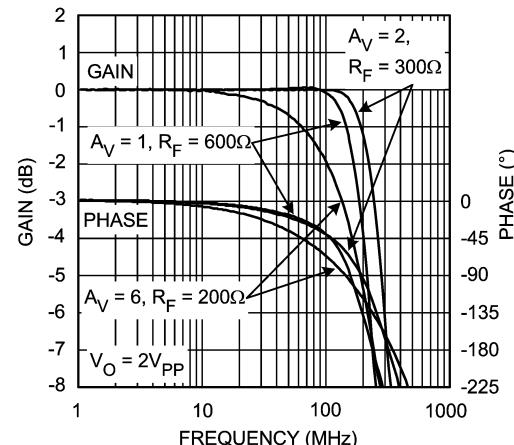
Package	Part Number	Package Marking	Transport Media	NSC Drawing
5-Pin SOT23	LMH6714MF	A95A	1k Units Tape and Reel	MF05A
	LMH6714MFX		3k Units Tape and Reel	
8-Pin SOIC	LMH6714MA	LMH6714MA	95 Units/Rail	M08A
	LMH6714MAX		2.5k Units Tape and Reel	
6-Pin SOT23	LMH6720MF	A96A	1k Units Tape and Reel	MF06A
	LMH6720MFX		3k Units Tape and Reel	
8-Pin SOIC	LMH6720MA	LMH6720MA	95 Units/Rail	M08A
	LMH6720MAX		2.5k Units Tape and Reel	
14-Pin SOIC	LMH6722MA	LMH6722MA	55 Units/Rail	M14A
	LMH6722MAX		2.5 Units Tape and Reel	

## Typical Performance Characteristics ( $A_V = 2$ , $R_F = 300\Omega$ , $R_L = 100\Omega$ Unless Specified).

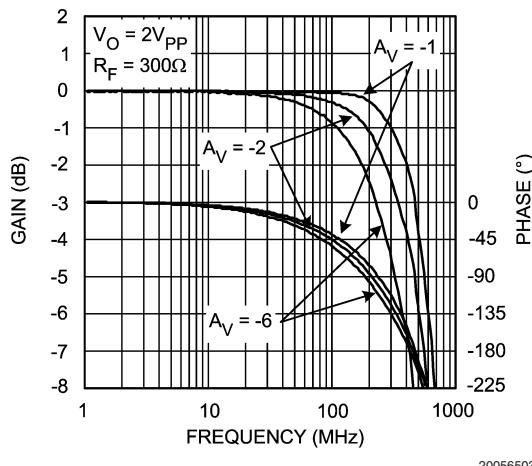
### Non-Inverting Small Signal Frequency Response



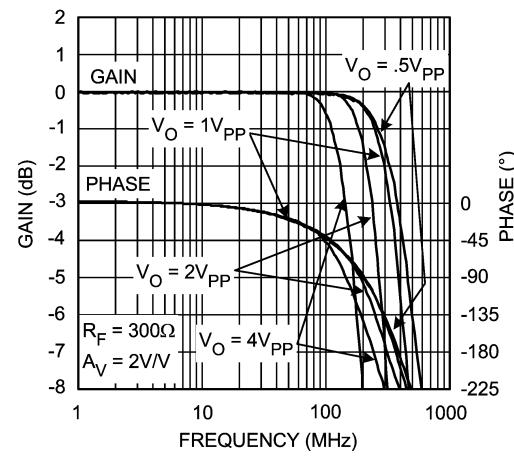
### Non-Inverting Large Signal Frequency Response



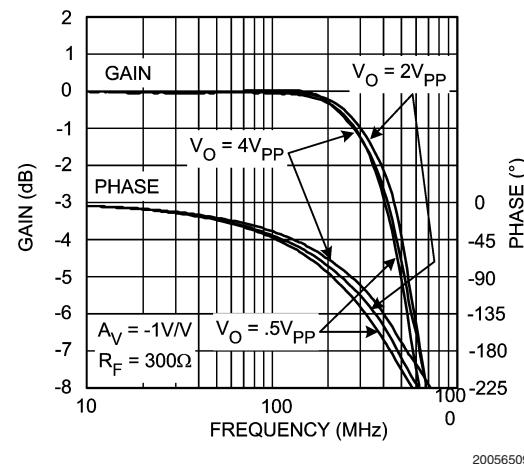
### Inverting Frequency Response



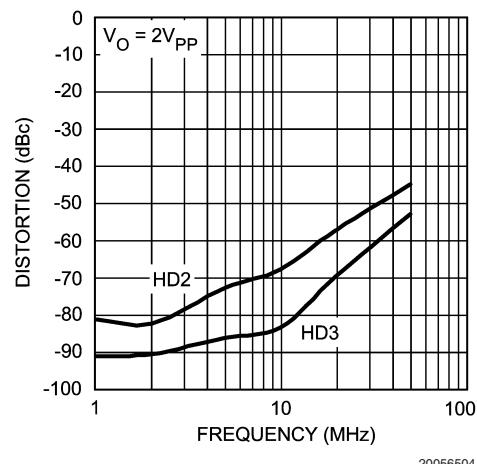
### Non-Inverting Frequency Response vs. $V_O$



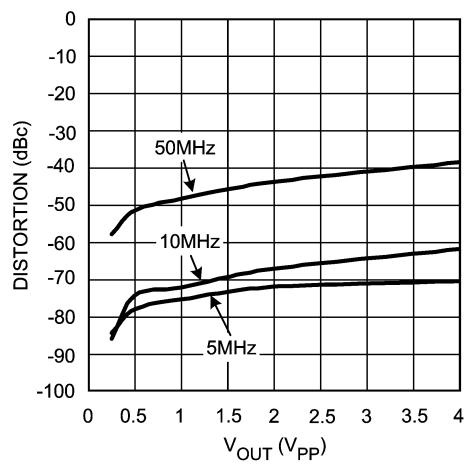
### Inverting Frequency Response vs. $V_O$



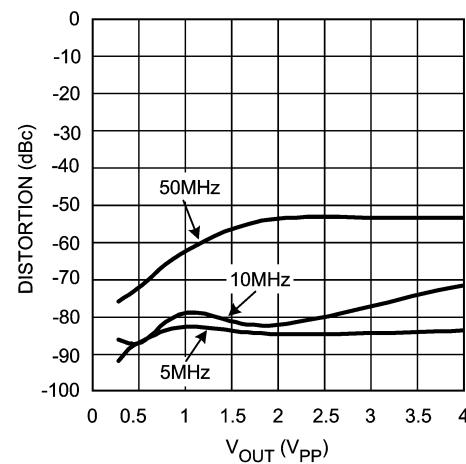
### Harmonic Distortion vs. Frequency



**Typical Performance Characteristics** ( $A_V = 2$ ,  $R_F = 300\Omega$ ,  $R_L = 100\Omega$  Unless Specified). (Continued)

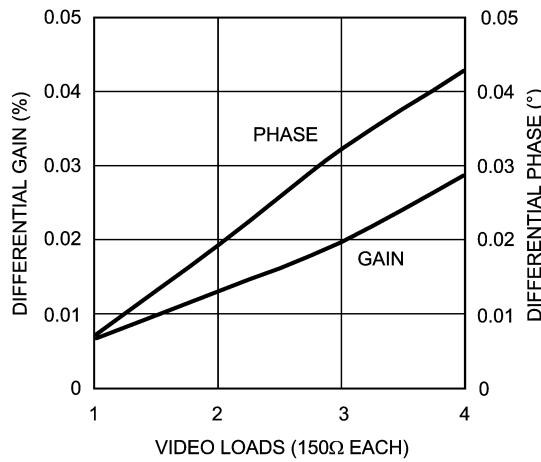
2nd Harmonic Distortion vs.  $V_{OUT}$ 

20056502

3rd Harmonic Distortion vs.  $V_{OUT}$ 

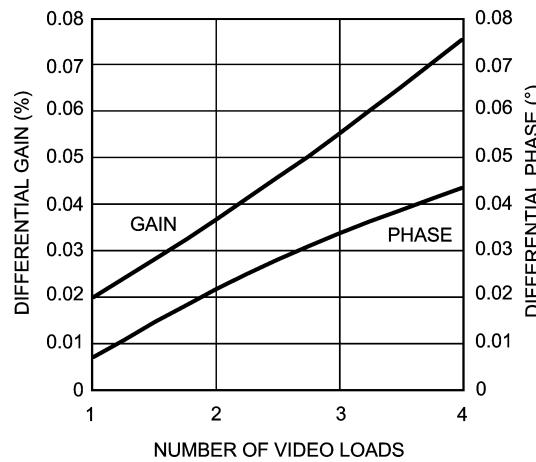
20056501

DG/DP (LMH6714)



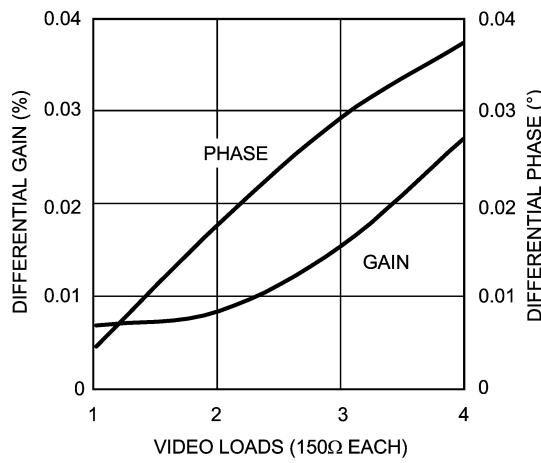
20056528

DG/DP (LMH6720)



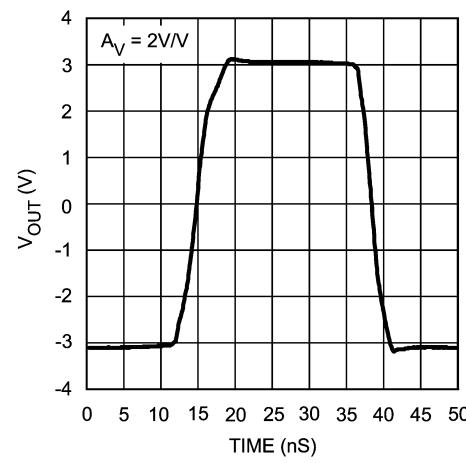
20056505

DG/DP (LMH6722)



20056535

Large Signal Pulse Response

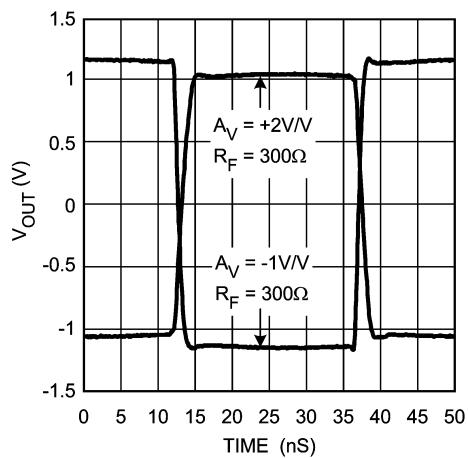


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## Typical Performance Characteristics

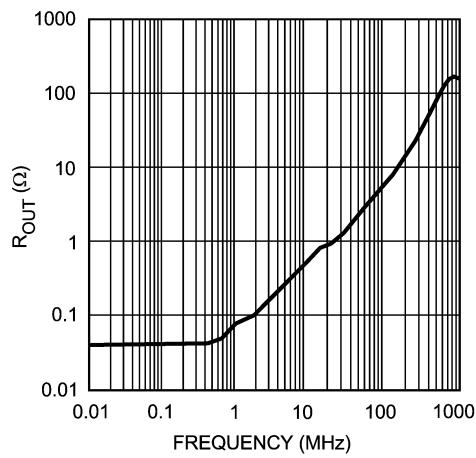
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Small Signal Pulse Response



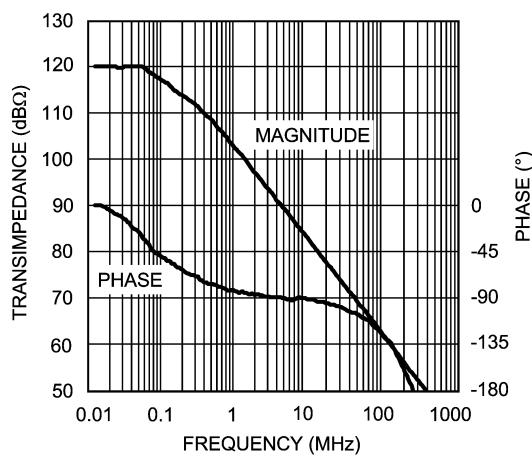
20056510

Closed Loop Output Resistance



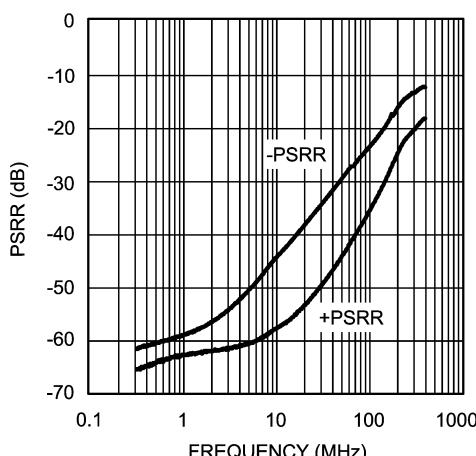
20056511

Open Loop Transimpedance Z(s)



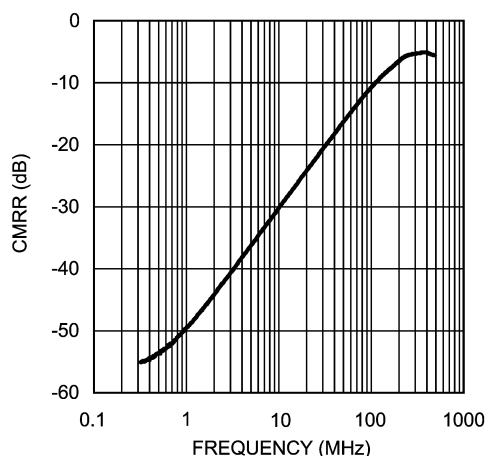
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PSRR vs. Frequency

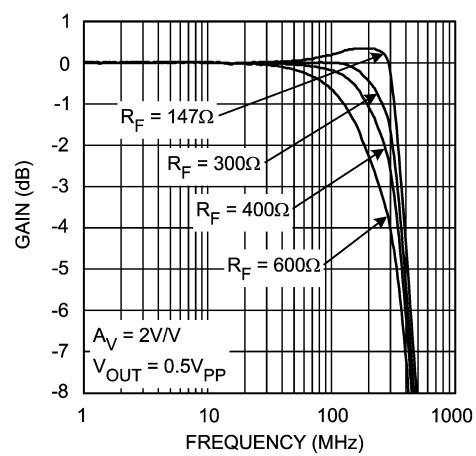


20056516

CMRR vs. Frequency

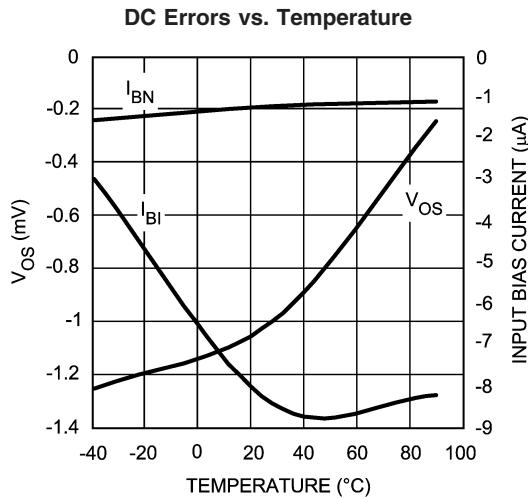


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Frequency Response vs.  $R_F$ 

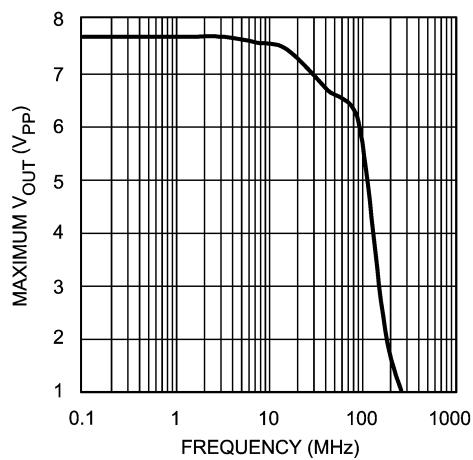
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**Typical Performance Characteristics** ( $A_V = 2$ ,  $R_F = 300\Omega$ ,  $R_L = 100\Omega$  Unless Specified). (Continued)



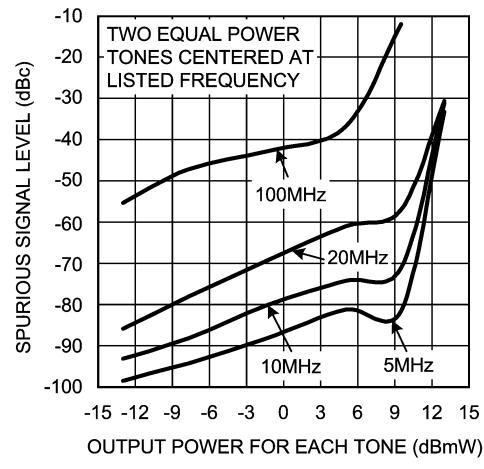
20056518

**Maximum  $V_{OUT}$  vs. Frequency**



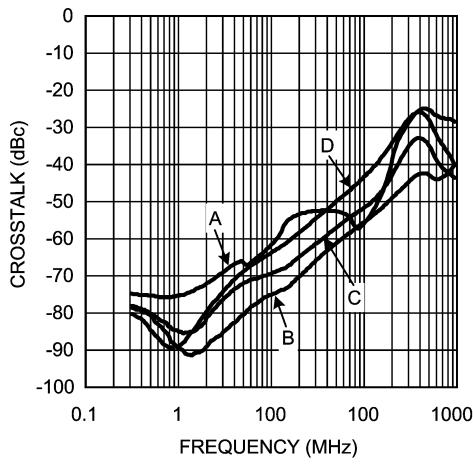
20056526

**3rd Order Intermodulation vs. Output Power**



20056527

**Crosstalk vs. Frequency (LMH6722)**  
for each channel with all others active



20056536

## Application Section

### FEEDBACK RESISTOR SELECTION

One of the key benefits of a current feedback operational amplifier is the ability to maintain optimum frequency response independent of gain by using appropriate values for the feedback resistor ( $R_F$ ). The Electrical Characteristics and Typical Performance plots specify an  $R_F$  of  $300\Omega$ , a gain of  $\pm 2V/V$  and  $\pm 5V$  power supplies (unless otherwise specified). Generally, lowering  $R_F$  from its recommended value will peak the frequency response and extend the bandwidth while increasing the value of  $R_F$  will cause the frequency response to roll off faster. Reducing the value of  $R_F$  too far below its recommended value will cause overshoot, ringing and, eventually, oscillation.

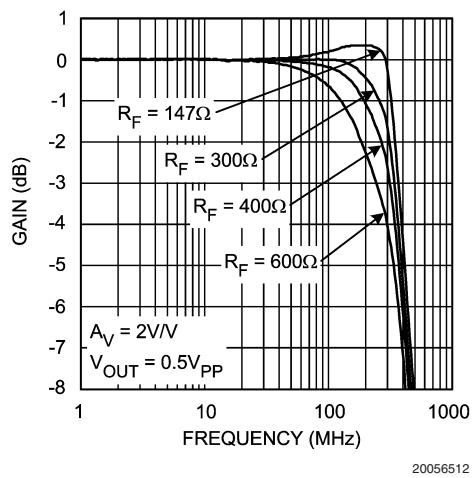


FIGURE 1. Frequency Response vs.  $R_F$

The plot labeled "Frequency Response vs.  $R_F$ " shows the LMH6714/6720/6722's frequency response as  $R_F$  is varied ( $R_L = 100\Omega$ ,  $A_V = +2$ ). This plot shows that an  $R_F$  of  $147\Omega$  results in peaking. An  $R_F$  of  $300\Omega$  gives near maximal bandwidth and gain flatness with good stability. An  $R_F$  of  $400\Omega$  gives excellent stability with only a small bandwidth penalty. Since all applications are slightly different it is worth some experimentation to find the optimal  $R_F$  for a given circuit. Note that it is not possible to use a current feedback amplifier with the output shorted directly to the inverting input. The buffer configuration of the LMH6714/6720/6722 requires a  $600\Omega$  feedback resistor for stable operation.

For more information see Application Note OA-13 which describes the relationship between  $R_F$  and closed-loop frequency response for current feedback operational amplifiers. The value for the inverting input impedance for the LMH6714/6720/6722 is approximately  $180\Omega$ . The LMH6714/6720/6722 is designed for optimum performance at gains of  $+1$  to  $+6 V/V$  and  $-1$  to  $-5V/V$ . When using gains of  $\pm 7V/V$  or more the low values of  $R_G$  required will make inverting input impedances very low.

When configuring the LMH6714/6720/6722 for gains other than  $\pm 2V/V$ , it is usually necessary to adjust the value of the feedback resistor. The two plots labeled "R<sub>F</sub> vs. Non-inverting Gain" and "R<sub>F</sub> vs. Inverting Gain" provide recommended feedback resistor values for a number of gain selections.

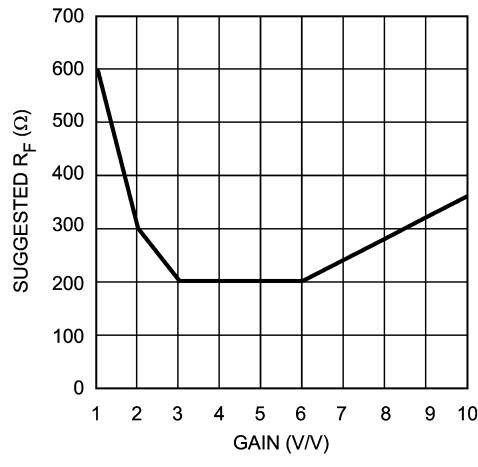


FIGURE 2.  $R_F$  vs. Non-Inverting Gain

In the "R<sub>F</sub> vs. Non-Inverting Gain" and the "R<sub>F</sub> vs. Inverting Gain" charts the recommended value of R<sub>F</sub> is depicted by the solid line, which starts high, decreases to  $200\Omega$  and begins increasing again. The reason that a higher R<sub>F</sub> is required at higher gains is the need to keep R<sub>G</sub> from decreasing too far below the output impedance of the input buffer. For the LMH6714/6720/6722 the output resistance of the input buffer is approximately  $180\Omega$  and  $50\Omega$  is a practical lower limit for R<sub>G</sub>. Due to the limitations on R<sub>G</sub> the LMH6714/6720/6722 begins to operate in a gain bandwidth limited fashion for gains of  $\pm 5V/V$  or greater.

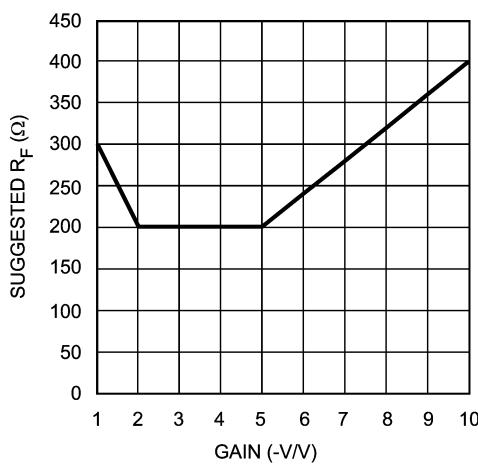


FIGURE 3.  $R_F$  vs. Inverting Gain

### ACTIVE FILTERS

When using any current feedback Operational Amplifier as an active filter it is important to be very careful when using reactive components in the feedback loop. Anything that reduces the impedance of the negative feedback, especially at higher frequencies, will almost certainly cause stability problems. Likewise capacitance on the inverting input needs

## Application Section (Continued)

to be avoided. See Application Notes OA-7 and OA-26 for more information on Active Filter applications for Current Feedback Op Amps.

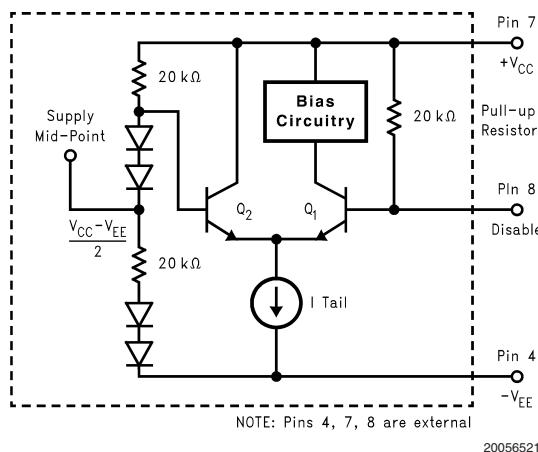


FIGURE 4. Enable/Disable Operation

### ENABLE/DISABLE OPERATION USING $\pm 5$ V SUPPLIES (LMH6720 ONLY)

The LMH6720 has a TTL logic compatible disable function. Apply a logic low ( $< 2.0V$ ) to the DS pin and the LMH6720 is disabled. Apply a logic high ( $> 2.0V$ ), or let the pin float and the LMH6720 is enabled. Voltage, not current, at the Disable pin determines the enable/disable state. Care must be exercised to prevent the disable pin voltage from going more than .8V below the midpoint of the supply voltages (0V with split supplies,  $V_{CC}/2$  with single supplies) doing so could cause transistor Q1 to Zener resulting in damage to the disable circuit. The core amplifier is unaffected by this, but disable operation could become slower as a result.

Disabled, the LMH6720 inputs and output become high impedances. While disabled the LMH6720 quiescent current is approximately  $500\mu A$ . Because of the pull up resistor on the disable circuit the  $I_{CC}$  and  $I_{EE}$  currents are not balanced in the disabled state. The positive supply current ( $I_{CC}$ ) is approximately  $500\mu A$  while the negative supply current ( $I_{EE}$ ) is only  $200\mu A$ . The remaining  $I_{EE}$  current of  $300\mu A$  flows through the disable pin.

The disable function can be used to create analog switches or multiplexers. Implement a single analog switch with one LMH6720 positioned between an input and output. Create an analog multiplexer with several LMH6720's. The LMH6720 is at its best at a gain of 1 for multiplexer applications because there is no  $R_G$  to shunt signals to ground.

### DISABLE LIMITATIONS (LMH6720 ONLY)

The feedback Resistor ( $R_F$ ) limits off isolation in inverting gain configurations. During shutdown the impedance of the LMH6720 inputs and output become very high ( $> 1M\Omega$ ), however  $R_F$  and  $R_G$  are the dominant factor for effective output impedance.

Do not apply voltages greater than  $+V_{CC}$  or less than 0V ( $V_{CC}/2$  single supply) to the disable pin. The input ESD diodes will also conduct if the signal leakage through the feedback resistors brings the inverting input near either supply rail.

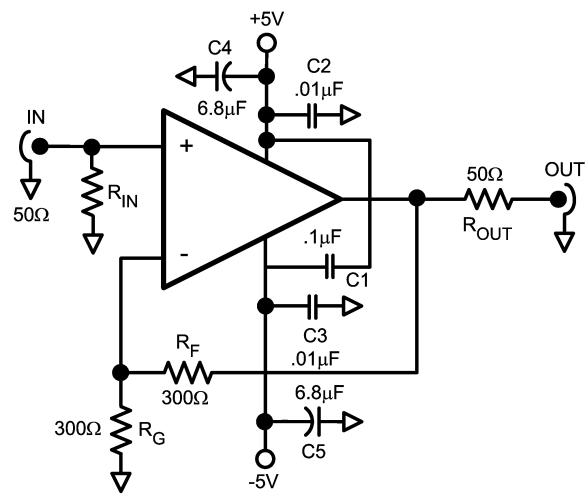


FIGURE 5. Typical Application with Suggested Supply Bypassing

### LAYOUT CONSIDERATIONS

Whenever questions about layout arise, use the evaluation board as a guide. The following Evaluation boards are available with sample parts:

LMH6714	SOT	CLC730216
	SOIC	CLC730227
LMH6720	SOT	CLC730216
	SOIC	CLC730227
LMH6722	SOIC	CLC730231

To reduce parasitic capacitances, the ground plane should be removed near the input and output pins. To reduce series inductance, trace lengths of components in the feedback loop should be minimized. For long signal paths controlled impedance lines should be used, along with impedance matching at both ends.

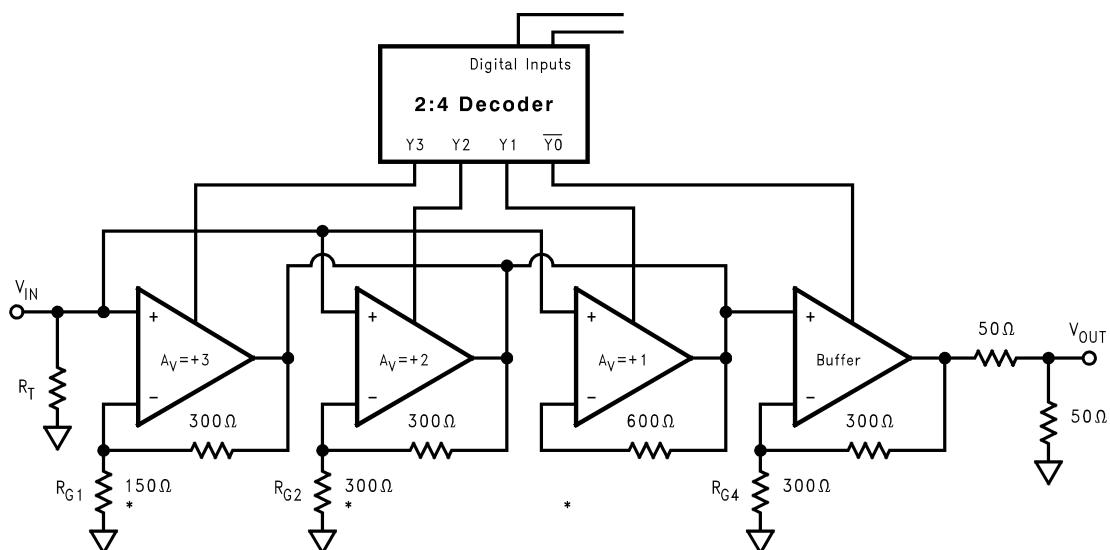
Bypass capacitors should be placed as close to the device as possible. Bypass capacitors from each rail to ground are applied in pairs. The larger electrolytic bypass capacitors can be located anywhere on the board, the smaller ceramic capacitors should be placed as close to the device as possible. In addition Figure 2 shows a capacitor (C1) across the supplies with no connection to ground. This capacitor is optional, however it is required for best 2nd Harmonic suppression. If this capacitor is omitted C2 and C3 should be increased to  $.1\mu F$  each.

### VIDEO PERFORMANCE

The LMH6714/6720/6722 has been designed to provide excellent performance with both PAL and NTSC composite video signals. Performance degrades as the loading is increased, therefore best performance will be obtained with back terminated loads. The back termination reduces reflections from the transmission line and effectively masks capacitance from the amplifier output stage. While all parts offer excellent video performance the LMH6714 and LMH6722 are slightly better than the LMH6720.

## Application Section (Continued)

### WIDE BAND DIGITAL PROGRAMMABLE GAIN AMPLIFIER (LMH6720 ONLY)



\*NOTE: Selectable gains can be changed by using different  $R_g$  resistors.

20056519

FIGURE 6. Wideband Digitally Controlled Programmable Gain Amplifier

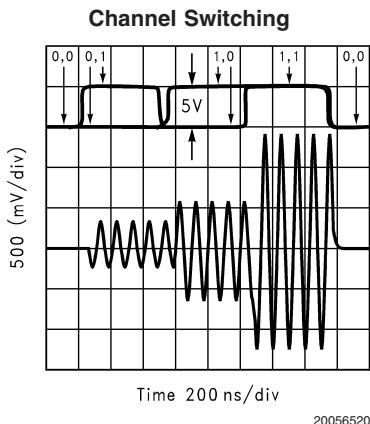


FIGURE 7. PGA Output

As shown in *Figure 6* and *Figure 7* the LMH6720 can be used to construct a digitally controlled programmable gain amplifier. Each amplifier is configured to provide a digitally selectable gain. To provide for accurate gain settings, 1% or better tolerance is recommended on the feedback and gain resistors. The gain provided by each digital code is arbitrary through selection of the feedback and gain resistor values.

#### AMPLITUDE EQUALIZATION

Sending signals over coaxial cable greater than 50 meters in length will attenuate high frequency signal components much more than lower frequency components. An equalizer can be made to pre emphasize the higher frequency components so that the final signal has less distortion. This process can be done at either end of the cable. The circuit in *Figure 8* shows a receiver with some additional components

in the feedback loop to equalize the incoming signal. The RC networks peak the signal at higher frequencies. This peaking is a piecewise linear approximation of the inverse of the frequency response of the coaxial cable. *Figure 9* shows the effect of this equalization on a digital signal that has passed through 150 meters of coaxial cable. *Figure 10* shows a Bode plot of the frequency response of the circuit in *Figure 8* along with equations needed to design the pole and zero frequencies. *Figure 11* shows a network analyzer plot of an LMH6714/6720/6722 with the following component values:

$$R_G = 309\Omega$$

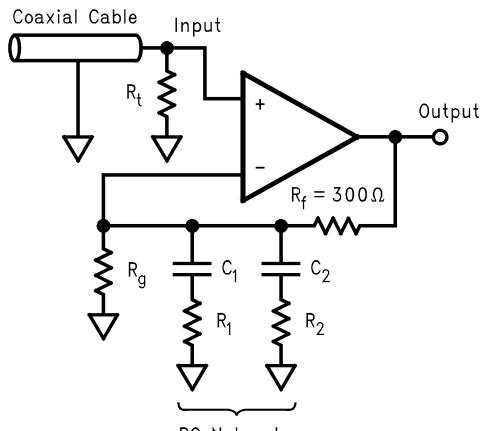
$$R1 = 450\Omega$$

$$C1 = 470\text{pF}$$

$$R2 = 91\Omega$$

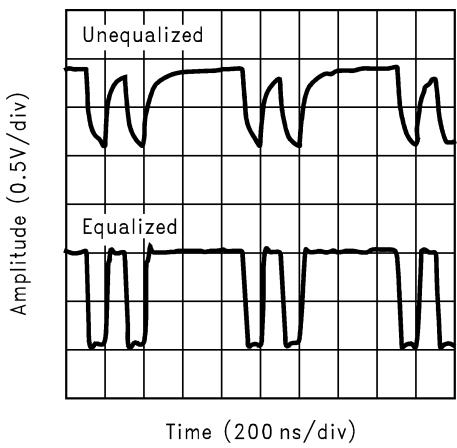
$$C2 = 68\text{pF}$$

## Application Section (Continued)



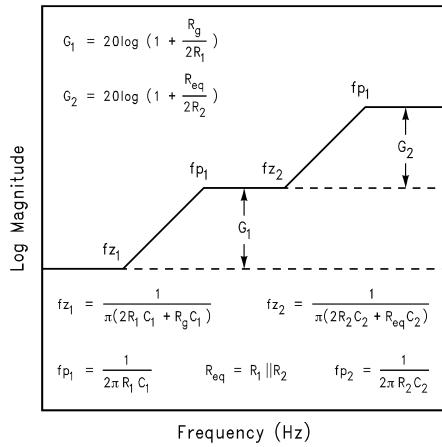
20056522

FIGURE 8.



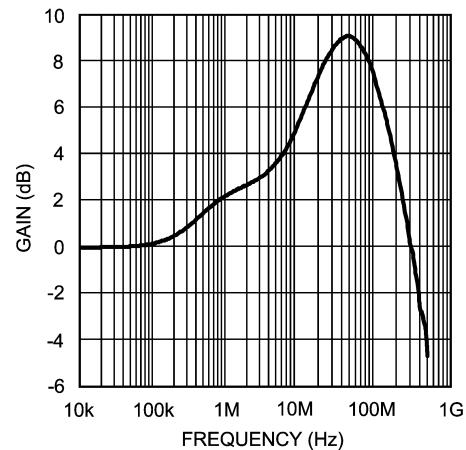
20056529

FIGURE 9. Digital Signal without and with Equalization



20056530

FIGURE 10. Design Equations



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FIGURE 11. Equalizer Frequency Response

## POWER DISSIPATION

Follow these steps to determine the Maximum power dissipation for the LMH6714/6720/6722:

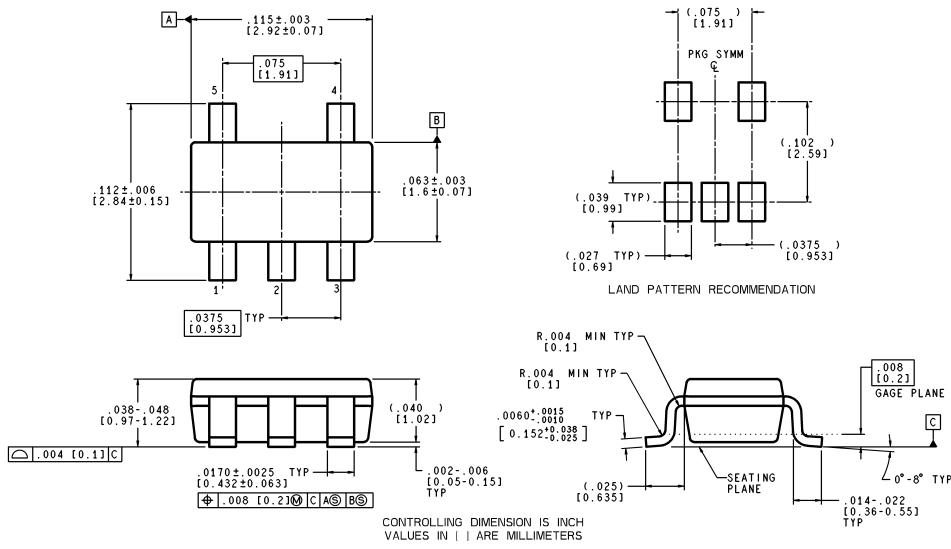
1. Calculate the quiescent (no load) power:  $P_{AMP} = I_{CC} (V_{CC} - V_{EE})$
2. Calculate the RMS power at the output stage:  $P_{OUT} (\text{RMS}) = ((V_{CC} - V_{OUT} (\text{RMS})) * I_{OUT} (\text{RMS}))$ , where  $V_{OUT}$  and  $I_{OUT}$  are the voltage and current across the external load.
3. Calculate the total RMS power:  $P_T = P_{AMP} + P_{OUT}$

The maximum power that the LMH6714/6720/6722, package can dissipate at a given temperature can be derived with the following equation:

$P_{MAX} = (150^\circ - T_A) / \theta_{JA}$ , where  $T_A$  = Ambient temperature ( $^\circ\text{C}$ ) and  $\theta_{JA}$  = Thermal resistance, from junction to ambient, for a given package ( $^\circ\text{C/W}$ ). For the SOIC package  $\theta_{JA}$  is  $148^\circ\text{C/W}$ , for the SOT it is  $250^\circ\text{C/W}$ .

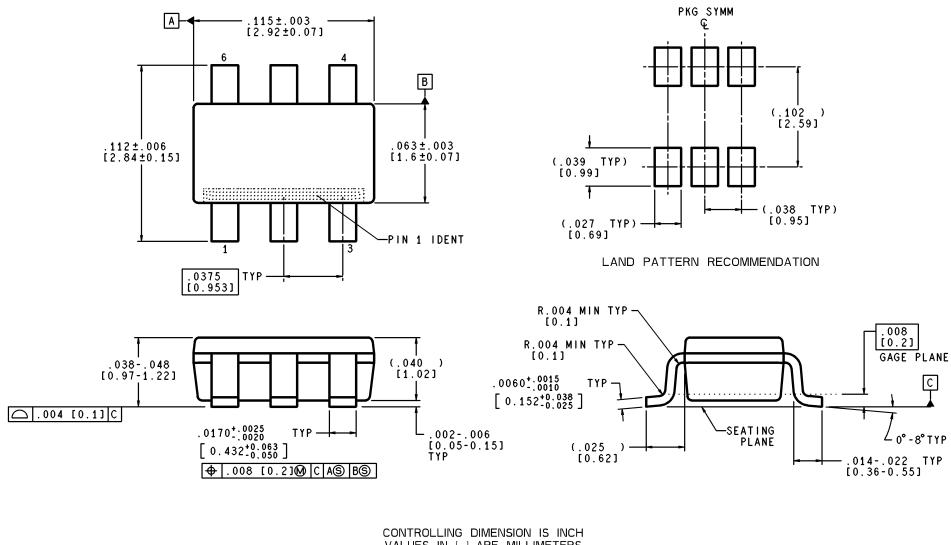
## Physical Dimensions inches (millimeters)

unless otherwise noted



MF05A (Rev A)

**5-Pin SOT23**  
NS Product Number MF05A

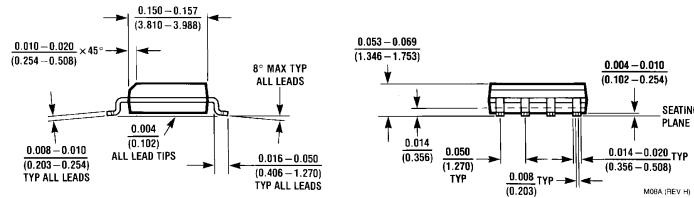
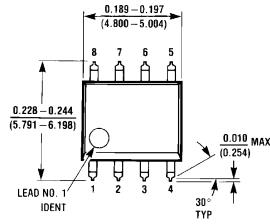


MF06A (Rev A)

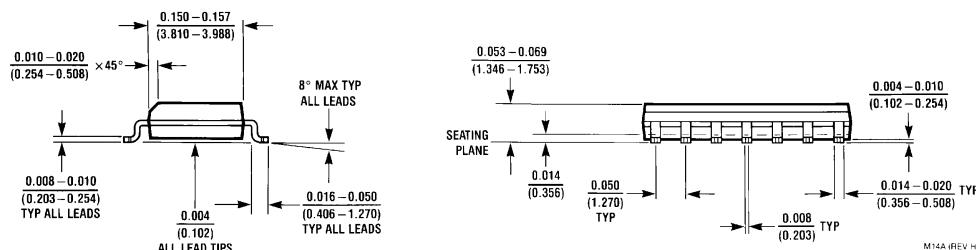
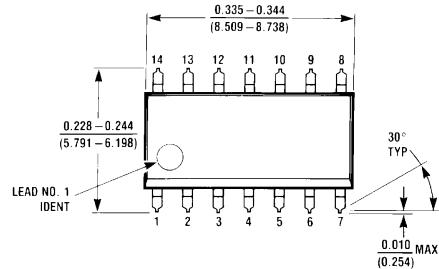
**6-Pin SOT23**  
NS Product Number MF06A

## Physical Dimensions

inches (millimeters) unless otherwise noted (Continued)



**8-Pin SOIC**  
NS Product Number M08A



**14-Pin SOIC**  
NS Product Number M14A

## **Notes**

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