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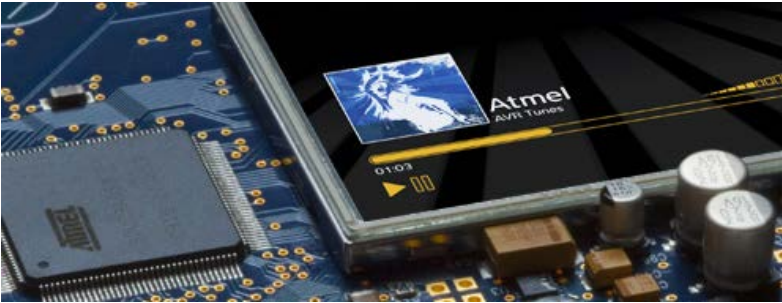
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JTAGICE 3

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Mid-range development tool for Atmel 8-bit and 32-bit AVR microcontrollers with on-chip debugging for source level symbolic debugging, NanoTrace (if supported by the device) and device programming.

Key Features

- Supports JTAG, aWire, SPI and PDI interfaces
- 3 hardware program breakpoints and 1 maskable data breakpoint (depending on the OCD module on the AVR device)
- Symbolic debug of complex data types including scope information
- Up to 128 software breakpoints
- 1.8V to 5.5V target operation
- Uploads 256KB code in ~14 seconds (XMEGA using JTAG interface)
- USB powered

Ordering Code:ATJTAGICE3

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Software Description

AVR BSDL files
(126 KB, updated 10/09)
Zip archive with BSDL files for Atmel AVR devices.



megaAVR BSDL files
(77 KB, updated 10/09)
Zip archive with BSDL files for Atmel megaAVR devices.



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