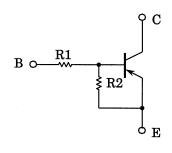
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process)

## RN2221,RN2222,RN2223 RN2224,RN2225,RN2226,RN2227

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- High current type  $(I_{C(MAX)} = -800 \text{mA})$
- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- Low VCE (sat)
- Complementary to RN1221~RN1227

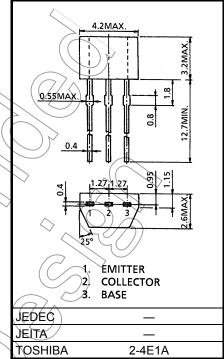
## **Equivalent Circuit**



## **Bias Resistor Values**

Type No.	R1 (kΩ)	R2 (kΩ)
RN2221	1	1
RN2222	2.2	2.2
RN2223	4.7	4.7
RN2224	10	10
RN2225	0.47	10
RN2226 (	1)	10
RN2227	2.2	10

#### Unit: mm



Weight: 0.13g (typ.)

## Absolute Maximum Ratings (Ta = 25°C)

Characteristi	Symbol	Rating	Unit		
Collector-base voltage	/RN <del>22</del> 21~2227	<b>У</b> СВО	_50	V	
Collector-emitter voltage	TANZZZI ZZZI	VCEO	-50	V	
	RN2221~2224		-10		
Emitter-base voltage	RN2225, 2226	VEBO	-5	V	
	RN2227 />	~	-6		
Collector current	4	IC	-800	mA	
Collector power dissipation	RN2221~2227	> P <sub>C</sub>	300	mW	
Junction temperature	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

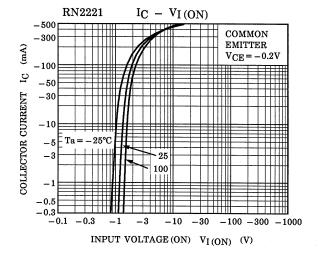
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

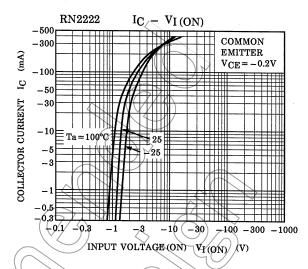


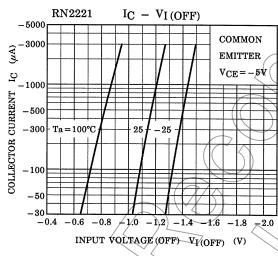
# Electrical Characteristics (Ta = 25°C)

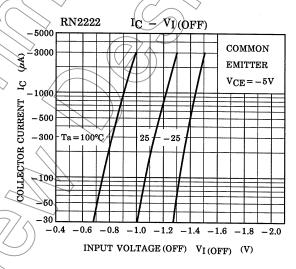
Characteristic		Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	RN2221~2227	1	_	$V_{CB} = -50V$ , $I_E = 0$	_	_	-100	nA
Collector cut-on current	TANZZZ I ZZZZI	_	_	$V_{CE} = -50V, I_B = 0$	_	_	-500	ш
	RN2221		_	4	-3.85	_	-7.14	
	RN2222		_	\\	-1.75	_	-3.25	
	RN2223		_	$V_{EB} = -10V, I_{E} = 0$	70.82	\ <u></u>	-1.52	
Emitter cut-off current	RN2224	I <sub>EBO</sub>	_		-0.38	) —	-0.71	mA
	RN2225		_	V = 5V   = 0	-0.365	_	-0.682	
	RN2226		_	$V_{EB} = -5V, I_{C} = 0$	-0,35	_	-0.65	
	RN2227		_	$V_{EB} = -6V, I_C = 0$	-0.378	_	-0.703	
	RN2221		_		60	_	_	
	RN2222		_		65		_	
	RN2223		_	$\mathcal{A}(\mathcal{A})$	70	(F)	\_	
DC current gain	RN2224	h <sub>FE</sub>	_	V <sub>CE</sub> = -1V, I <sub>C</sub> = -100mA	90 🕜	1	_	_
	RN2225		_		90	7-/	· _	
	RN2226		_		90	1/	) —	
	RN2227		_		90	J()	_	
Collector-emitter	RN2221	M	(	I <sub>C</sub> = -50mA, I <sub>B</sub> = -2mA	7	$\supset$	0.05	V
saturation voltage	RN2222~2227	V <sub>CE</sub> (sat)		I <sub>C</sub> = -50mA, I <sub>B</sub> = -1mA	$(\mathcal{I})$	_	-0.25	V
	RN2221				-1.0	_	-3.5	
	RN2222		76	$\Diamond$ $(\vee)$	1.4	_	-4.5	
	RN2223	$\mathcal{A}($	7/		-2.0	_	-6.5	
Input voltage (ON)	RN2224	V <sub>I (ON)</sub>	7	V <sub>CE</sub> = 70.2V I <sub>C</sub> = -100mA	-3.0	_	-12.0	V
	RN2225		\ <u>~</u>	10 1001111	-0.6	_	-2.0	
	RN2226		<i>)</i> —		-0.7	_	-2.5	
	RN2227	$\mathcal{C}$	_		-1.0	_	-3.0	
	RN2221~2224		_		-0.8	_	-1.3	
Input voltage (OFF)	RN2225, 2226	VI (OFF)	_	V <sub>CE</sub> = -5V, I <sub>C</sub> = -0.1mA	-0.4	_	-0.8	V
	RN2227	( ) ) ` '		10 - Q. (IIIA	-0.5	_	-1.0	
Translation frequency	RN2221~2227	ft	+()	V <sub>CE</sub> = -5V, I <sub>C</sub> = -20mA	_	200	_	MHz
Collector output capacitance	RN2221~2227	C <sub>ob</sub>		V <sub>CB</sub> = -10V, I <sub>E</sub> = 0 f = 1MHz	_	13	_	pF
	RN2221		11	>	0.7	1.0	1.3	
$\wedge \wedge$	RN2222		_		1.54	2.2	2.86	
	RN2223		$\searrow$		3.29	4.7	6.11	
Input resistor	RN2224	(R1	_	_	7	10	13	kΩ
	RN2225	(1)	_		0.329	0.47	0.61	
	RN2226		_		0.7	1.0	1.3	
	RN2227		_		1.54	2.2	2.86	
	RN2221~2224		_		0.9	1.0	1.1	
Decistor votic	RN2225	D4/D0	_	1	0.0423	0.047	0.0517	
Resistor ratio	RN2226	> R1/R2	_	_	0.09	0.1	0.11	_
	RN2227		_		0.2	0.22	0.24	

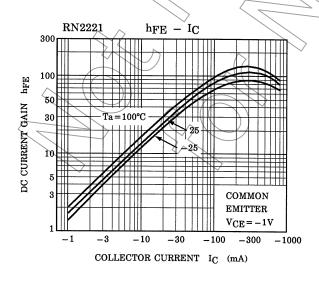
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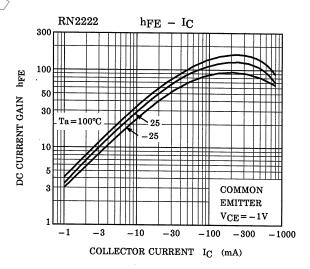


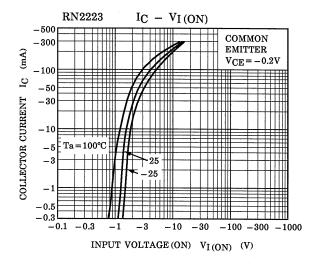


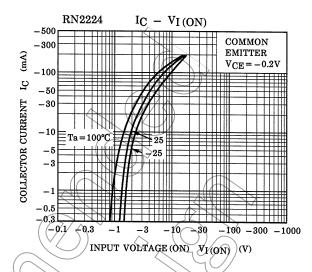


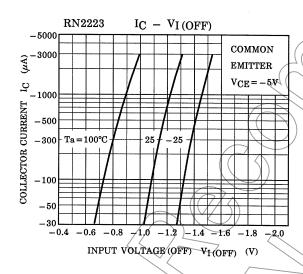


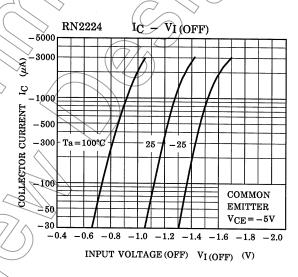


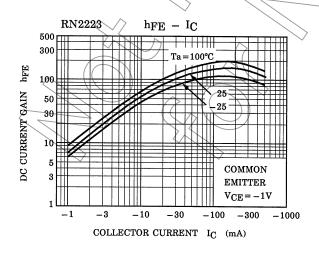


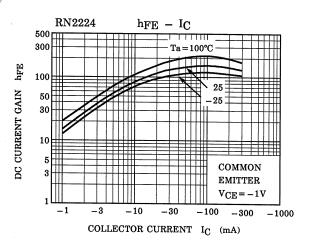


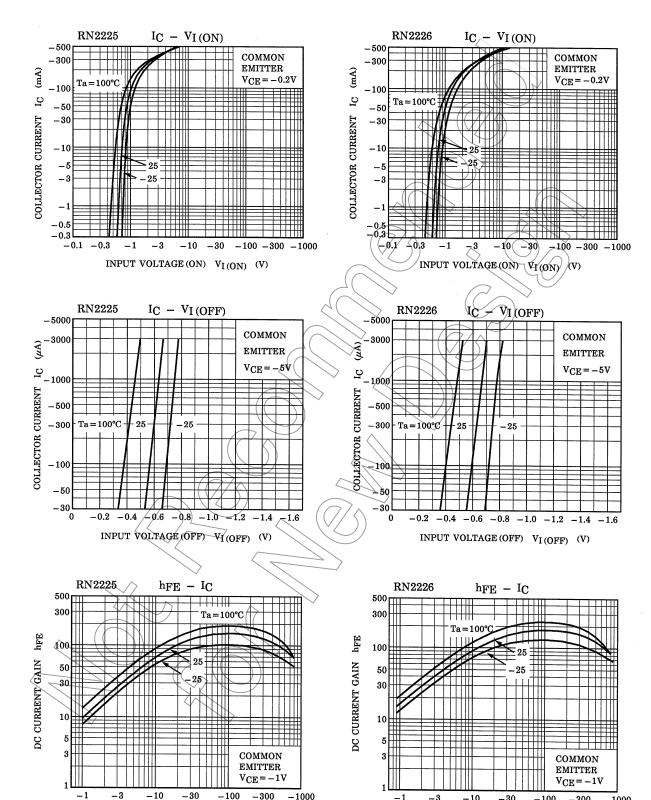












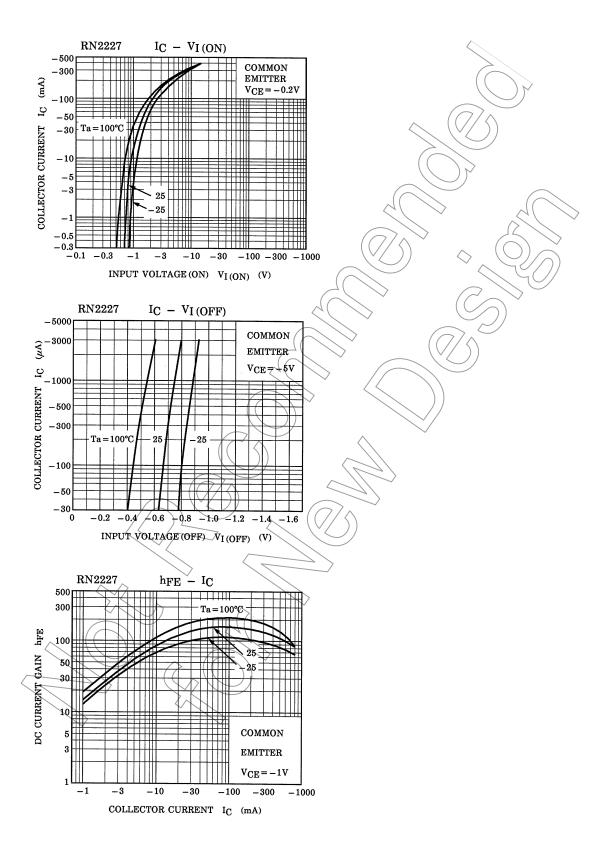
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