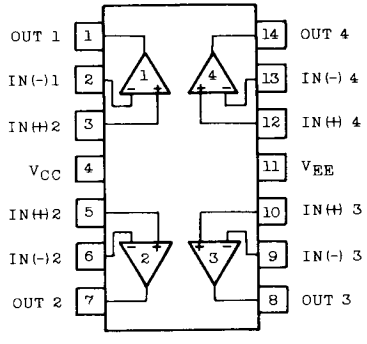
TA75064P/F

J-FET INPUT LOW-POWER
QUAD OPERATIONAL AMPLIFIER

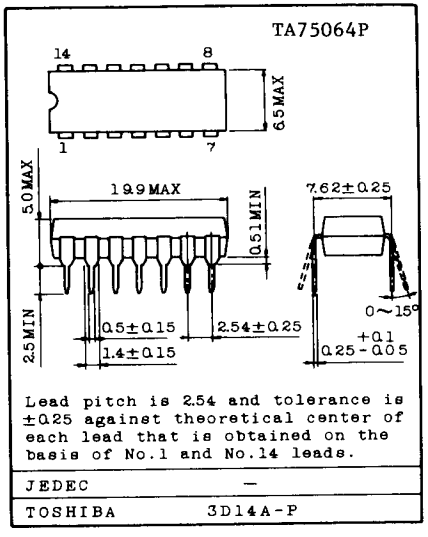
The TA75064P and TA75064F are J-FET input low-power operational amplifiers with low input bias, offset current and a fast slew rate.
The TA75064P is pin compatible with the TA75902P and 324. The TA75064F is mini-flat package.
The TA75064P series is an excellent choice for active filters, integrators, buffers and sample-and-hold circuits.

- Low Supply Current : 1.0mA MAX.
- High Input Impedance
- Low Input Bias Current : 400pA MAX.
- Low Input Offset Current : 200pF MAX.
- High Slew Rate : 3.5V/μsec
- Internal Frequency Compensation
- Output Short Circuit Protection

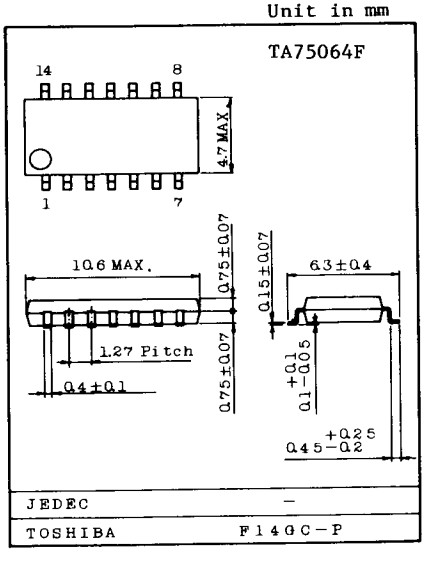
PIN CONNECTION (TOP VIEW)



Unit in mm



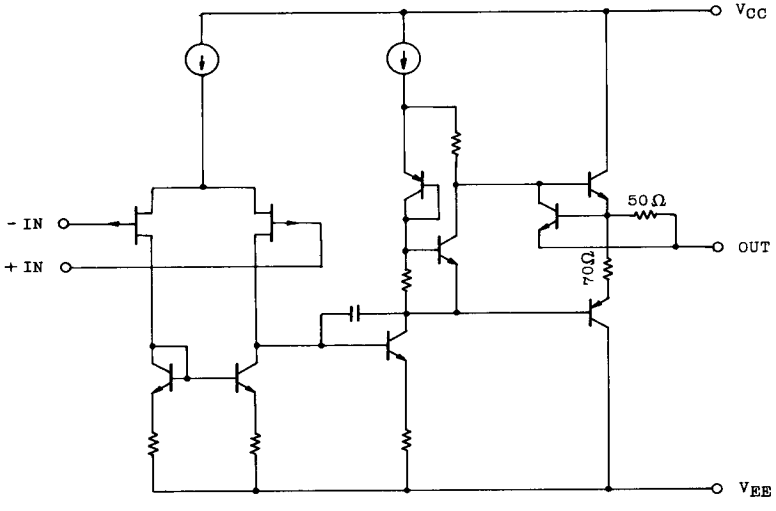
Unit in mm



MAXIMUM RATINGS (Ta=25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
Supply Voltage		V_{CC}	+18	V
		V_{EE}	-18	
Differential Input Voltage		DV_{IN}	±30	V
Input Voltage		V_{IN}	±15	V
Power Dissipation	TA75064P	P_D	625	mW
	TA75064F		280	
Operating Temperature		Topr	-40~85	°C
Storage Temperature		Tstg	-55~125	°C

EQUIVALENT CIRCUIT



ELECTRICAL CHARACTERISTICS (V_{CC}=15V, V_{EE}=-15V, Ta=25°C)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Input Offset Voltage	V_{IO}	-	Rg≦10kΩ	-	3	15	mV
TC of Input Offset Voltage	TCV_{IO}	-	-	-	10	-	μV/°C
Input Bias Current	I_I	-	Tj=25°C	-	30	400	pA
Input Offset Current	I_{IO}	-	Tj=25°C	-	5	200	pA
Common Mode Input Voltage	CMV_{IN}	-		±11.5	±12	-	V
Maximum Output Voltage	V_{OM}		R_L=10kΩ	±10	±13.5	-	V
Input Resistance	R_{IN}	-	-	-	10^{12}	-	Ω
Voltage Gain (Open Loop)	G_V	-	V_{OUT}=±10V, R_L=10kΩ	3	6	-	V/mV
Common Mode Input Signal Rejection Ratio	CMRR	-	Rg≦10kΩ	70	76	-	dB
Supply Voltage Rejection Ratio	SVRR	-	Rg≦10kΩ	70	76	-	dB
Slew Rate	SR	-	G_V=1, R_L=10kΩ	-	3.5	-	V/μs
Unity Gain Cross Frequency	f_T	-	Open Loop	-	1	-	MHz
Supply Current	I_{CC}, I_{EE}	-	-	-	800	1000	μA
Equivalent Input Noise Voltage	V_{NI}	-	R_S=100Ω, f=1kHz	-	42	-	nV/$\sqrt{Hz}$