

# 74ABT125

Quad buffer; 3-state

Rev. 10.1 — 18 January 2024

Product data sheet

## 1. General description

The 74ABT125 is a quad buffer/line driver with 3-state outputs controlled by the output enable inputs ( $\overline{nOE}$ ). A HIGH on  $\overline{nOE}$  causes the outputs to assume a high impedance OFF-state. This device is fully specified for partial power down applications using  $I_{OFF}$ . The  $I_{OFF}$  circuitry disables the output, preventing the potentially damaging backflow current through the device when it is powered down.

## 2. Features and benefits

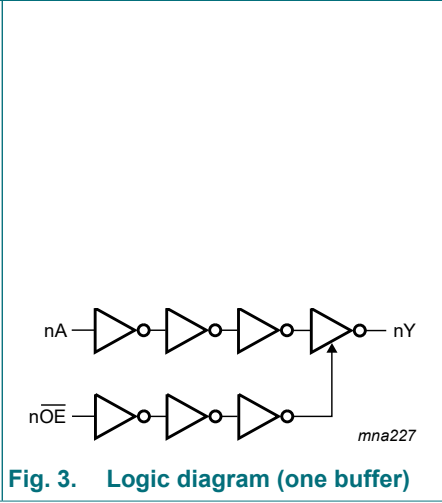
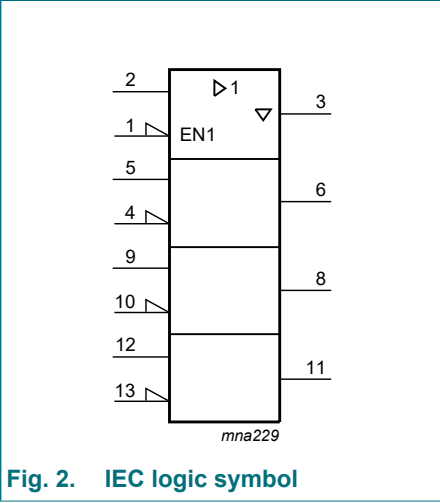
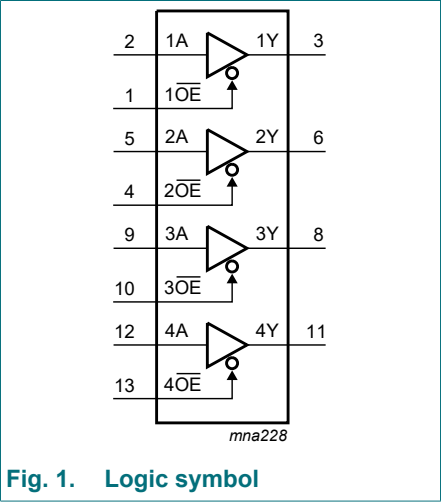
- Supply voltage range from 4.5 V to 5.5 V
- BiCMOS high speed and output drive
- Direct interface with TTL levels
- Power-up 3-state
- Inputs are disabled during 3-state mode
- $I_{OFF}$  circuitry provides partial Power-down mode operation
- Latch-up protection exceeds 500 mA per JESD78B class II level A
- Quad bus interface
- 3-state buffers
- Live insertion and extraction permitted
- Output capability: HIGH -32 mA; LOW +64 mA
- ESD protection:
  - HBM: ANSI/ESDA/JEDEC JS-001 class 2 exceeds 2000 V
  - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C

## 3. Ordering information

Table 1. Ordering information

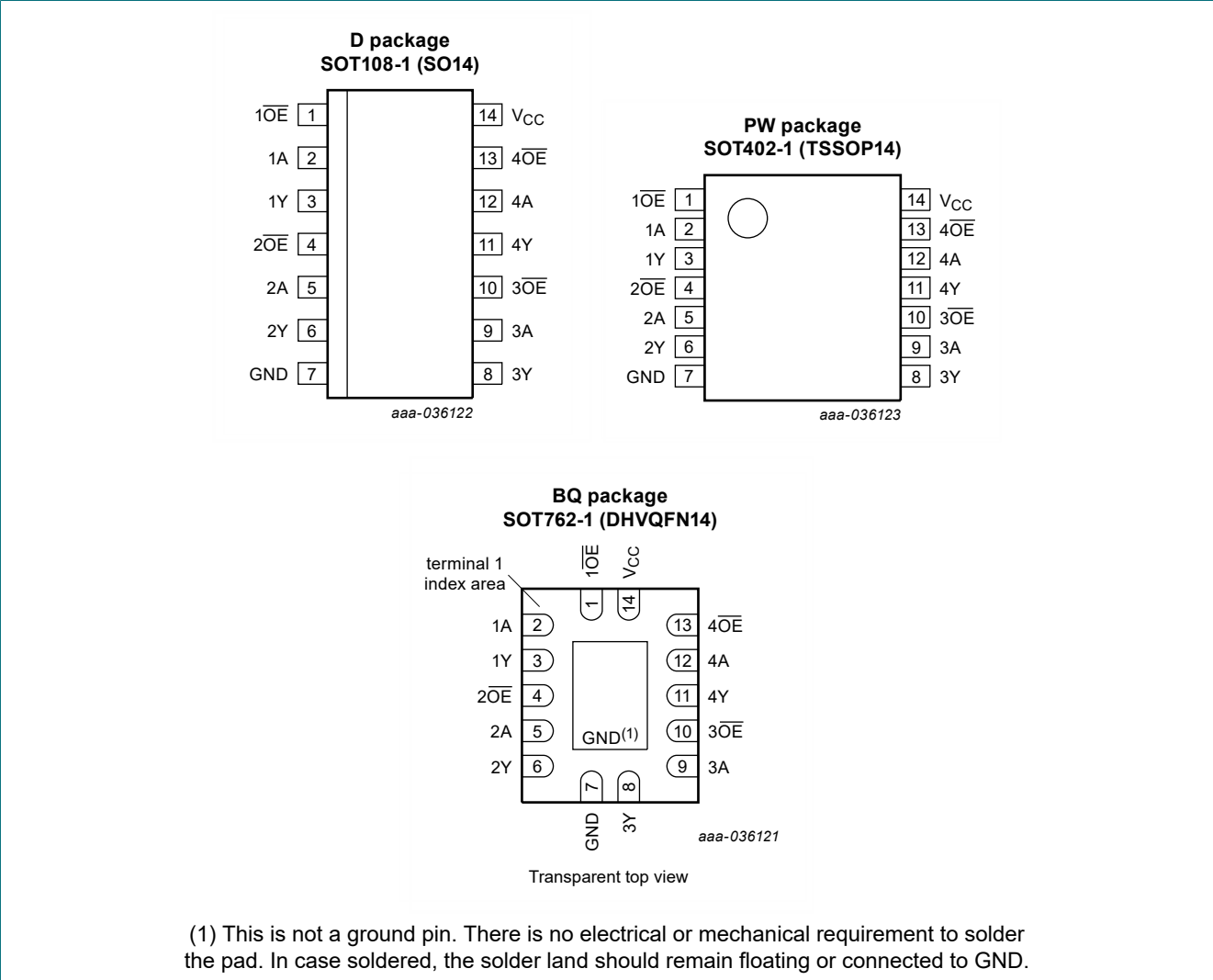
| Type number                | Package           |          |                                                                                                                              |                          |
|----------------------------|-------------------|----------|------------------------------------------------------------------------------------------------------------------------------|--------------------------|
|                            | Temperature range | Name     | Description                                                                                                                  | Version                  |
| <a href="#">74ABT125D</a>  | -40 °C to +85 °C  | SO14     | plastic small outline package; 14 leads; body width 3.9 mm                                                                   | <a href="#">SOT108-1</a> |
| <a href="#">74ABT125PW</a> | -40 °C to +85 °C  | TSSOP14  | plastic thin shrink small outline package; 14 leads; body width 4.4 mm                                                       | <a href="#">SOT402-1</a> |
| <a href="#">74ABT125BQ</a> | -40 °C to +85 °C  | DHVQFN14 | plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 14 terminals; body 2.5 × 3 × 0.85 mm | <a href="#">SOT762-1</a> |

4. Functional diagram



5. Pinning information

5.1. Pinning



6. Functional description

Table 3. Function selection

H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

| Inputs |    | Output |
|--------|----|--------|
| nOE    | nA | nY     |
| L      | L  | L      |
| L      | H  | H      |
| H      | X  | Z      |

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter               | Conditions                              | Min  | Max  | Unit |
|------------------|-------------------------|-----------------------------------------|------|------|------|
| V <sub>CC</sub>  | supply voltage          |                                         | -0.5 | +7.0 | V    |
| V <sub>I</sub>   | input voltage           | [1]                                     | -1.2 | +7.0 | V    |
| V <sub>O</sub>   | output voltage          | output in OFF-state or HIGH-state [1]   | -0.5 | +5.5 | V    |
| I <sub>IK</sub>  | input clamping current  | V <sub>I</sub> < 0 V                    | -18  | -    | mA   |
| I <sub>OK</sub>  | output clamping current | V <sub>O</sub> < 0 V                    | -50  | -    | mA   |
| I <sub>O</sub>   | output current          | output in LOW-state                     | -    | 128  | mA   |
| T <sub>j</sub>   | junction temperature    | [2]                                     | -    | 150  | °C   |
| T <sub>stg</sub> | storage temperature     |                                         | -65  | +150 | °C   |
| P <sub>tot</sub> | total power dissipation | T <sub>amb</sub> = -40 °C to +85 °C [3] | -    | 500  | mW   |

- [1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
- [2] The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability.
- [3] For SOT108-1 (SO14) package: P<sub>tot</sub> derates linearly with 10.1 mW/K above 100 °C.  
For SOT402-1 (TSSOP14) package: P<sub>tot</sub> derates linearly with 7.3 mW/K above 81 °C.  
For SOT762-1 (DHVQFN14) package: P<sub>tot</sub> derates linearly with 9.6 mW/K above 98 °C.

8. Recommended operating conditions

Table 5. Operating conditions

Voltages are referenced to GND (ground = 0 V).

| Symbol           | Parameter                           | Conditions  | Min | Max             | Unit |
|------------------|-------------------------------------|-------------|-----|-----------------|------|
| V <sub>CC</sub>  | supply voltage                      |             | 4.5 | 5.5             | V    |
| V <sub>I</sub>   | input voltage                       |             | 0   | V <sub>CC</sub> | V    |
| V <sub>IH</sub>  | HIGH-level input voltage            |             | 2.0 | -               | V    |
| V <sub>IL</sub>  | LOW-level Input voltage             |             | -   | 0.8             | V    |
| I <sub>OH</sub>  | HIGH-level output current           |             | -32 | -               | mA   |
| I <sub>OL</sub>  | LOW-level output current            |             | -   | 64              | mA   |
| Δt/ΔV            | input transition rise and fall rate |             | -   | 10              | ns/V |
| T <sub>amb</sub> | ambient temperature                 | in free air | -40 | +85             | °C   |

9. Static characteristics

Table 6. Static characteristics

| Symbol                | Parameter                          | Conditions                                                                                                                     | 25 °C |       |      | -40 °C to +85 °C |      | Unit |
|-----------------------|------------------------------------|--------------------------------------------------------------------------------------------------------------------------------|-------|-------|------|------------------|------|------|
|                       |                                    |                                                                                                                                | Min   | Typ   | Max  | Min              | Max  |      |
| V <sub>IK</sub>       | input clamping voltage             | V <sub>CC</sub> = 4.5 V; I <sub>IK</sub> = -18 mA                                                                              | -     | -0.9  | -1.2 | -                | -1.2 | V    |
| V <sub>OH</sub>       | HIGH-level output voltage          | V <sub>I</sub> = V <sub>IL</sub> or V <sub>IH</sub>                                                                            |       |       |      |                  |      |      |
|                       |                                    | V <sub>CC</sub> = 4.5 V; I <sub>OH</sub> = -3 mA                                                                               | 2.5   | 2.9   | -    | 2.5              | -    | V    |
|                       |                                    | V <sub>CC</sub> = 5.0 V; I <sub>OH</sub> = -3 mA                                                                               | 3.0   | 3.4   | -    | 3.0              | -    | V    |
|                       |                                    | V <sub>CC</sub> = 4.5 V; I <sub>OH</sub> = -32 mA                                                                              | 2.0   | 2.4   | -    | 2.0              | -    | V    |
| V <sub>OL</sub>       | LOW-level output voltage           | V <sub>CC</sub> = 4.5 V; I <sub>OL</sub> = 64 mA;<br>V <sub>I</sub> = V <sub>IL</sub> or V <sub>IH</sub>                       | -     | 0.35  | 0.55 | -                | 0.55 | V    |
| I <sub>I</sub>        | input leakage current              | V <sub>CC</sub> = 5.5 V; V <sub>I</sub> = GND or 5.5 V                                                                         | -     | ±0.01 | ±1.0 | -                | ±1.0 | µA   |
| I <sub>OFF</sub>      | power-off leakage current          | V <sub>CC</sub> = 0.0 V; V <sub>I</sub> or V <sub>O</sub> ≤ 4.5 V                                                              | -     | ±5.0  | ±100 | -                | ±100 | µA   |
| I <sub>O(pu/pd)</sub> | power-up/power-down output current | V <sub>CC</sub> = 2.1 V; V <sub>O</sub> = 0.5 V;<br>V <sub>I</sub> = GND or V <sub>CC</sub> ; $\overline{OE}$ = don't care [1] | -     | ±5.0  | ±50  | -                | ±50  | µA   |
| I <sub>OZ</sub>       | OFF-state output current           | V <sub>CC</sub> = 5.5 V; V <sub>I</sub> = V <sub>IL</sub> or V <sub>IH</sub>                                                   |       |       |      |                  |      |      |
|                       |                                    | V <sub>O</sub> = 2.7 V                                                                                                         | -     | 1.0   | 50   | -                | 50   | µA   |
|                       |                                    | V <sub>O</sub> = 0.5 V                                                                                                         | -     | -1.0  | -50  | -                | -50  | µA   |
| I <sub>CEX</sub>      | output high leakage current        | HIGH-state; V <sub>O</sub> = 5.5 V;<br>V <sub>CC</sub> = 5.5 V; V <sub>I</sub> = GND or V <sub>CC</sub>                        | -     | 5.0   | 50   | -                | 50   | µA   |
| I <sub>O</sub>        | output current                     | V <sub>CC</sub> = 5.5 V; V <sub>O</sub> = 2.5 V [2]                                                                            | -50   | -100  | -180 | -50              | -180 | mA   |
| I <sub>CC</sub>       | supply current                     | V <sub>CC</sub> = 5.5 V; V <sub>I</sub> = GND or V <sub>CC</sub>                                                               |       |       |      |                  |      |      |
|                       |                                    | outputs HIGH-state                                                                                                             | -     | 65    | 250  | -                | 250  | µA   |
|                       |                                    | outputs LOW-state                                                                                                              | -     | 12    | 15   | -                | 30   | mA   |
|                       |                                    | outputs disabled                                                                                                               | -     | 65    | 250  | -                | 250  | µA   |
| ΔI <sub>CC</sub>      | additional supply current          | per control pin; V <sub>CC</sub> = 5.5 V;<br>one control input at 3.4 V,<br>other inputs at V <sub>CC</sub> or GND [3]         |       |       |      |                  |      |      |
|                       |                                    | outputs enabled                                                                                                                | -     | 0.5   | 1.5  | -                | 1.5  | mA   |
|                       |                                    | outputs disabled                                                                                                               | -     | 50    | 250  | -                | 250  | mA   |
|                       |                                    | one enable input at 3.4 V and<br>other inputs at V <sub>CC</sub> or GND;<br>outputs disabled                                   | -     | 0.5   | 1.5  | -                | 1.5  | mA   |
| C <sub>I</sub>        | input capacitance                  | V <sub>I</sub> = 0 V or V <sub>CC</sub>                                                                                        | -     | 4     | -    | -                | -    | pF   |
| C <sub>O</sub>        | output capacitance                 | outputs disabled; V <sub>O</sub> = 0 V or V <sub>CC</sub>                                                                      | -     | 7     | -    | -                | -    | pF   |

[1] This parameter is valid for any V<sub>CC</sub> between 0 V and 2.1 V, with a transition time of up to 10 ms.  
From V<sub>CC</sub> = 2.1 V to V<sub>CC</sub> = 5 V ± 10 %, a transition time of up to 100 µs is permitted.

[2] Not more than one output should be tested at a time, and the duration of the test should not exceed one second.

[3] This is the increase in supply current for each input at 3.4 V.

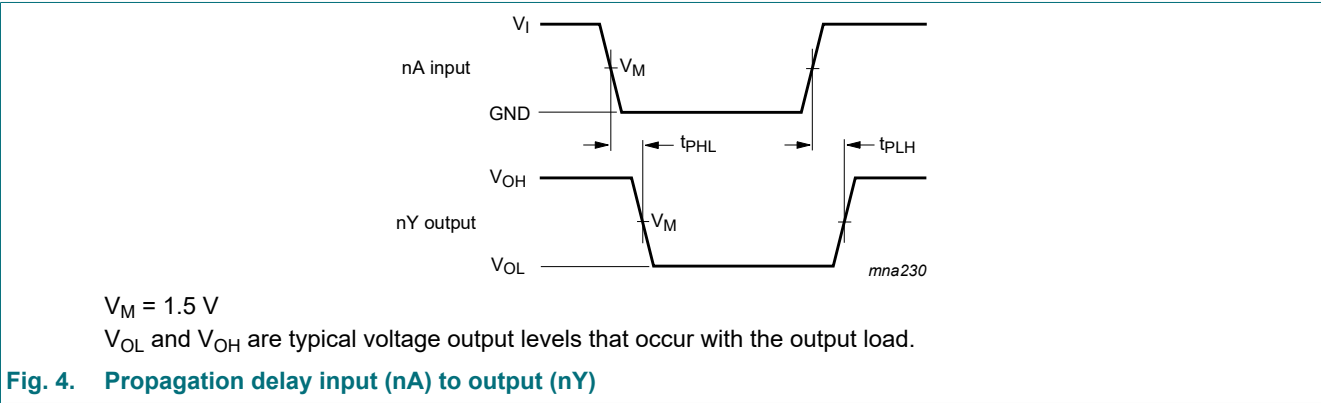
10. Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V. Test circuit is shown in Fig. 6.

| Symbol           | Parameter                           | Conditions                                 | 25 °C; V <sub>CC</sub> = 5.0 V |     |     | -40 °C to +85 °C;<br>V <sub>CC</sub> = 5.0 V ± 0.5 V |     | Unit |
|------------------|-------------------------------------|--------------------------------------------|--------------------------------|-----|-----|------------------------------------------------------|-----|------|
|                  |                                     |                                            | Min                            | Typ | Max | Min                                                  | Max |      |
| t <sub>PLH</sub> | LOW to HIGH propagation delay       | nA to nY, see Fig. 4                       | 1.0                            | 2.8 | 4.1 | 1.0                                                  | 4.6 | ns   |
| t <sub>PHL</sub> | HIGH to LOW propagation delay       | nA to nY; see Fig. 4                       | 1.0                            | 3.1 | 4.6 | 1.0                                                  | 4.9 | ns   |
| t <sub>PZH</sub> | OFF-state to HIGH propagation delay | n $\overline{\text{OE}}$ to nY; see Fig. 5 | 1.0                            | 3.2 | 5.0 | 1.0                                                  | 5.9 | ns   |
| t <sub>PZL</sub> | OFF-state to LOW propagation delay  | n $\overline{\text{OE}}$ to nY; see Fig. 5 | 1.0                            | 4.2 | 6.2 | 1.0                                                  | 6.8 | ns   |
| t <sub>PHZ</sub> | HIGH to OFF-state propagation delay | n $\overline{\text{OE}}$ to nY; see Fig. 5 | 1.0                            | 4.1 | 5.4 | 1.0                                                  | 6.2 | ns   |
| t <sub>PLZ</sub> | LOW to OFF-state propagation delay  | n $\overline{\text{OE}}$ to nY; see Fig. 5 | 1.5                            | 2.8 | 5.0 | 1.5                                                  | 5.5 | ns   |

10.1. Waveforms and test circuit



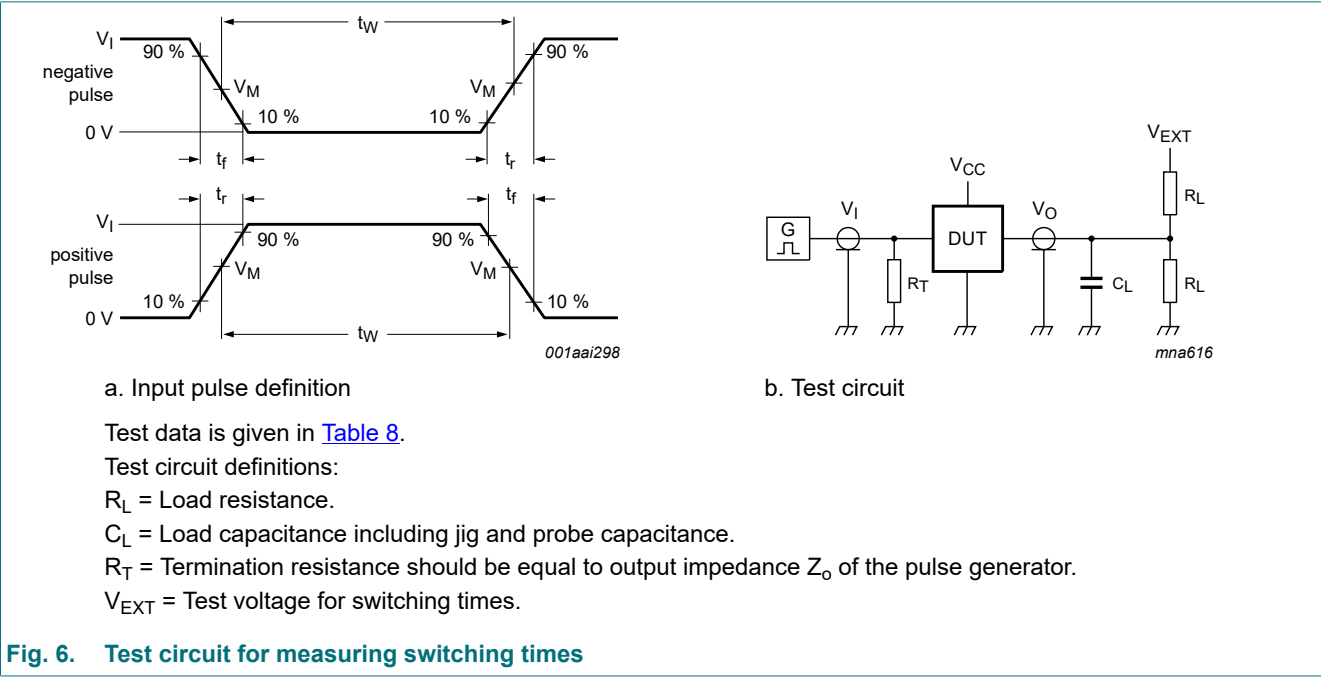
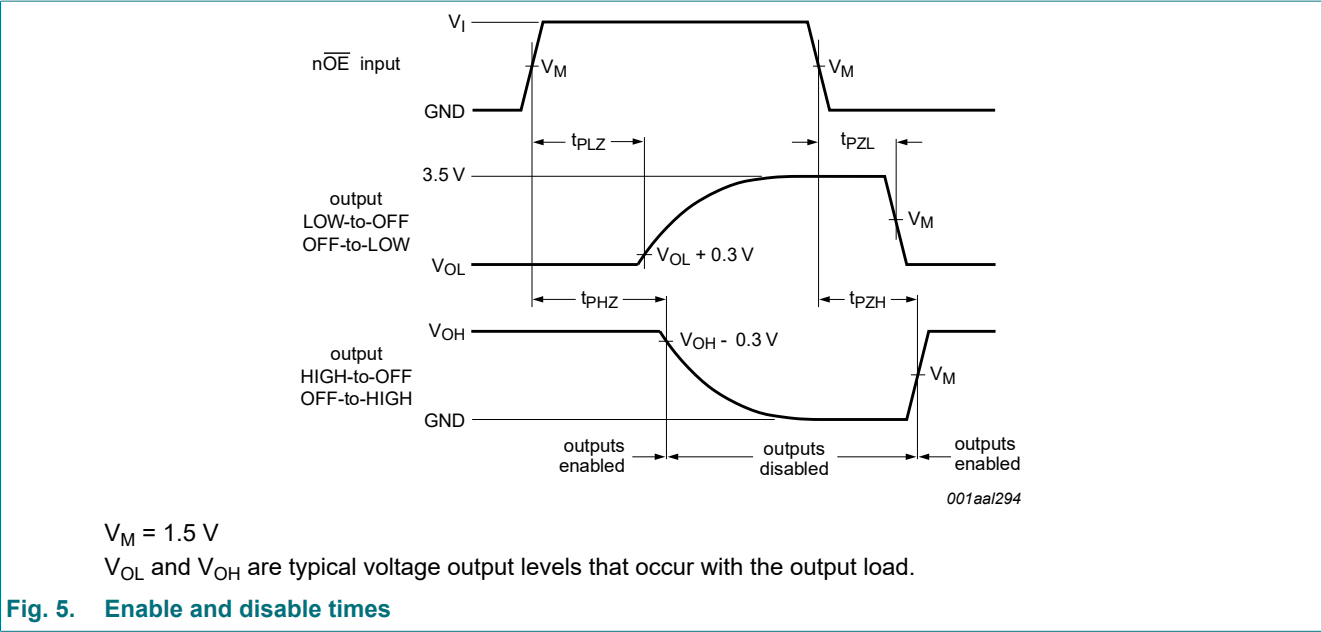


Table 8. Test data

| Input |       |        |                      | Load  |              | $V_{EXT}$          |                    |                    |
|-------|-------|--------|----------------------|-------|--------------|--------------------|--------------------|--------------------|
| $V_I$ | $f_i$ | $t_W$  | $t_r, t_f$           | $C_L$ | $R_L$        | $t_{PHL}, t_{PLH}$ | $t_{PZH}, t_{PHZ}$ | $t_{PZL}, t_{PLZ}$ |
| 3.0 V | 1 MHz | 500 ns | $\leq 2.5\text{ ns}$ | 50 pF | 500 $\Omega$ | open               | open               | 7.0 V              |

11. Package outline

SO14: plastic small outline package; 14 leads; body width 3.9 mm

SOT108-1

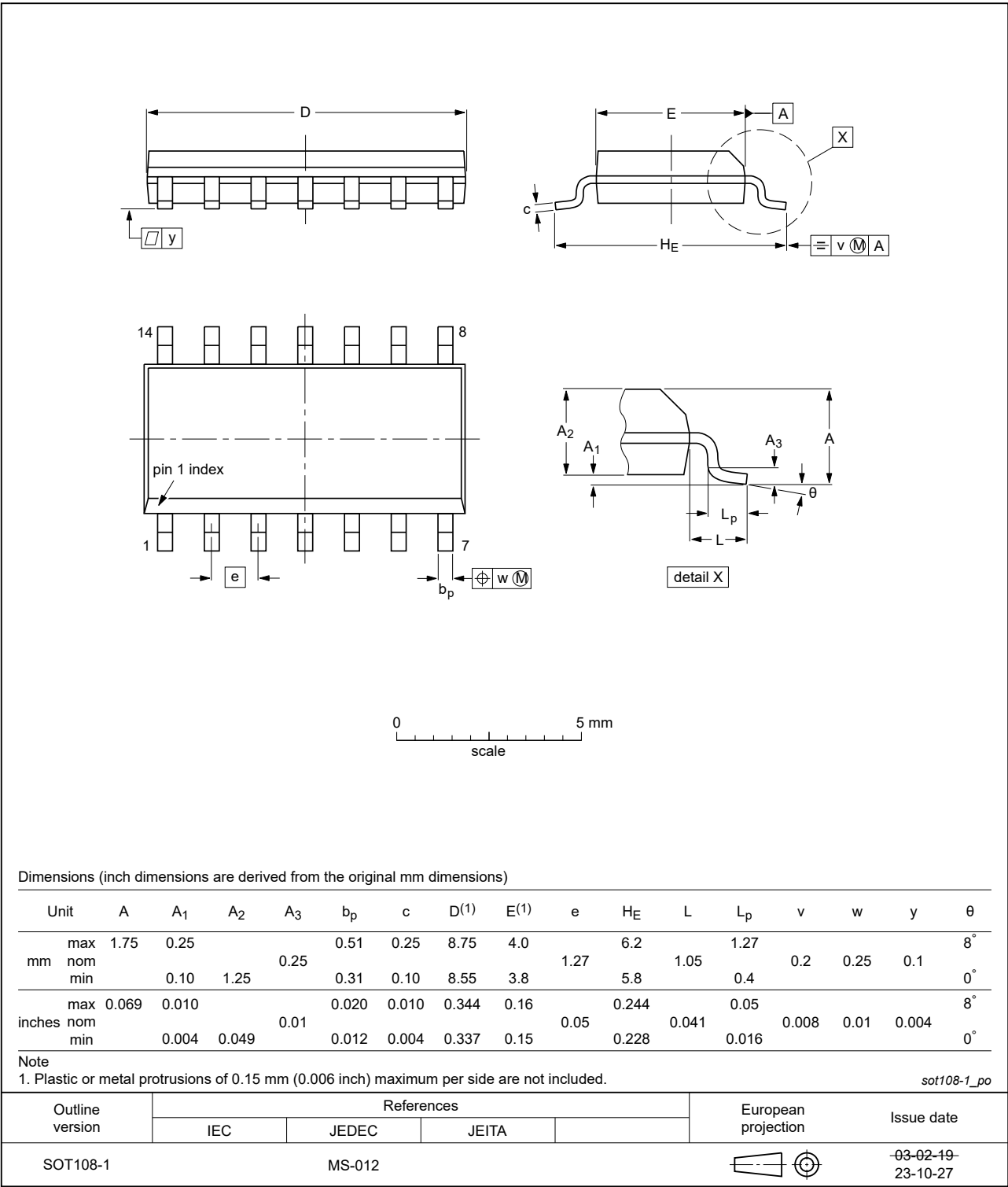


Fig. 7. Package outline SOT108-1 (SO14)



TSSOP14: plastic thin shrink small outline package; 14 leads; body width 4.4 mm

SOT402-1

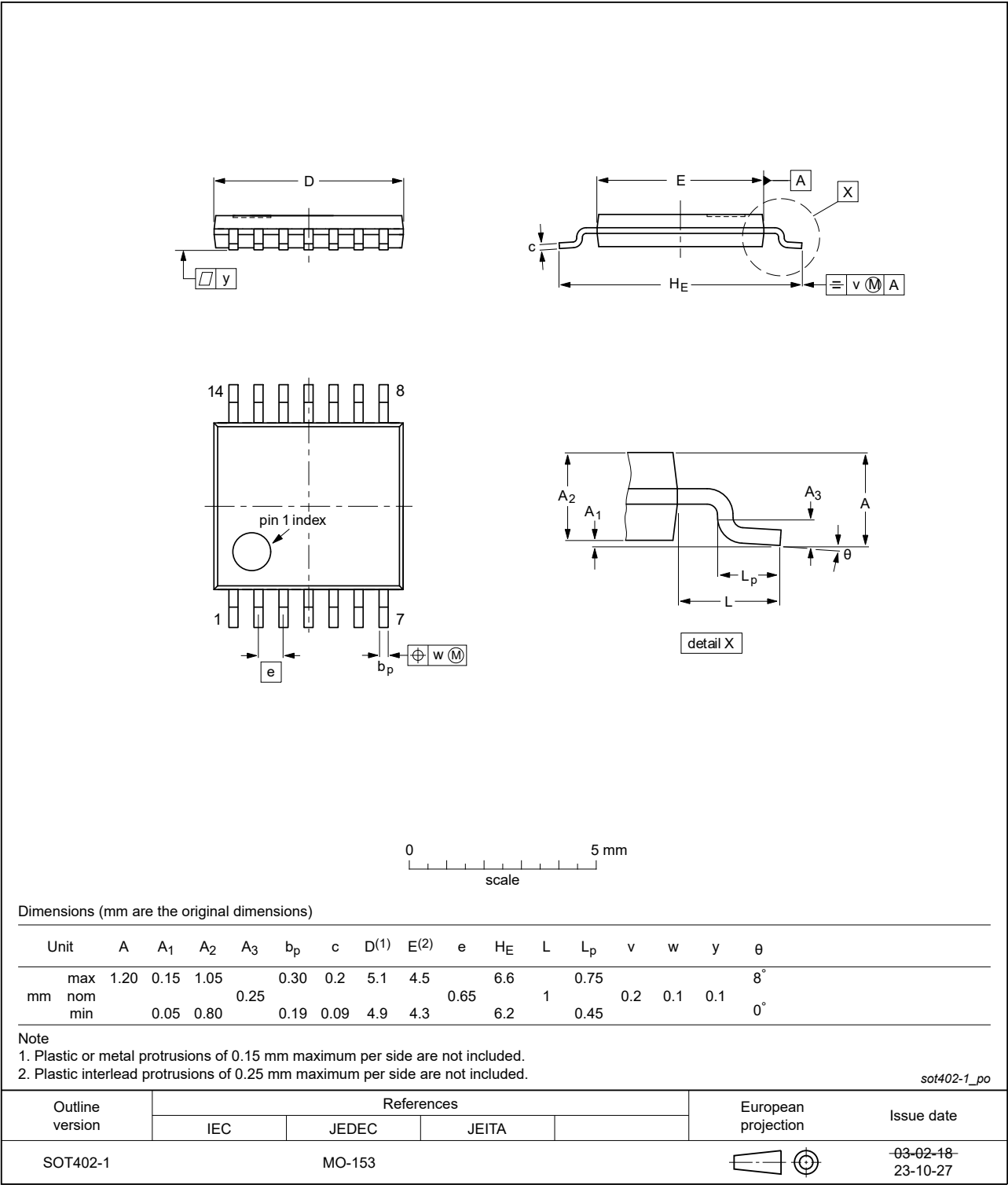
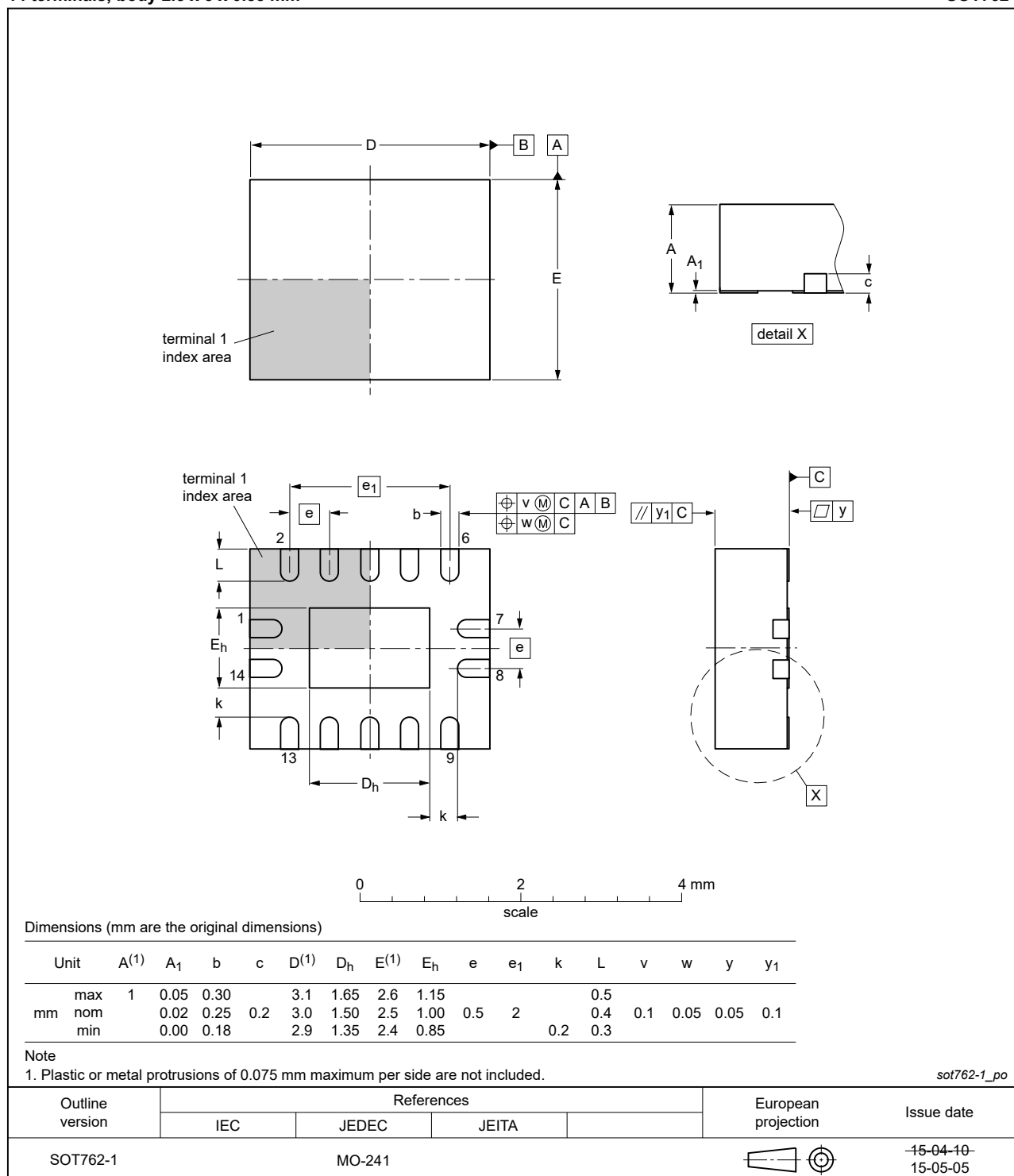


Fig. 8. Package outline SOT402-1 (TSSOP14)

**DHVQFN14:** plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 14 terminals; body 2.5 x 3 x 0.85 mm

SOT762-1



**Fig. 9. Package outline SOT762-1 (DHVQFN14)**

12. Abbreviations

Table 9. Abbreviations

| Acronym | Description                                     |
|---------|-------------------------------------------------|
| BiCMOS  | Bipolar Complementary Metal Oxide Semiconductor |
| CDM     | Charged Device Model                            |
| DUT     | Device Under Test                               |
| ESD     | ElectroStatic Discharge                         |
| HBM     | Human Body Model                                |
| TTL     | Transistor-Transistor Logic                     |

13. Revision history

Table 10. Revision history

| Document ID     | Release date                                                                                                                                                                                                                                                                                                                                                                                                                      | Data sheet status     | Change notice | Supersedes   |
|-----------------|-----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------|---------------|--------------|
| 74ABT125 v.10.1 | 20240118                                                                                                                                                                                                                                                                                                                                                                                                                          | Product data sheet    | -             | 74ABT125 v.9 |
| Modifications:  | <ul style="list-style-type: none"><li>Section 2: ESD specification updated according to the latest JEDEC standard.</li><li>Fig. 7, Fig. 8: Aligned SO and TSSOP package outline drawings to JEDEC MS-012 and MO-153.</li></ul>                                                                                                                                                                                                    |                       |               |              |
| 74ABT125 v.9    | 20230313                                                                                                                                                                                                                                                                                                                                                                                                                          | Product data sheet    | -             | 74ABT125 v.8 |
| Modifications:  | <ul style="list-style-type: none"><li>Table 6: I<sub>CC(max)</sub> value at +85 °C (outputs disabled) corrected. (errata).</li></ul>                                                                                                                                                                                                                                                                                              |                       |               |              |
| 74ABT125 v.8    | 20210630                                                                                                                                                                                                                                                                                                                                                                                                                          | Product data sheet    | -             | 74ABT125 v.7 |
| Modifications:  | <ul style="list-style-type: none"><li>The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia.</li><li>Legal texts have been adapted to the new company name where appropriate.</li><li>Type number 74ABT125DB (SOT337-1/SSOP14) removed.</li><li>Section 1 and Section 2 updated.</li><li>Section 7: Derating values for P<sub>tot</sub> total power dissipation updated.</li></ul> |                       |               |              |
| 74ABT125 v.7    | 20151125                                                                                                                                                                                                                                                                                                                                                                                                                          | Product data sheet    | -             | 74ABT125 v.6 |
| Modifications:  | <ul style="list-style-type: none"><li>Type number 74ABT125N (SOT27-1) removed.</li></ul>                                                                                                                                                                                                                                                                                                                                          |                       |               |              |
| 74ABT125 v.6    | 20111103                                                                                                                                                                                                                                                                                                                                                                                                                          | Product data sheet    | -             | 74ABT125 v.5 |
| Modifications:  | <ul style="list-style-type: none"><li>Legal pages updated</li></ul>                                                                                                                                                                                                                                                                                                                                                               |                       |               |              |
| 74ABT125 v.5    | 20101124                                                                                                                                                                                                                                                                                                                                                                                                                          | Product data sheet    | -             | 74ABT125 v.4 |
| 74ABT125 v.4    | 20100427                                                                                                                                                                                                                                                                                                                                                                                                                          | Product data sheet    | -             | 74ABT125 v.3 |
| 74ABT125 v.3    | 20080429                                                                                                                                                                                                                                                                                                                                                                                                                          | Product data sheet    | -             | 74ABT125 v.2 |
| 74ABT125 v.2    | 19980116                                                                                                                                                                                                                                                                                                                                                                                                                          | Product specification | -             | 74ABT125 v.1 |
| 74ABT125 v.1    | 19960305                                                                                                                                                                                                                                                                                                                                                                                                                          | -                     | -             | -            |

## 14. Legal information

### Data sheet status

| Document status [1][2]         | Product status [3] | Definition                                                                            |
|--------------------------------|--------------------|---------------------------------------------------------------------------------------|
| Objective [short] data sheet   | Development        | This document contains data from the objective specification for product development. |
| Preliminary [short] data sheet | Qualification      | This document contains data from the preliminary specification.                       |
| Product [short] data sheet     | Production         | This document contains the product specification.                                     |

- [1] Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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Date of release: 18 January 2024