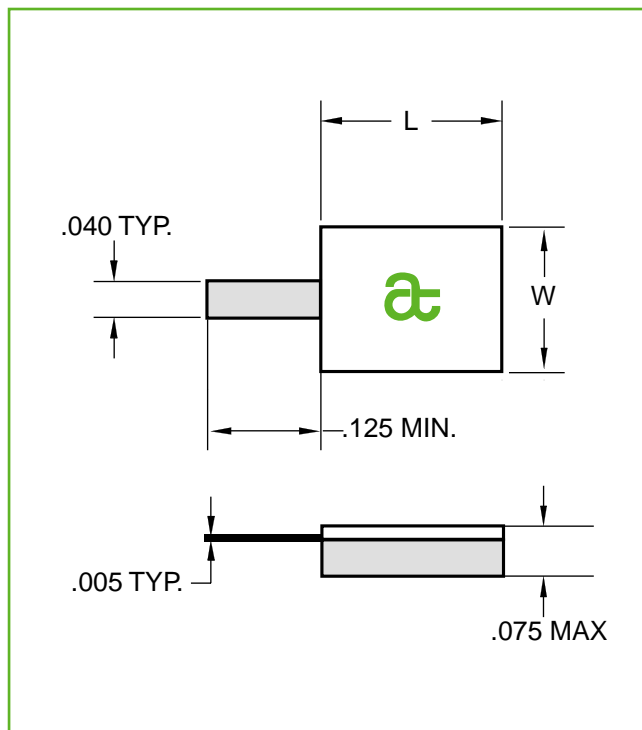


Leaded Chip Terminations Style LT

General Specifications

- **Nominal Impedance:** 50 Ω
- **Resistive Tolerance:** $\pm 5\%$ Standard (2% Available).
- **Operating Temp. Range:** -55°C to $+150^{\circ}\text{C}$
- **Temperature Coefficient:** ± 150 ppm/ $^{\circ}\text{C}$
- **Mechanical Tolerance:** $\pm .010$ inches (unless otherwise specified)
- **Resistive Elements:** Proprietary film.
- **Substrate Material:** Aluminum Nitride.
- **Lead Terminals:** 100% Silver
- **Cover:** Alumina
- **Reliability:** MIL-PRF-55342

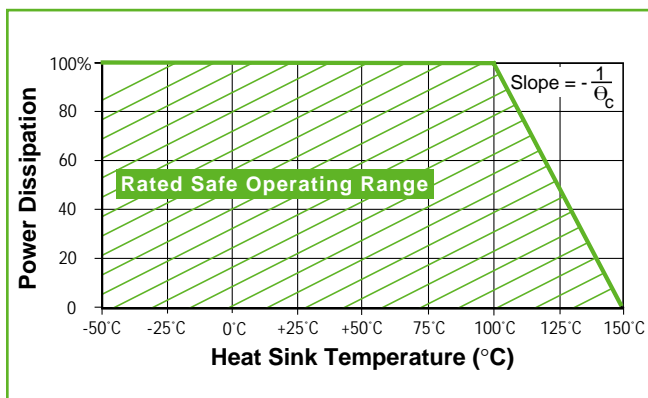


ATC Part Number	W $\pm .010$	L $\pm .010$	T $\pm .005$	Frequency Range (GHz)	VSWR (Max)	Power Max* (Watts)
LT12010T0050J	.100	.200	.040	DC - 6.0	1.25:1	30
LT12525T0050J	.245	.245	.040	DC - 2.5	1.15:1	60
LT12335T0050J	.350	.230	.040	DC - 4.0	1.15:1	50
LT13725T0050J	.250	.375	.040	DC - 1.1	1.20:1	150
LT13737T0050J	.370	.370	.040	DC - 1.0	1.30:1	225

* Test Condition: Chip soldered to a large copper carrier whose surface is at 100°C ; maximum rated power applied.

Specification: The resistance of the film shall change no more than 0.5% during and after a 1000-hr. Burn-in per MIL-PRF-55342

Power Derating



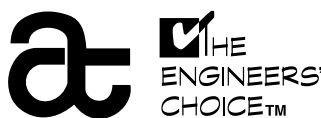
ATC Part Number Code

LT1	2010	T	0100	J	BK
Case Style	Case Size	Termination		Packaging BK - Bulk	Tolerance See table below.
				Value	
Resistive Tolerances					
Code	G (%)	J (%)			
Tol.	± 2	± 5			

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COMPONENT AND CUSTOM INTEGRATED PACKAGING SOLUTIONS
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