

TB2913HQ

Maximum Power 47 W BTL × 4-ch Audio Power IC

The TB2913HQ is 4-ch BTL audio amplifier for car audio applications.

This IC can generate higher power: $POUT_{MAX} = 47\text{ W}$ as it includes the pure complementary P-ch and N-ch DMOS output stage.

It is designed to yield low distortion ratio for 4-ch BTL audio power amplifier, built-in standby function, muting function, and various kinds of protectors.

Additionally, Clip detector is built in.

Features

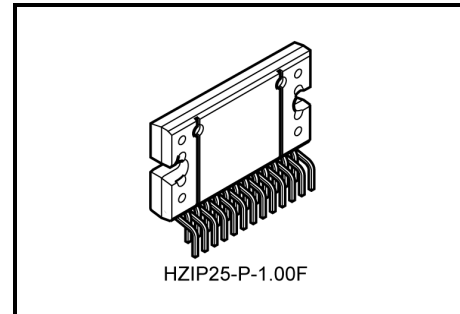
- High power output
 - : $POUT_{MAX} (1) = 47\text{ W (typ.)}$
($V_{CC} = 14.4\text{ V}$, $f = 1\text{ kHz}$, JEITA max, $R_L = 4\ \Omega$)
 - : $POUT_{MAX} (2) = 43\text{ W (typ.)}$
($V_{CC} = 13.7\text{ V}$, $f = 1\text{ kHz}$, JEITA max, $R_L = 4\ \Omega$)
 - : $POUT (1) = 29\text{ W (typ.)}$
($V_{CC} = 14.4\text{ V}$, $f = 1\text{ kHz}$, THD = 10%, $R_L = 4\ \Omega$)
 - : $POUT (2) = 25\text{ W (typ.)}$
($V_{CC} = 13.2\text{ V}$, $f = 1\text{ kHz}$, THD = 10%, $R_L = 4\ \Omega$)
- Low distortion ratio: THD = 0.015% (typ.)
($V_{CC} = 13.2\text{ V}$, $f = 1\text{ kHz}$, $POUT = 5\text{ W}$, $R_L = 4\ \Omega$)
- Low noise: $V_{NO} = 90\ \mu\text{Vrms (typ.)}$
($V_{CC} = 13.2\text{ V}$, $R_g = 0\ \Omega$, BW = 20 Hz~20 kHz, $R_L = 4\ \Omega$)
- Built-in standby switch function (pin 4)
- Built-in muting function (pin 22)
- Built-in Clip-detection function (pin 25)
- Built-in various protection circuits:
 - Thermal shut down, overvoltage, out to GND, out to V_{CC} , out to out short
- Operating supply voltage: $V_{CC (opr)} = 9\sim 18\text{ V (}R_L = 4\ \Omega\text{)}$

Note 1: Since this device's pins have a low withstanding voltage, please handle it with care.

Note 2: Install the product correctly. Otherwise, it may result in break down, damage and/or degradation to the product or equipment.

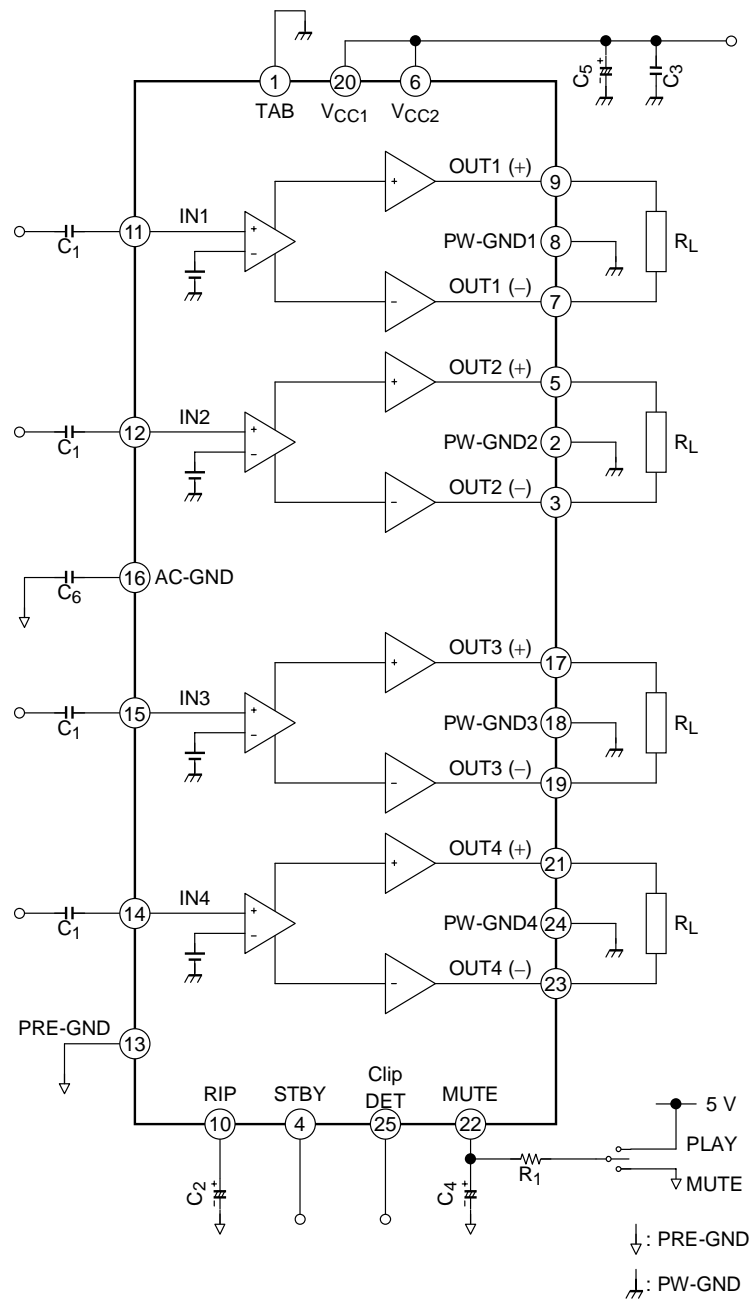
Note 3: These protection functions are intended to avoid some output short circuits or other abnormal conditions temporarily. These protect functions do not warrant to prevent the IC from being damaged.

- In case of the product would be operated with exceeded guaranteed operating ranges, these protection features may not operate and some output short circuits may result in the IC being damaged



Weight: 7.7 g (typ.)

Block Diagram



Note: Some of the functional blocks, circuits, or constants in the block diagram may be omitted or simplified for explanatory purpose

Caution and Application Method

(Description is made only on the single channel.)

1. Voltage Gain Adjustment

This IC has no NF (negative feedback) Pins. Therefore, the voltage gain can not be adjusted, but it makes the device a space and total costs saver.

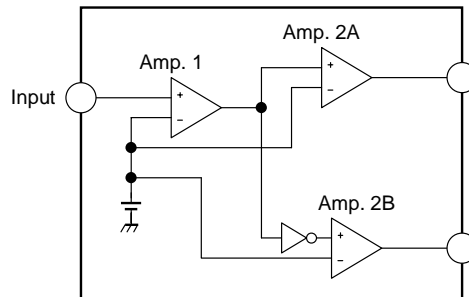


Figure 1 Block Diagram

The voltage gain of amp.1 : $GV_1 = 0\text{dB}$

The voltage gain of amp.2A, B : $GV_2 = 20\text{dB}$

The voltage gain of BTL connection: $GV(\text{BTL}) = 6\text{dB}$

Therefore, the total voltage gain is decided by expression below.

$$GV = GV_1 + GV_2 + GV(\text{BTL}) = 0 + 20 + 6 = 26\text{dB}$$

2. Standby SW Function (pin 4)

By means of controlling pin 4 (standby pin) to High and Low, the power supply can be set to ON and OFF. The threshold voltage of pin 4 is set at about $3V_{BE}$ (typ.), and the power supply current is about $2\mu\text{A}$ (typ.) in the standby state.

Control Voltage of Pin 4: V_{SB}

| Standby | Power | V_{SB} (V) |
|---------|-------|--------------|
| ON | OFF | 0~1.5 |
| OFF | ON | 3.5~6 V |

When changing the time constant of pin 4, check the pop noise.

Advantage of Standby SW

- (1) Since V_{CC} can directly be controlled to ON or OFF by the microcomputer, the switching relay can be omitted.
- (2) Since the control current is microscopic, the switching relay of small current capacity is satisfactory for switching.

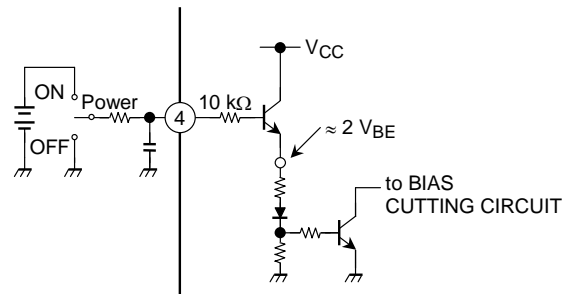


Figure 2 With pin 4 set to High, Power is turned ON

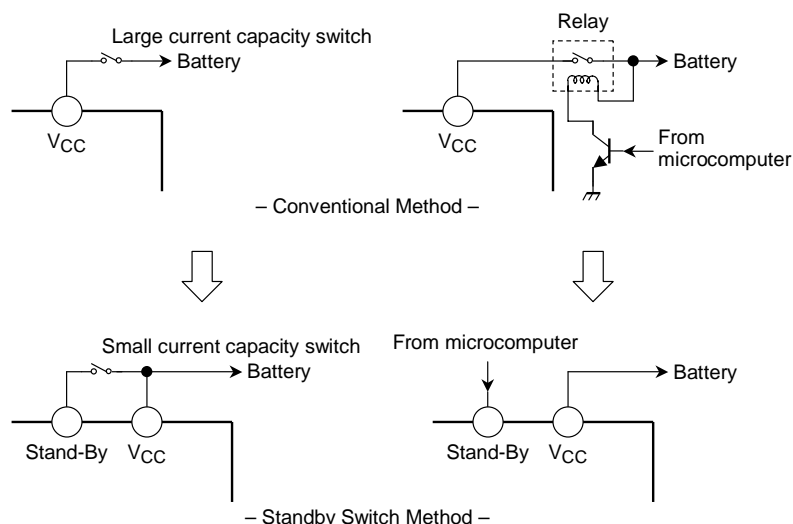


Figure 3

3. Muting Function (pin 22)

Audio muting function is enabled when pin 22 is Low. When the time constant of the muting function is determined by R_1 and C_4 , it should take into account the pop noise. The pop noise, which is generated when the power or muting function is turned ON/OFF, will vary according to the time constant. (Refer to Figure 4 and Figure 5.)

The pin 22 is designed to operate off 5 V so that the outside pull-up resistor R_1 is determined on the basis of this value:

ex) When control voltage is changed in to 6 V from 5 V.

$$6\text{ V}/5\text{ V} \times 47\text{ k} = 56\text{ k}$$

Additionally, as the V_{CC} is rapidly falling, the IC internal low voltage muting operates to eliminate the large pop noise basically.

The low voltage muting circuit pull 200uA current into the IC so that the effect of the internal low voltage muting does not become enough if the R_1 is too small value.

To obtain enough operation of the internal low voltage muting, a series resistor, R_1 at pin 22 should be 47 k Ω or more.

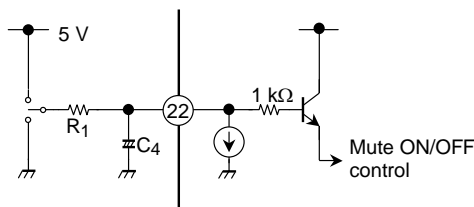
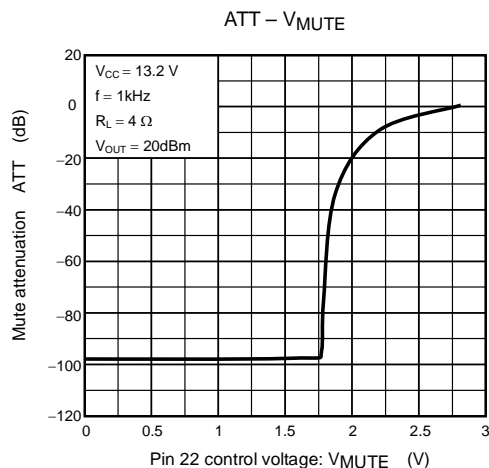


Figure 4 Muting Function

Figure 5 Mute Attenuation – V_{MUTE} (V)

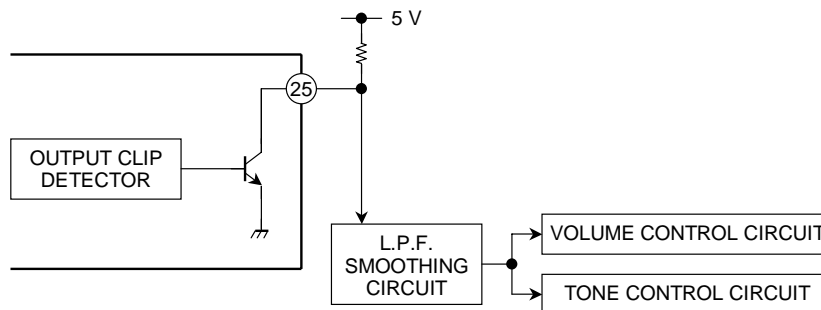
4. Output Clip Detection Function (pin 25)

The output clip detection terminal of pin 25 has the open collector output structure on chip as shown in Figure 6. In case that the output waveform is clipping, the clip detection circuit is operated and NPN Tr. is turned on.

It is possible to improve the audio quality with controlling the volume, tone control circuit through L.P.F. smoothing circuit as shown in Figure 7.

In case of being unused this function, use this IC as open connection on pin 25.

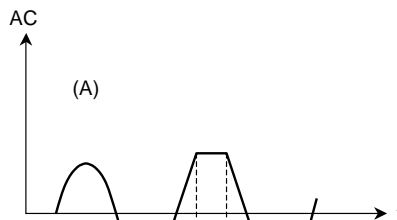
(Application)



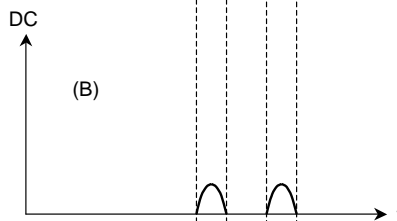
pin 25: Open collector output (active low)

Figure 6

(A) Output (AC wave form)



(B) Clip Detector Circuit (internal)



(B) Clip Detector Terminal (pin 25)

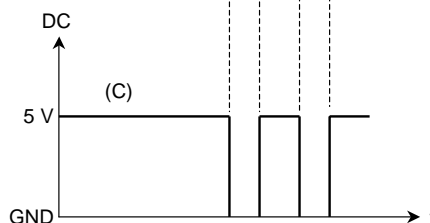


Figure 7 Clip Detection

5. Pop Noise Suppression

Since the AC-GND pin (pin 16) is used as the NF pin for all amps, the ratio between the input capacitance (C1) and the AC-to-GND capacitance (C6) should be 1:4.

Also, if the power is turned OFF before the C1 and C6 batteries have been completely charged, pop noise will be generated because of the DC input unbalance.

To counteract the noise, it is recommended that a longer charging time be used for C2 as well as for C1 and C6. Note that the time which audio output takes to start will be longer, since the C2 makes the muting time (the time from when the power is turned ON to when audio output starts) is fix.

The pop noise which is generated when the muting function is turned ON/OFF will vary according to the time constant of C4.

The greater the capacitance, the lower the pop noise. Note that the time from when the mute control signal is applied to C4 to when the muting function is turned ON/OFF will be longer.

6. External Component Constants

| Component Name | Recommended Value | Purpose | Effect | | Notes |
|----------------|-------------------|--|--|--|--|
| | | | Lower than recommended value | Higher than recommended value | |
| C1 | 0.22 μ F | To eliminate DC | Cut-off frequency is increased | Cut-off frequency is reduced | Pop noise is generated when V_{CC} is ON |
| C2 | 10 μ F | To reduce ripple | Powering ON/OFF is faster | Powering ON/OFF takes longer | |
| C3 | 0.1 μ F | To provide sufficient oscillation margin | Reduces noise and provides sufficient oscillation margin | | |
| C4 | 1 μ F | To reduce pop noise | High pop noise. Duration until muting function is turned ON/OFF is short | Low pop noise. Duration until muting function is turned ON/OFF is long | |
| C5 | 3900 μ F | Ripple filter | Power supply ripple filtering | | |
| C6 | 1 μ F | NF for all outputs | Pop noise is suppressed when $C1:C6 = 1:4$ | | Pop noise is generated when V_{CC} is ON |

Note: If recommended value is not used.

Maximum Ratings (Ta = 25°C)

| Characteristics | Symbol | Rating | Unit |
|-----------------------------|-------------------------|---------|------|
| Peak supply voltage (0.2 s) | V _{CC} (surge) | 50 | V |
| DC supply voltage | V _{CC} (DC) | 28 | V |
| Operation supply voltage | V _{CC} (opr) | 18 | V |
| Output current (peak) | I _O (peak) | 9 | A |
| Power dissipation | P _D (Note 2) | 125 | W |
| Operation temperature | T _{opr} | -40~85 | °C |
| Storage temperature | T _{stg} | -55~150 | °C |

Note 2: Package thermal resistance $\theta_{J-T} = 1^{\circ}\text{C/W}$ (typ.) (Ta = 25°C, with infinite heat sink)

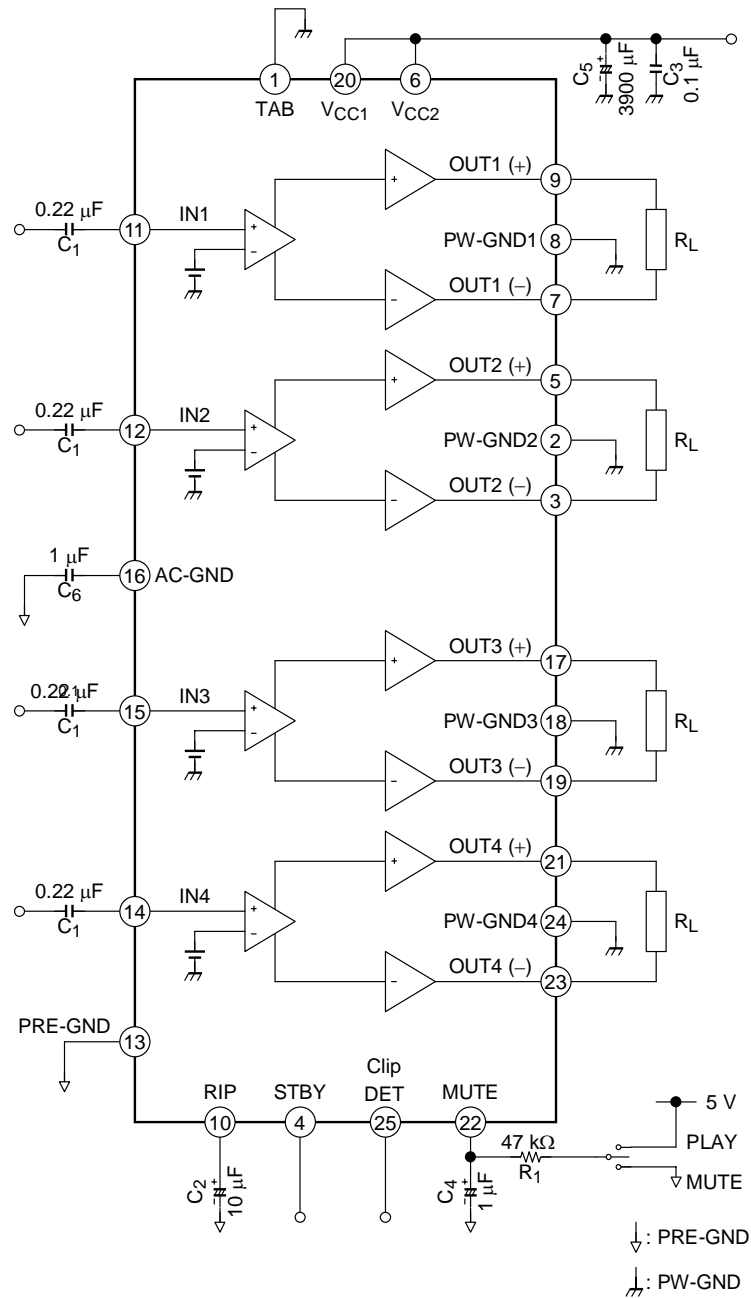
The absolute maximum ratings of a semiconductor device are a set of specified parameter values, which must not be exceeded during operation, even for an instant. If any of these rating would be exceeded during operation, the device electrical characteristics may be irreparably altered and the reliability and lifetime of the device can no longer be guaranteed. Moreover, these operations with exceeded ratings may cause break down, damage and/or degradation to any other equipment. Applications using the device should be designed such that each maximum rating will never be exceeded in any operating conditions. Before using, creating and/or producing designs, refer to and comply with the precautions and conditions set forth in this documents

Electrical Characteristics

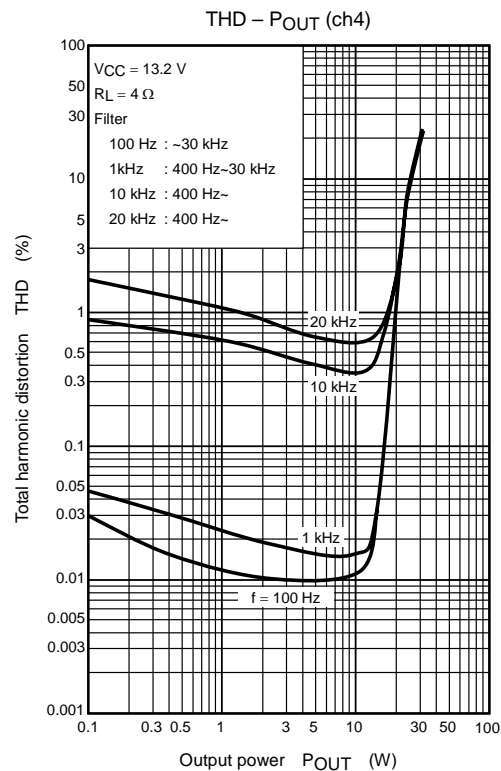
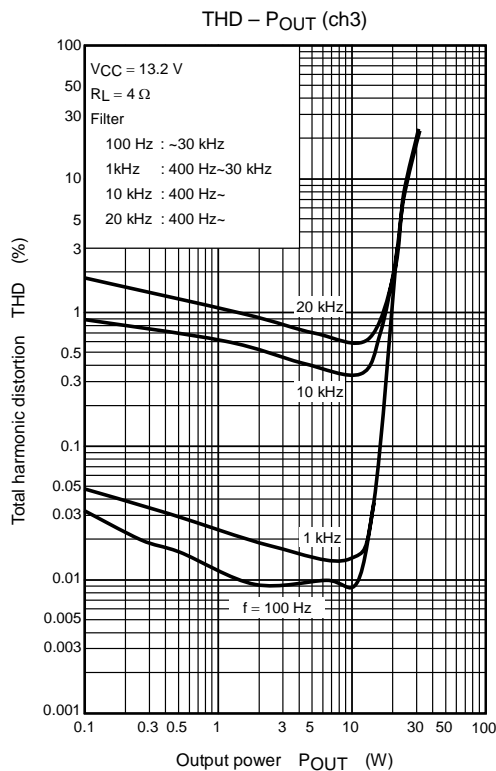
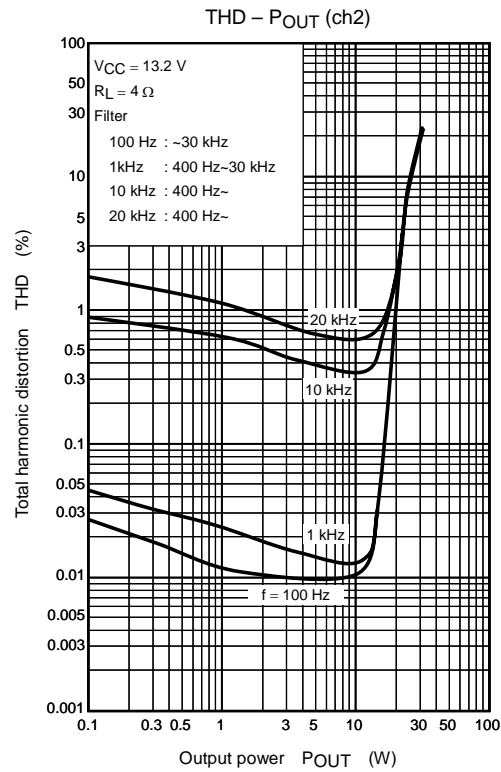
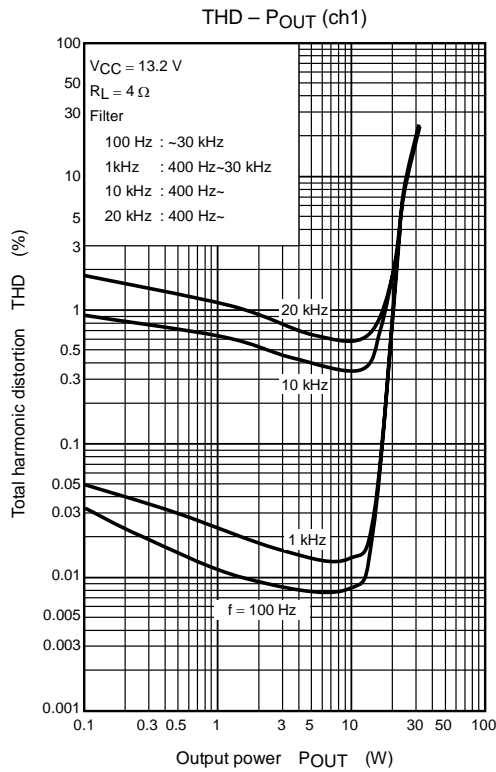
(unless otherwise specified, V_{CC} = 13.2 V, f = 1 kHz, R_L = 4 Ω , Ta = 25°C)

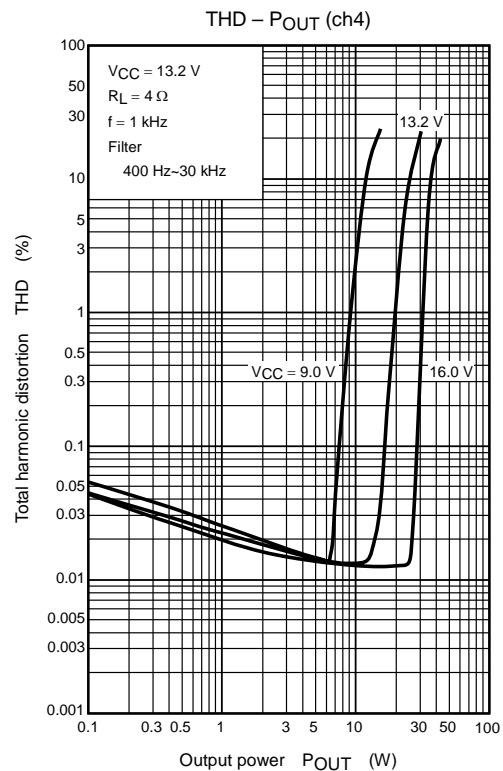
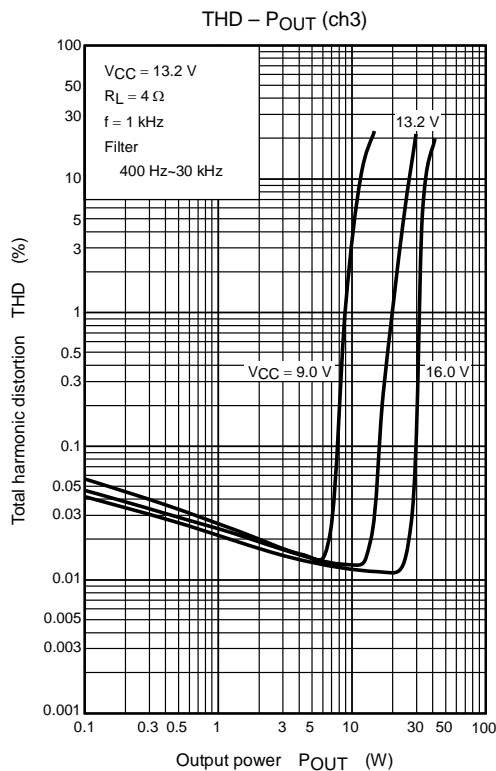
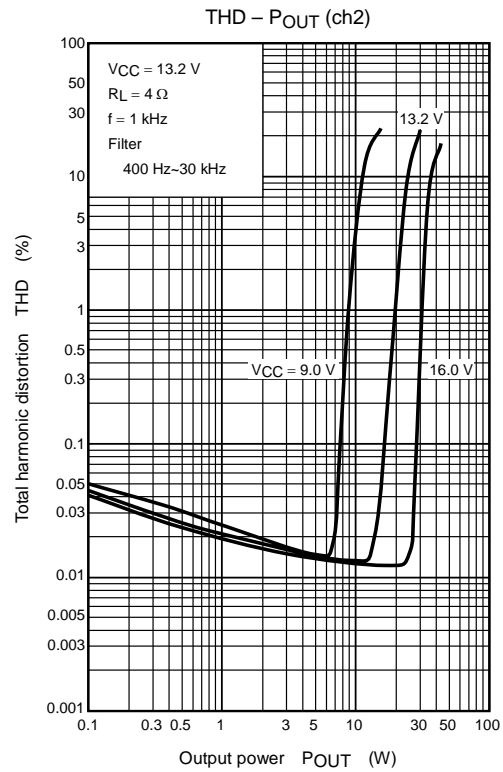
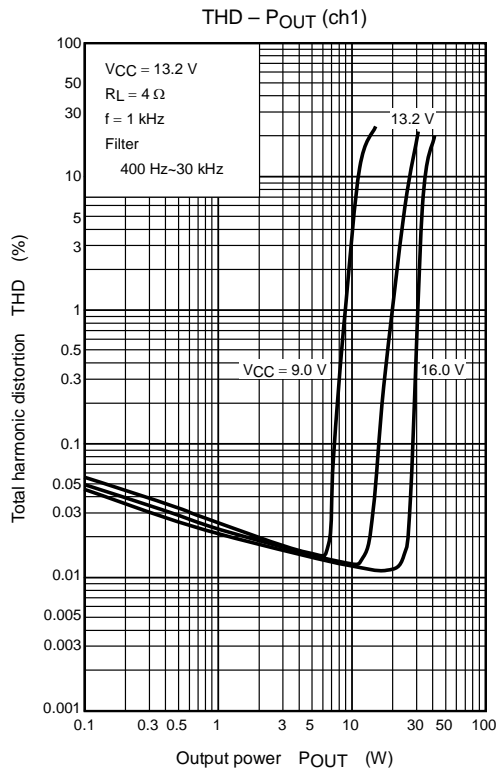
| Characteristics | Symbol | Test Circuit | Test Condition | Min | Typ. | Max | Unit |
|---------------------------|--------------------------|--------------|---|------|-------|------|------------------|
| Quiescent current | I _{CCQ} | — | V _{IN} = 0 | — | 200 | 400 | mA |
| Output power | P _{OUT} MAX (1) | — | V _{CC} = 14.4 V, max POWER | — | 47 | — | W |
| | P _{OUT} MAX (2) | — | V _{CC} = 13.7 V, max POWER | — | 43 | — | |
| | P _{OUT} (1) | — | V _{CC} = 14.4 V, THD = 10% | — | 29 | — | |
| | P _{OUT} (2) | — | THD = 10% | 23 | 25 | — | |
| Total harmonic distortion | THD | — | P _{OUT} = 5 W | — | 0.015 | 0.15 | % |
| Voltage gain | G _V | — | V _{OUT} = 0.775 Vrms | 24 | 26 | 28 | dB |
| Voltage gain ratio | ΔG_V | — | V _{OUT} = 0.775 Vrms | -1.0 | 0 | 1.0 | dB |
| Output noise voltage | V _{NO} (1) | — | R _g = 0 Ω , DIN45405 | — | 100 | — | μVrms |
| | V _{NO} (2) | — | R _g = 0 Ω , BW = 20 Hz~20 kHz | — | 90 | 200 | |
| Ripple rejection ratio | R.R. | — | f _{rip} = 100 Hz, R _g = 620 Ω V _{rip} = 0.775 Vrms | 50 | 60 | — | dB |
| Cross talk | C.T. | — | R _g = 620 Ω V _{OUT} = 0.775 Vrms | — | 70 | — | dB |
| Output offset voltage | V _{OFFSET} | — | — | -150 | 0 | 150 | mV |
| Input resistance | R _{IN} | — | — | — | 90 | — | k Ω |
| Standby current | I _{SB} | — | Standby condition | — | 2 | 10 | μA |
| Standby control voltage | V _{SB} H | — | POWER: ON | 3.5 | — | 6.0 | V |
| | V _{SB} L | — | POWER: OFF | 0 | — | 1.5 | |
| Mute control voltage | V _M H | — | MUTE: OFF | 3.0 | — | 6.0 | V |
| | V _M L | — | MUTE: ON, R ₁ = 47 k Ω | 0 | — | 0.5 | |
| Mute attenuation | ATT M | — | MUTE: ON V _{OUT} = 7.75 Vrms→Mute: OFF | 80 | 90 | — | dB |

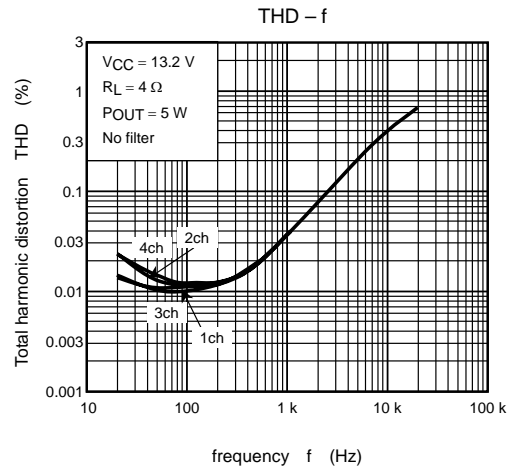
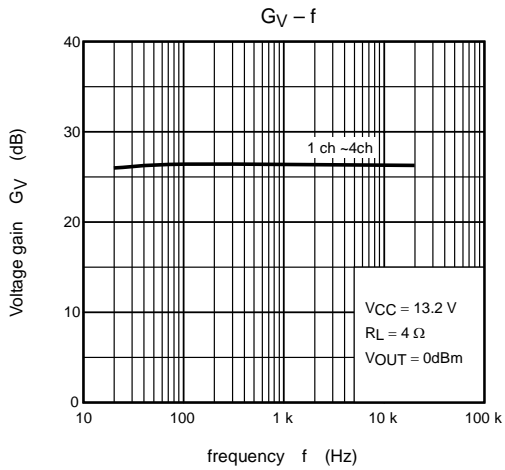
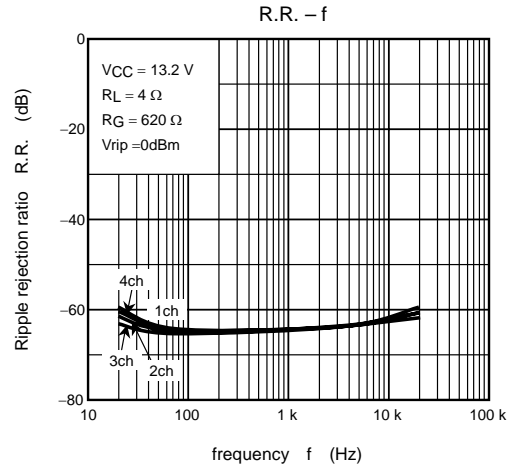
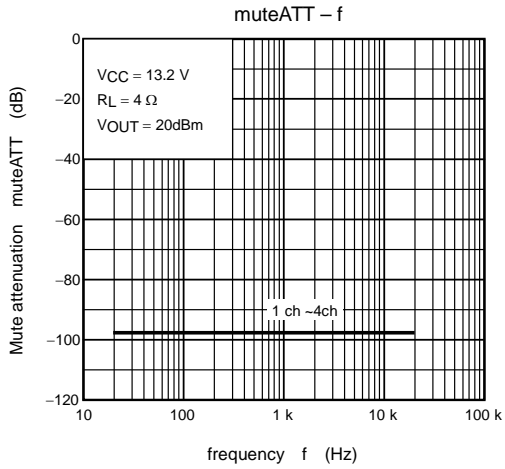
Test Circuit

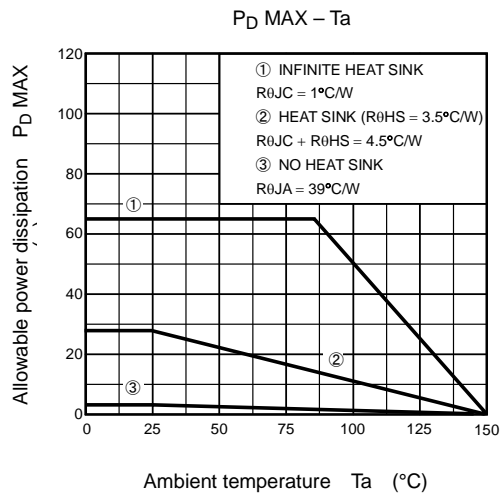
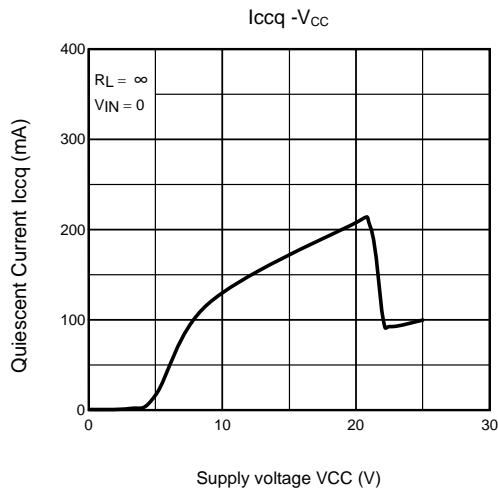
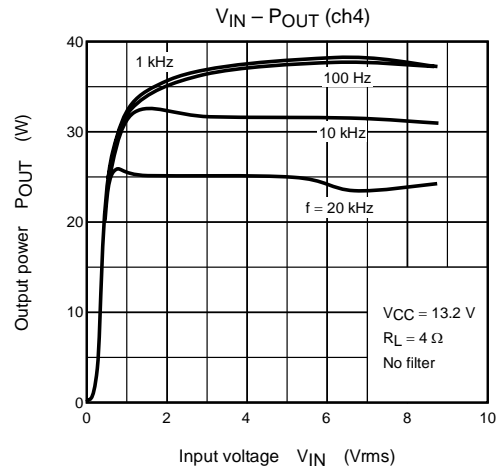
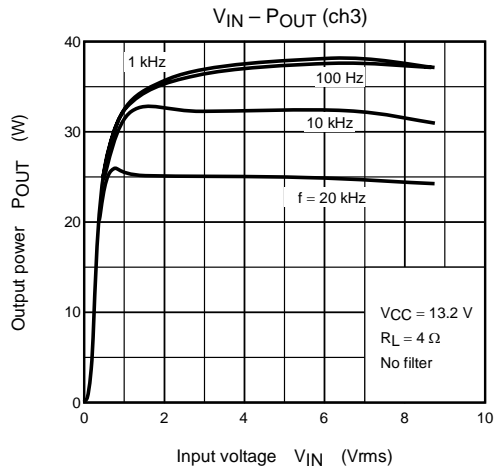
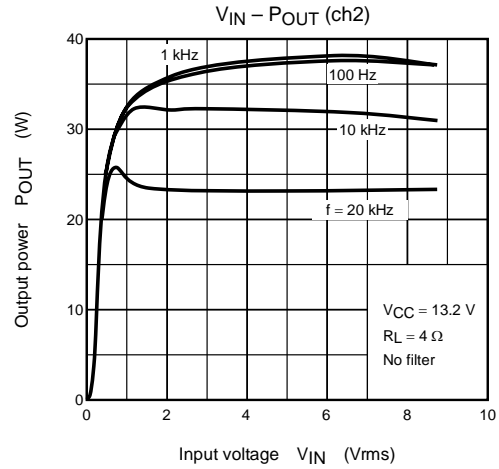
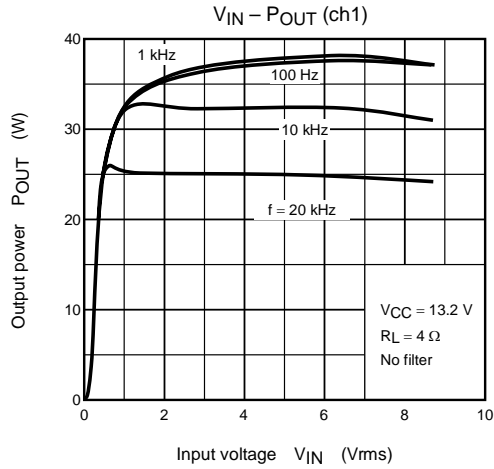


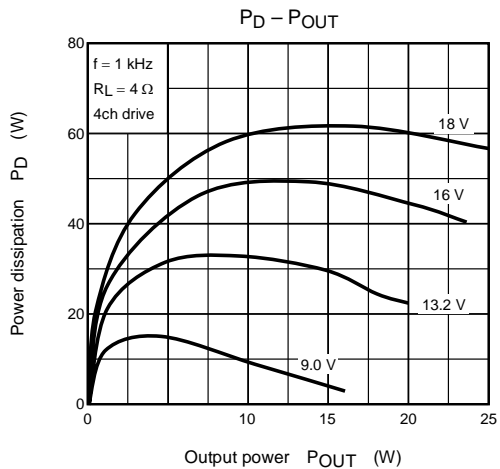
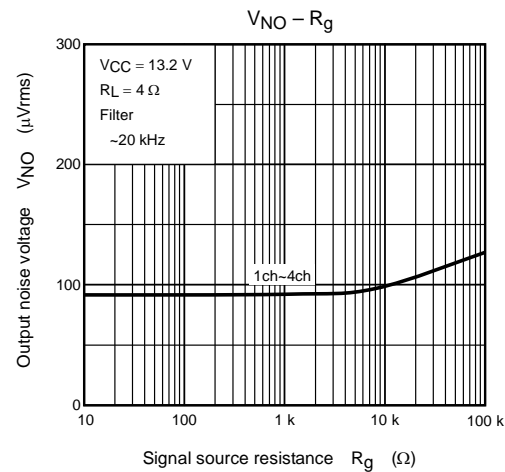
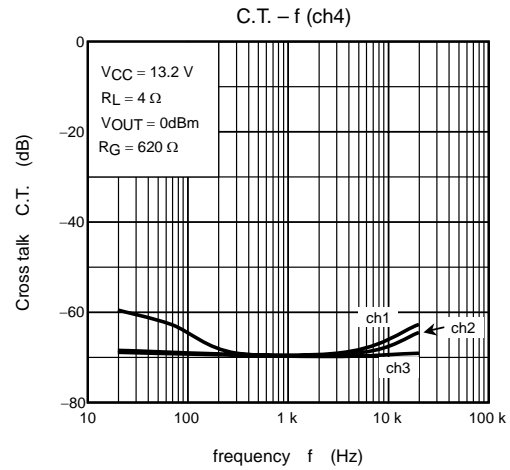
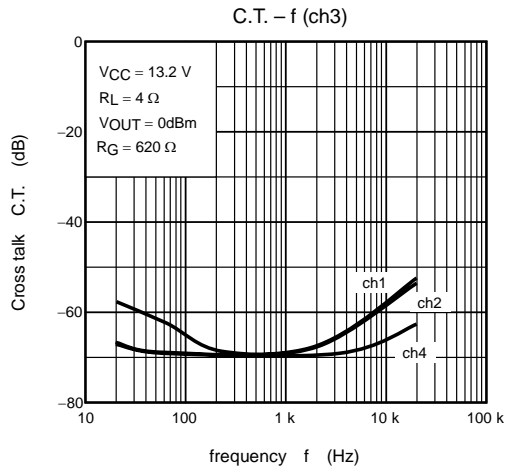
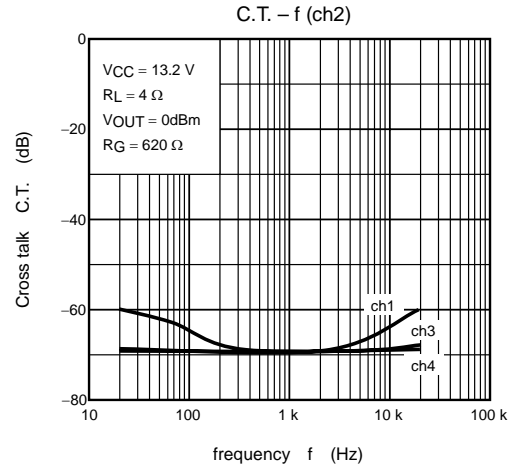
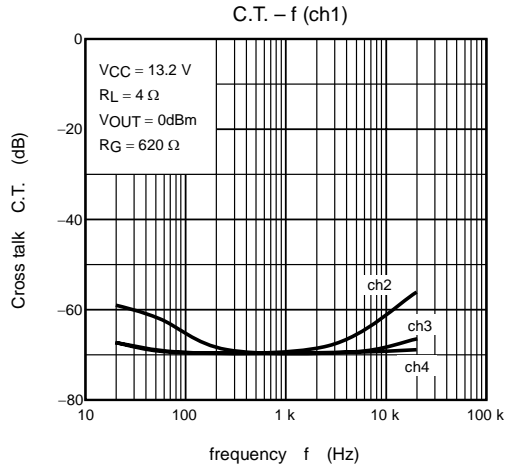
Components in the test circuits are only used to obtain and confirm the device characteristics.
 These components and circuits do not warrant to prevent the application equipment from malfunction or failure.







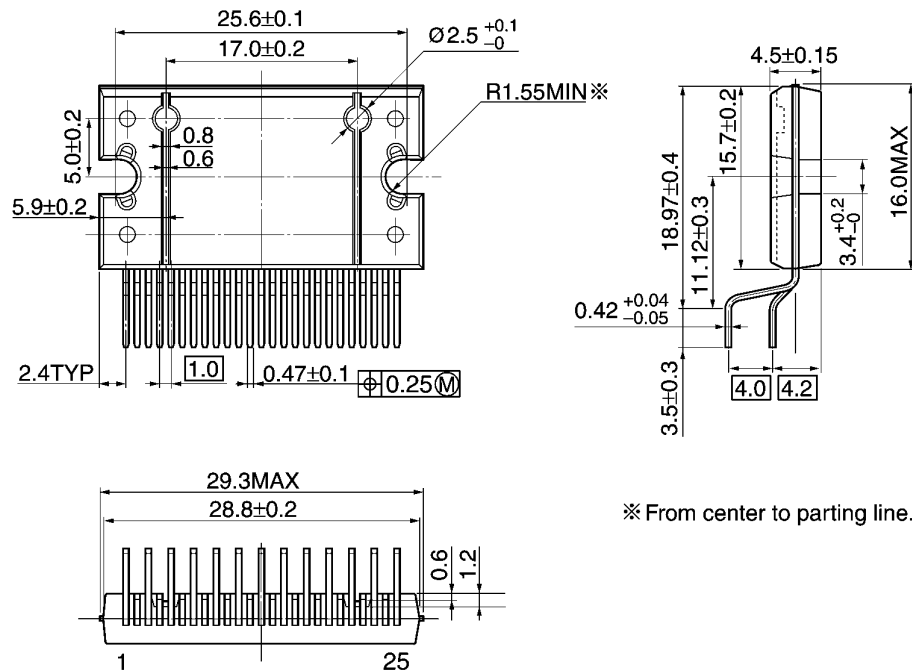




Package Dimensions

HZIP25-P-1.00F

Unit: mm



※ From center to parting line.

Weight: 7.7 g (typ.)

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