TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WG04FU,TC7WG04FK

Triple Inverter

Features

• High-level output current: $I_{OH}/I_{OL} = \pm 8 \text{ mA (min)}$

at $V_{CC} = 3 V$

• High-speed operation: $t_{pd} = 2.7 \text{ ns (typ.)}$

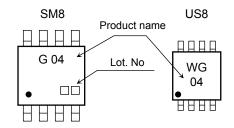
at $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$

• Operating voltage range: V_{CC} = 0.9~3.6 V

• 5.5-V tolerant inputs

• 3.6-V power down protection outputs

Marking



TC7WG04FU SSOP8-P-0.65 TC7WG04FK SSOP8-P-0.50A

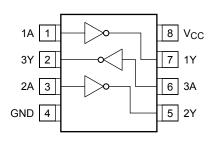
Weight

SSOP8-P-0.65 : 0.02 g (typ.) SSOP8-P-0.50A : 0.01 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit
Power supply voltage	V _{CC}	-0.5~4.6	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC output voltage	V _{OUT}	-0.5~4.6 (Note 1)	V
		-0.5~V _{CC} + 0.5 (Note 2)	V
Input diode current	l _{IK}	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	I _{OUT}	±25	mA
DC V _{CC} / ground current	Icc	±50	mA
Power dissipation	PD	300 (SM8) 200 (US8)	mW
Storage temperature	T _{stg}	-65~150	°C

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

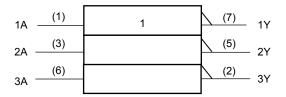
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{CC} = 0V

Note 2: High or Low State. IOUT absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

IEC Logic Symbol



Truth Table

Α	Υ
L	Н
Н	L

Operating Ranges

Characteristics	Symbol	Value	Unit	
Power supply voltage	V _{CC}	0.9~3.6	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	\/a	0~3.6 (Note 4)	V	
	V _{OUT}	0~V _{CC} (Note 5)	\ \ \	
Output Current		±8.0 (Note 6)		
		±4.0 (Note 7)		
		±3.0 (Note 8)	mA	
	I _{OH} /I _{OL}	±1.7 (Note 9)	IIIA	
		±0.3 (Note 10)		
		±0.02 (Note 11)		
Operating temperature	T _{opr}	−40~85	°C	
Input rise and fall time	dt/dV	0~10 (Note 12)	ns/V	

Note 4: $V_{CC} = 0V$

Note 5: High or Low state

Note 6: V_{CC} = 3.0~3.6 V

Note 7: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 8: V_{CC} = 1.65~1.95 V

Note 9: V_{CC} = 1.4~1.6 V

Note 10: V_{CC} = 1.1~1.3 V

Note 11: V_{CC} = 0.9 V

Note 12: V_{IN} = 0.8~2.0 V, V_{CC} = 3.0 V

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition						Ta = 25°C			Ta = -40~85°C		l lmi4
		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit			
					0.9	V _{CC}	_	_	V_{CC}	_	
High le					1.1~1.3	V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	
	High level	V _{IH}		_	1.4~1.6	V _{CC} × 0.65		_	V _{CC} × 0.65		
					1.65~ 1.95	V _{CC} × 0.65	_	_	V _{CC} × 0.65		
					2.3~2.7	1.7	_		1.7	_	
Input voltage					3.0~3.6	2.0			2.0		V
mpat voltago					0.9	_		GND		GND	·
					1.1~1.3	_	1	V _{CC} × 0.3		$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	
	Low level	V _{IL}	_		1.4~1.6	_		V _{CC} × 0.35		V _{CC} × 0.35	
					1.65~ 1.95	_	_	V _{CC} × 0.35		V _{CC} × 0.35	
					2.3~2.7	_	_	0.7	_	0.7	1
				_	_	8.0	_	0.8			
				I _{OH} =-0.02 mA	0.9	0.75	_	_	0.75	_	
		Vон	V _{IN} = V _{IL}	$I_{OH} = -0.3 \text{ mA}$	1.1~1.3	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
	High level			I _{OH} = -1.7 mA	1.4~1.6	V _{CC} × 0.75	_	_	V _{CC} × 0.75		
				I _{OH} = -3.0 mA	1.65~ 1.95	V _{CC} -0.45		_	V _{CC} -0.45		
				I _{OH} = -4.0 mA	2.3~2.7	2.0	_	_	2.0	_	
Output valta a				I _{OH} = -8.0 mA	3.0~3.6	2.48	_		2.48	_	
Output voltage				I _{OL} = 0.02 mA	0.9	_	_	0.1	_	0.1	V
			V _{IN} = V _{IH}	I _{OL} = 0.3 mA	1.1~1.3	_	_	V _{CC} × 0.25		V _{CC} × 0.25	
	Low level	V _{OL}		I _{OL} = 1.7 mA	1.4~1.6	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	
				I _{OL} = 3.0 mA	1.65~ 1.95	_	_	0.45		0.45	
				I _{OL} = 4.0 mA	2.3~2.7	_	_	0.4	_	0.4	
				I _{OL} = 8.0 mA	3.0~3.6	_		0.4		0.4	
Input leakage cu	rrent	I _{IN}	V _{IN} = 0~5.5V		0~3.6	_	_	±0.1	_	±1.0	μΑ
Power off leakag	Power off leakage current I _{OFF} V _{IN} = V _{OUT}		V _{IN} = 0~5. V _{OUT} = 0~	IN = 0~5.5V OUT = 0~3.6V		_		1.0		10.0	μА
Quiescent supply current I_{CC} $V_{IN} = V_{CC}$ or GND		or GND	3.6	_	_	1.0	_	10.0	μΑ		

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AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
Characteristics	Syllibol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time		C_L = 10 pF, R_L = 1 M Ω	0.9	_	24.4	_	_	_	
			1.1~1.3	_	11.6	21.7	1.0	40.5	
			1.4~1.6	_	6.5	9.8	1.0	11.6	
			1.65~ 1.95	_	4.9	7.0	1.0	7.6	
			2.3~2.7	_	3.2	4.4	1.0	4.9	
	^t pLH ^t pHL		3.0~3.6	_	2.4	3.5	1.0	4.1	
		C_L = 15 pF, R_L = 1 M Ω	0.9	_	26.9	_	_	_	ns
			1.1~1.3	_	12.7	24.2	1.0	42.1	
			1.4~1.6	_	7.1	10.7	1.0	12.9	
			1.65~ 1.95	_	5.3	7.5	1.0	7.7	
			2.3~2.7	_	3.5	4.8	1.0	5.5	
			3.0~3.6	_	2.7	3.8	1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	37.0	_	_		
			1.1~1.3	_	17.1	33.9	1.0	64.1	
			1.4~1.6	_	9.3	14.3	1.0	17.4	
			1.65~ 1.95	_	6.9	9.8	1.0	10.2	
			2.3~2.7	_	4.6	6.2	1.0	6.6	
			3.0~3.6		3.7	4.8	1.0	5.2	
Input capacitance	C _{IN}	_	3.6	_	3	_	_	_	pF
Power dissipation capacitance	C _{PD}	(Note 13)	0.9 ~ 3.6	_	10	_		_	pF

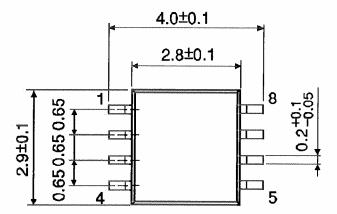
Note 13: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

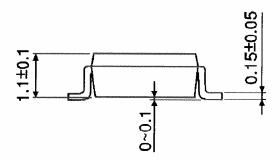
Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$

Package Dimensions

SSOP8-P-0.65 Unit: mm



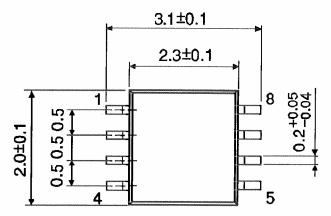


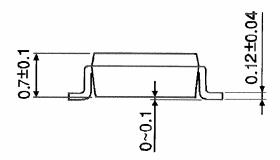
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Weight: 0.02 g (typ.)

Package Dimensions

SSOP8-P-0.50A Unit: mm





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Weight: 0.01 g (typ.)

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20070701-EN GENERAL

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