

MDTL LOGIC DIAGRAMS

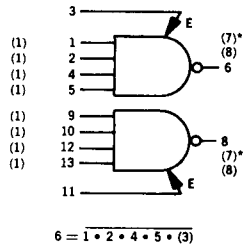
Numbers at ends of terminals represent pin numbers. Numbers in parenthesis indicate loading.

"G" Package: V_{cc} = pin 10, Gnd = pin 5 unless otherwise noted.

"F" and "P" Packages: V_{cc} = pin 14, Gnd = pin 7 unless otherwise noted.

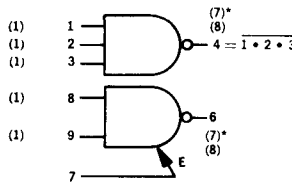
GATES

**MC830F, MC830P, MC930F
MC861F, MC861P, MC961F**
Dual 4-Input Gate NAND/NOR



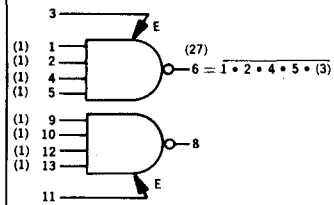
*APPLIES TO MC861F, MC861P,
MC961F

**MC830G, MC930G
MC861G, MC961G**
Dual 3-2 Input Gate NAND/NOR

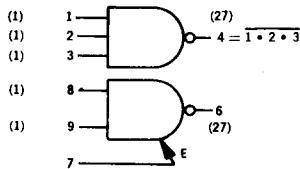


*APPLIES TO MC861G, MC961G

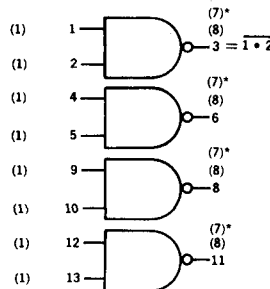
MC844F, MC844P, MC944F
Dual 4-Input Power Gate NAND/NOR



MC844G, MC944G
Dual 3-2 Input Power Gate
NAND/NOR

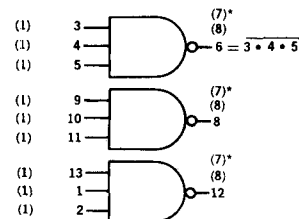


**MC846F, MC846P, MC946F
MC849F, MC849P, MC949F**
Quad 2-Input Gate NAND/NOR



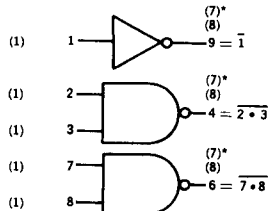
*APPLIES TO MC849F, MC849P,
MC949F

**MC862F, MC862P, MC962F
MC863F, MC863P, MC963F**
Triple 3-Input Gate NAND/NOR



*APPLIES TO MC863F, MC863P,
MC963F

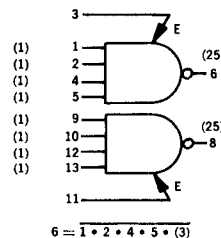
**MC862G, MC962G
MC963G, MC963G**
Dual 2-Input Gate NAND/NOR
Plus Inverter



*APPLIES TO MC863G, MC963G

BUFFERS

MC832F, MC832P, MC932F
Dual 4-Input Buffer



MC932G, MC932G
Dual 3-2 Input Buffer

