

Application	Part No.	V_R (V)	V_{RM} (V)	I_o (mA)	t_{rr} Max. (ns)	C_T Max. (pF)	I_{surge} 60Hz 1~ (A)	Package	Circuits
High-speed Switching	DAN201	80	80	100	4	3.5	—	FTR	Fig.1
	DAN209	80	80	100	4	3.5	—	TO-92	Fig.1
	DAN209S	80	80	100	4	3.5	—	SPT	Fig.1
	DAN215	80	80	100	4	3.5	—	FTL	Fig.1
	DAN401	80	80	25	4	3.5	—	SIP5Pin	Fig.2
	DAN601	80	80	25	4	3.5	—	SIP7Pin	Fig.3
	DAN801	80	80	25	4	3.5	—	SIP9Pin	Fig.4
	DAN803	80	80	25	4	3.5	—	SIP9Pin	Fig.5
	DAP201	80	80	100	4	3.5	—	FTR	Fig.6
	DAP209	80	80	100	4	3.5	—	TO-92	Fig.6
	DAP209S	80	80	100	4	3.5	—	SPT	Fig.6
	DAP215	80	80	100	4	3.5	—	FTL	Fig.6
	DAP401	80	80	25	4	3.5	—	SIP5Pin	Fig.7
	DAN403	80	80	100	4	3.5	—	LF9Pin	Fig.13
	DAP601	80	80	25	4	3.5	—	SIP7Pin	Fig.8
	DAP801	80	80	25	4	3.5	—	SIP9Pin	Fig.9
	DAP803	80	80	25	4	3.5	—	SIP9Pin	Fig.10
$2 V_{F8}$ connected in series	DA203	20	20	100	—	4	—	FTR	Fig.11
	DA210S	20	20	100	—	4	—	SPT	Fig.11
	DA216	20	20	100	—	4	—	FTL	Fig.11
	DA218S	80	80	100	—	3.5	—	SPT	Fig.12
Rectifying	DAN208	100	150	1000	—	20	10.0	ATR	Fig.14
	DAN213	100	150	1000	—	20	10.0	ATV	Fig.14
	DAP208	100	150	1000	—	20	10.0	ATR	Fig.15
	DAP213	100	150	1000	—	20	10.0	ATV	Fig.15

Internal circuit



Fig.1

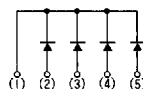


Fig.2

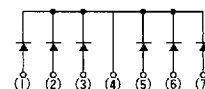


Fig.3

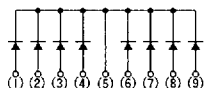


Fig.4

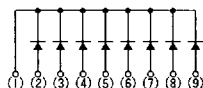


Fig.5



Fig.6

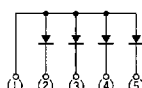


Fig.7

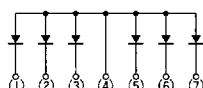


Fig.8

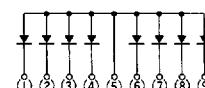


Fig.9

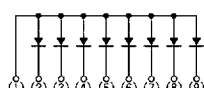


Fig.10



Fig.11



Fig.12

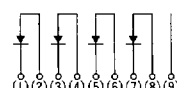


Fig.13



Fig.14



Fig.15

Diode Arrays

(Unit : mm)

SPT	TO-92	FTR
ATR	SIP 5 pin	SIP 7 pin
SIP 9 pin	LF 9 pin	FTL TL2
FTL TL3	FTL TL4	ATV TV2
ATV TV3	ATV TV4	ATV TV6

7828999 0009696 661