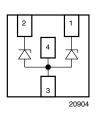
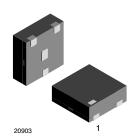


# Low Capacitance, 2-Line ESD Protection Diode





#### **MARKING** (example only)



Dot = pin 1 marking

YY = type code (see table below)

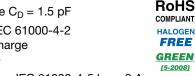
XX = date code

#### **DESIGN SUPPORT TOOLS** click logo to get started



#### **FEATURES**

- Compact LLP75-4L package
- Low package height < 0.6 mm
- 2-line ESD protection
- Low leakage current < 0.1 μA</li>
- Low load capacitance C<sub>D</sub> = 1.5 pF
- ESD immunity acc. IEC 61000-4-2
  - ± 15 kV contact discharge
  - ± 15 kV air discharge



- High surge current acc. IEC 61000-4-5 I<sub>PP</sub> > 3 A
- Soldering can be checked by standard vision inspection. no X-ray necessary
- e4 precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

ORDERING INFORMATION					
DEVICE NAME ORDERING CODE		TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY		
VBUS052BD-HTF	VBUS052BD-HTF-GS08	3000	15 000		

PACKAGE DATA						
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VBUS052BD-HTF	LLP75-4L	U7	4.2 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	260 °C/10 s at terminals

ABSOLUTE MAXIMUM RATINGS VBUS052BD-HTF						
RATING	TEST CONDITIONS	SYMBOL	VALUE	UNIT		
Peak pulse current	Acc. IEC 61000-4-5, t <sub>p</sub> = 8/20 μs/single shot	I <sub>PPM</sub>	3	А		
Peak pulse power	Acc. IEC 61000-4-5, t <sub>P</sub> = 8/20 μs/single shot	P <sub>PP</sub>	45	W		
F0D :	Contact discharge acc. IEC 61000-4-2; 10 pulses		± 15	kV		
ESD immunity	Air discharge acc. IEC 61000-4-2; 10 pulses	$V_{ESD}$	± 15	kV		
Operating temperature	Junction temperature	$T_J$	-40 to +125	°C		
Storage temperature		T <sub>STG</sub>	-40 to +150	°C		

#### **APPLICATION NOTE**

The VBUS052BD-HTF is a two-line ESD protection device with the characteristic of a Z-diode with a high ESD immunity and a very low capacitance which makes it usable for high frequency applications like USB2.0 or HDMI.

With the VBUS052BD-HTF two high speed data lines can be protected against transient voltage signals like ESD (electro static discharge). Connected to the data line (pin 1 and 2) and to ground (pin 3) negative transients will be clamped close below the ground level while positive transients will be clamped close above the 5 V working range. The clamping behavior of the VBUS052BD-HTF is bidirectional but asymmetrical (BiAs) and so it offers the best protection for applications running up to 5 V.

ELECTRICAL CHARACTERISTICS VBUS052BD-HTF								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N <sub>channel</sub>	-	-	2	lines		
Reverse stand-off voltage	at $I_R = 0.1 \mu A$ ; pin 1 or pin 2 to pin 3	$V_{RWM}$	-	-	5	V		
Reverse current	at $V_R = V_{RWM} = 5 \text{ V}$ ; pin 1 or pin 2 to pin 3	I <sub>R</sub>	-	< 0.01	0.1	μΑ		
Reverse breakdown voltage	at $I_R = 1$ mA; pin 1 or pin 2 to pin 3	$V_{BR}$	6.9	7.9	8.7	V		
Reverse clamping voltage	at I <sub>PP</sub> = 3 A, acc. IEC 61000-4-5; pin 1 or pin 2 to pin 3	V <sub>C</sub>	-	-	16	V		
Forward clamping voltage	at I <sub>F</sub> = 3 A, acc. IEC 61000-4-5; pin 3 to pin 1 or pin 2	V <sub>F</sub>	-	4.8	6	V		
Capacitance	at $V_R = 0$ V; $f = 1$ MHz; pin 1 or pin 2 to pin 3	C <sub>D</sub>	-	1.5	2.5	pF		

#### Note

#### **TYPICAL CHARACTERISTICS**

T<sub>amb</sub> = 25 °C, unless otherwise specified

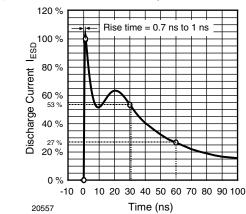


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330  $\Omega$ /150 pF)

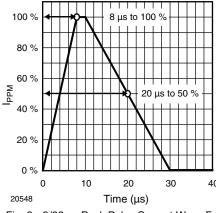


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

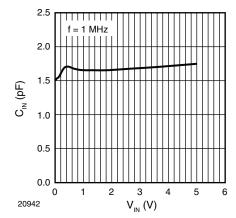


Fig. 3 - Typical Capacitance  $C_{\text{D}}$  vs. Reverse Voltage  $V_{\text{R}}$ 

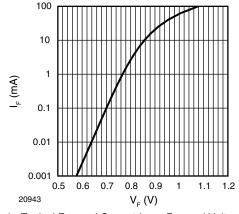


Fig. 4 - Typical Forward Current  $I_{\text{F}}$  vs. Forward Voltage  $V_{\text{F}}$ 

<sup>•</sup> Ratings at 25 °C, ambient temperature unless otherwise specified



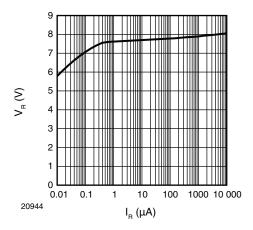


Fig. 5 - Typical Reverse Voltage V<sub>R</sub> vs. Reverse Current I<sub>R</sub>

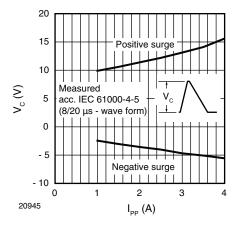


Fig. 6 - Typical Clamping Voltage vs. Peak Pulse Current I<sub>PP</sub>

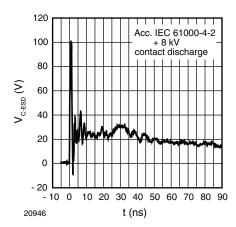


Fig. 7 - Typical Clamping Performance at + 8 kV Contact Discharge (acc. IEC 61000-4-2)

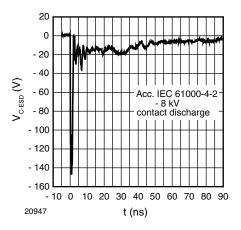


Fig. 8 - Typical Clamping Performance at - 8 kV Contact Discharge (acc. IEC 61000-4-2)

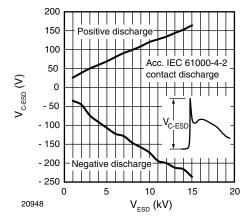
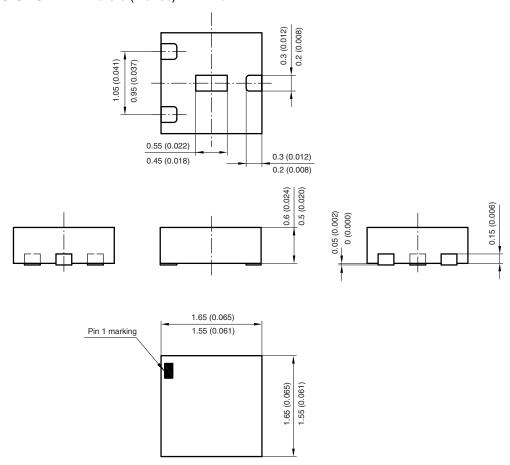


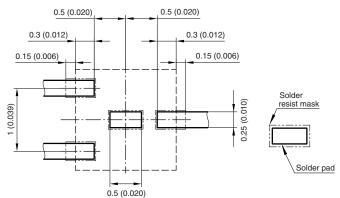
Fig. 9 - Typical Peak Clamping Voltage at ± ESD Contact Discharge (acc. IEC 61000-4-2)

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#### PACKAGE DIMENSIONS in millimeters (inches): LLP75-4L

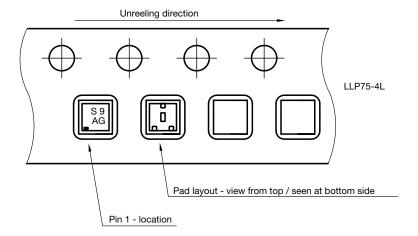


#### Foot print recommendation:



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