



OPA342 OPA2342 OPA4342

www.ti.com

# Low-Cost, Low-Power, Rail-to-Rail OPERATIONAL AMPLIFIERS *MicroAmplifier*™ Series

# **FEATURES**

- LOW QUIESCENT CURRENT: 150µA typ
- RAIL-TO-RAIL INPUT
- RAIL-TO-RAIL OUTPUT (within 1mV)
- **SINGLE SUPPLY CAPABILITY**
- LOW COST
- MicroSIZE PACKAGE OPTIONS: SOT23-5 MSOP-8 TSSOP-14
- BANDWIDTH: 1MHz
  SLEW RATE: 1V/µs
  THD + NOISE: 0.006%

# **APPLICATIONS**

- COMMUNICATIONS
- PCMCIA CARDS
- DATA ACQUISITION
- PROCESS CONTROL
- AUDIO PROCESSING
- ACTIVE FILTERS
- TEST EQUIPMENT
- CONSUMER ELECTRONICS

# **DESCRIPTION**

The OPA342 series rail-to-rail CMOS operational amplifiers are designed for low-cost, low-power, miniature applications. They are optimized to operate on a single supply as low as 2.5V with an input commonmode voltage range that extends 300mV beyond the supplies.

Rail-to-rail input/output and high-speed operation make them ideal for driving sampling Analog-to-Digital Converters (ADC). They are also well suited for general-purpose and audio applications and providing I/V conversion at the output of Digital-to-Analog Converters (DAC). Single, dual, and quad versions have identical specs for design flexibility.

The OPA342 series offers excellent dynamic response with a quiescent current of only 250µA max. Dual and quad designs feature completely independent circuitry for lowest crosstalk and freedom from interaction.

PACKAGE	SINGLE OPA342	DUAL OPA2342	QUAD OPA4342
SOT23-5	~		
MSOP-8		~	
SO-8	~	~	
TSSOP-14			V
SO-14			~
DIP-14			~

SPICE MODEL available at www.burr-brown.com.



# SPECIFICATIONS: $V_S = 2.7V$ to 5.5V

At  $T_A$  = +25°C,  $R_L$  = 10k $\Omega$  connected to  $V_S/2$  and  $V_{OUT}$  =  $V_S/2$ , unless otherwise noted. **Boldface** limits apply over the temperature range,  $T_A$  = -40°C to +85°C.

			OPA342NA, UA OPA2342EA, UA OPA4342EA, UA, PA				
PARAMETER		CONDITION	MIN	TYP	MAX	UNITS	
vs Power Supply $T_A = -40^{\circ}C \text{ to } +85^{\circ}C$ Channel Separation, dc $f = 1 \text{kHz}$	V <sub>os</sub> d <b>V<sub>os</sub>/dT</b> PSRR	$V_{\rm CM} = V_{\rm S}/2$ $V_{\rm S} = 2.7 {\rm V~to~5.5 V}, \ V_{\rm CM} < ({\rm V+})~-1.8 {\rm V}$ $V_{\rm S} = 2.7 {\rm V~to~5.5 V}, \ V_{\rm CM} < ({\rm V+})~-1.8 {\rm V}$		±1 ±1 ±3 30 0.2 132	±6 ±6 200 <b>250</b>	mV <b>mV</b> μV/°C μV/V μ <b>V/V</b> dB	
INPUT BIAS CURRENT Input Bias Current T <sub>A</sub> = -40°C to +85°C Input Offset Current	I <sub>B</sub>		±0.2   ±10   See Typical Curve   ±0.2   ±10		pA <b>pA</b> pA		
NOISE Input Voltage Noise, f = 0.1Hz to 50kH Input Voltage Noise Density, f = 1kHz Current Noise Density, f = 1kHz	Iz e <sub>n</sub> i <sub>n</sub>			8 30 0.5		μVrms nV/√Hz fA/√Hz	
INPUT VOLTAGE RANGE Common-Mode Voltage Range Common-Mode Rejection Ratio T <sub>A</sub> = -40°C to +85°C Common-Mode Rejection Ratio T <sub>A</sub> = -40°C to +85°C Common-Mode Rejection Ratio T <sub>A</sub> = -40°C to +85°C	V <sub>CM</sub> CMRR CMRR	$\begin{split} & V_S = +5.5 V,  -0.3 V < V_{CM} < (V+)  -  1.8 \\ & V_S = +5.5 V,  -0.3 V < V_{CM} < (V+)  -  1.8 \\ & V_S = +5.5 V,  -0.3 V < V_{CM} <  5.8 V \\ & V_S = +5.5 V,  -0.3 V < V_{CM} <  5.8 V \\ & V_S = +2.7 V,  -0.3 V < V_{CM} <  3 V \\ & V_S = +2.7 V,  -0.3 V < V_{CM} <  3 V \\ & V_S = +2.7 V,  -0.3 V <  V_{CM} <  3 V \\ \end{split}$	-0.3 76 <b>74</b> 66 <b>64</b> 62 <b>60</b>	88 78 74	(V+) + 0.3	V dB dB dB dB dB	
INPUT IMPEDANCE Differential Common-Mode				10 <sup>13</sup>    3 10 <sup>13</sup>    6		Ω    pF Ω    pF	
OPEN-LOOP GAIN Open-Loop Voltage Gain $T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$ $T_A = -40^{\circ}\text{C to } +85^{\circ}\text{C}$	A <sub>OL</sub>	$\begin{array}{l} R_L = 100 k\Omega, \ 10 \text{mV} < V_O < (\text{V+}) - 10 \text{mV} \\ R_L = 100 k\Omega, \ 10 \text{mV} < V_O < (\text{V+}) - 10 \text{mV} \\ R_L = 5 k\Omega, \ 400 \text{mV} < V_O < (\text{V+}) - 400 \text{mV} \\ R_L = 5 k\Omega, \ 400 \text{mV} < V_O < (\text{V+}) - 400 \text{mV} \end{array}$	104 <b>100</b> 96 <b>90</b>	124 114		dB <b>dB</b> dB <b>dB</b>	
FREQUENCY RESPONSE Gain-Bandwidth Product Slew Rate Settling Time, 0.1% 0.01% Overload Recovery Time Total Harmonic Distortion + Noise, f = 1kHz	GBW SR THD+N	$C_{L} = 100 pF$ $G = 1$ $V_{S} = 5.5 V, 2 V Step$ $V_{S} = 5.5 V, 2 V Step$ $V_{IN} \bullet G = V_{S}$ $V_{S} = 5.5 V, V_{O} = 3 V p - p^{(1)}, G = 1$		1 1 5 8 2.5 0.006		MHz V/μs μs μs μs	
OUTPUT Voltage Output Swing from Rail <sup>(2)</sup> $T_A = -40^{\circ}C \text{ to } +85^{\circ}C$ $T_A = -40^{\circ}C \text{ to } +85^{\circ}C$ Short-Circuit Current Capacitive Load Drive	I <sub>SC</sub> C <sub>LOAD</sub>	$\begin{split} R_L &= 100 k \Omega, \ A_{OL} \geq 96 dB \\ R_L &= 100 k \Omega, \ A_{OL} \geq 104 dB \\ R_L &= 100 k \Omega, \ A_{OL} \geq 100 dB \\ R_L &= 5 k \Omega, \ A_{OL} \geq 96 dB \\ R_L &= 5 k \Omega, \ A_{OL} \geq 90 dB \\ Per \ Channel \end{split}$		1 3 20 ±15 See Typical Cur	10 10 400 400	mV mV mV mV mA	
POWER SUPPLY Specified Voltage Range Operating Voltage Range Quiescent Current (per amplifier) T <sub>A</sub> = -40°C to +85°C	V <sub>S</sub>	I <sub>O</sub> = 0A	2.7	2.5 to 5.5 150	5.5 250 <b>300</b>	V V μΑ μ <b>Α</b>	
TEMPERATURE RANGE Specified Range Operating Range Storage Range Thermal Resistance SOT23-5 Surface Mount MSOP-8 Surface Mount SO-8 Surface Mount	$ heta_{\sf JA}$		-40 -55 -65	200 150 150	+85 +125 +150	°C °C °C °C/W °C/W °C/W	
SO-8 Surface Mount TSSOP-14 Surface Mount SO-14 Surface Mount DIP-14				150 100 100 100		°C/\ °C/\ °C/\	

NOTES: (1)  $V_{OUT} = 0.25V$  to 3.25V. (2) Output voltage swings are measured between the output and power-supply rails.



# **ABSOLUTE MAXIMUM RATINGS(1)**

Supply Voltage, V+ to V	
Signal Input Terminals, Voltage <sup>(2)</sup>	(V–) –0.5V to (V+) +0.5V
Current <sup>(2)</sup>	10mA
Output Short-Circuit(3)	Continuous
Operating Temperature	55°C to +125°C
Storage Temperature	65°C to +150°C
Junction Temperature	150°C
Lead Temperature (soldering, 10s)	300°C
ESD Tolerance (Human Body Model)	4000V

NOTES: (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability. These are stress ratings only. Functional operation of the device at these conditions, or beyond the specified operating conditions, is not implied. (2) Input terminals are diode-clamped to the power supply rails. Input signals that can swing more than 0.5V beyond the supply rails should be current-limited to 10mA or less. (3) Short-circuit to ground, one amplifier per package.

# ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Burr-Brown recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

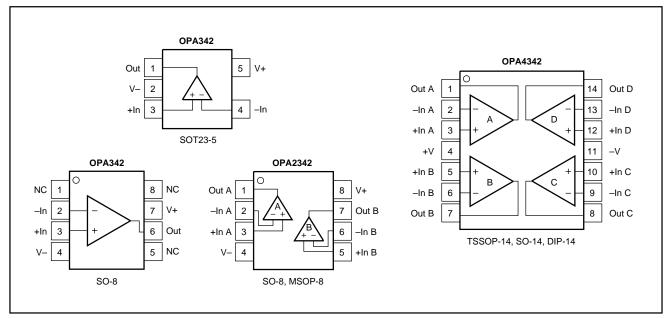
ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

## PACKAGE/ORDERING INFORMATION

PRODUCT	PACKAGE	PACKAGE DRAWING NUMBER	SPECIFIED TEMPERATURE RANGE	PACKAGE MARKING	ORDERING NUMBER <sup>(1)</sup>	TRANSPORT MEDIA
OPA342NA "OPA342UA	SOT23-5 " SO-8	331 " 182 "	-40°C to +85°C -40°C to +85°C	B42 " OPA342UA "	OPA342NA/250 OPA342NA/3K OPA342UA OPA342UA/2K5	Tape and Reel Tape and Reel Rails Tape and Reel
OPA2342EA OPA2342UA	MSOP-8 SO-8	337 " 182 "	-40°C to +85°C -40°C to +85°C	C42 " OPA2342UA "	OPA2342EA/250 OPA2342EA/2K5 OPA2342UA OPA2342UA/2K5	Tape and Reel Tape and Reel Rails Tape and Reel
OPA4342EA OPA4342UA	TSSOP-14 SO-14	357 " 235 "	-40°C to +85°C -40°C to +85°C	OPA4342EA " OPA4342UA "	OPA4342EA/250 OPA4342EA/2K5 OPA4342UA OPA4342UA/2K5	Tape and Reel Tape and Reel Rails Tape and Reel
OPA4342PA	DIP-14	010	-40°C to +85°C	OPA4342PA	OPA4342PA	Rails

NOTE: (1) Models with a slash (/) are available only in Tape and Reel in the quantities indicated (e.g., /3K indicates 3000 devices per reel). Ordering 3000 pieces of "OPA342NA/3K" will get a single 3000-piece Tape and Reel.

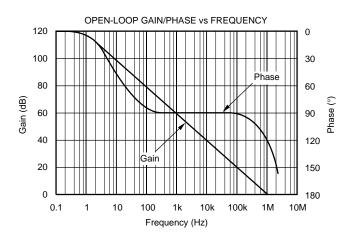
## **PIN CONFIGURATIONS**

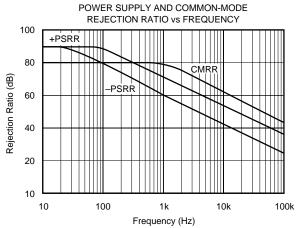


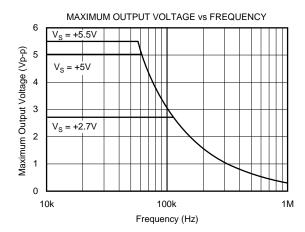


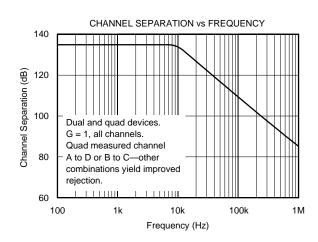
# TYPICAL PERFORMANCE CURVES

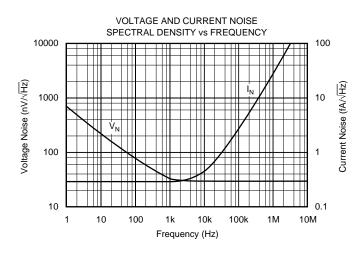
At  $T_A$  = +25°C,  $V_S$  = +5V, and  $R_L$  = 10k $\Omega$  connected to  $V_S/2$ , unless otherwise noted.

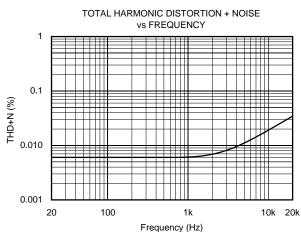








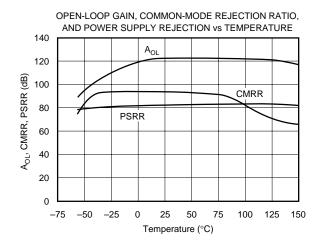


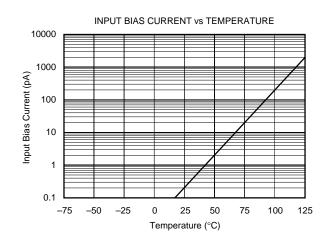


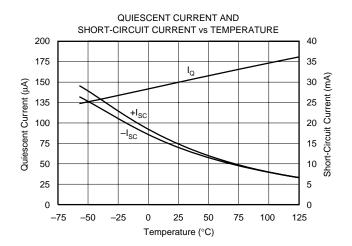


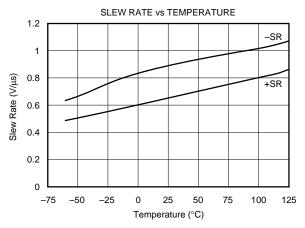
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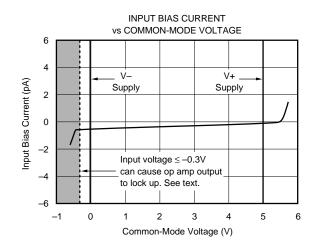
At  $T_A = +25^{\circ}C$ ,  $V_S = +5V$ , and  $R_L = 10k\Omega$  connected to  $V_S/2$ , unless otherwise noted.

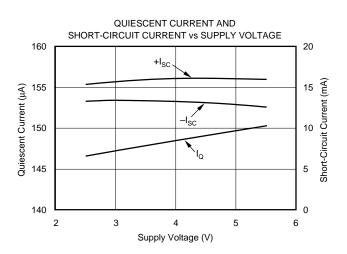








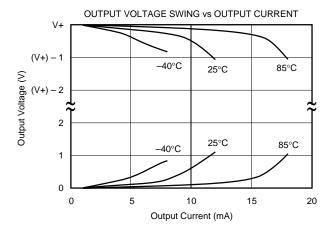


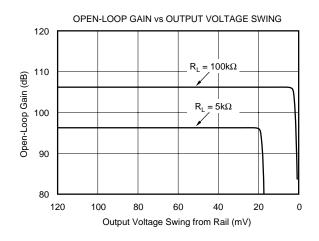


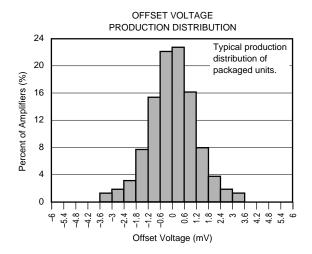


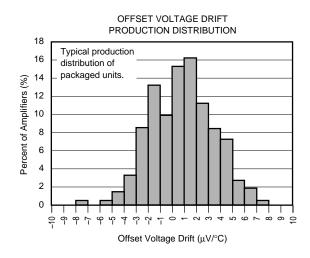
# **TYPICAL PERFORMANCE CURVES (Cont.)**

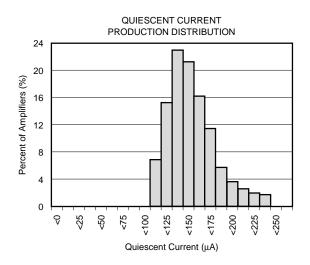
At  $T_A$  = +25°C,  $V_S$  = +5V, and  $R_L$  = 10k $\Omega$  connected to  $V_S/2$ , unless otherwise noted.

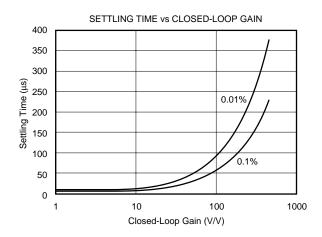








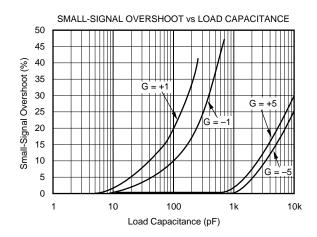


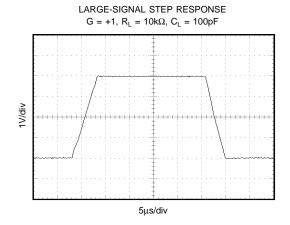


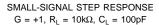


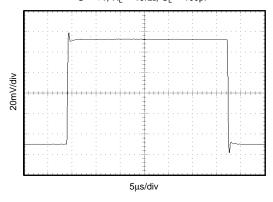
# **TYPICAL PERFORMANCE CURVES (Cont.)**

At  $T_A$  = +25°C,  $V_S$  = +5V, and  $R_L$  = 10k $\Omega$  connected to  $V_S/2$ , unless otherwise noted.











# APPLICATIONS INFORMATION

OPA342 series op amps are unity gain stable and can operate on a single supply, making them highly versatile and easy to use.

Rail-to-rail input and output swing significantly increases dynamic range, especially in low supply applications. Figure 1 shows the input and output waveforms for the OPA342 in unity-gain configuration. Operation is from  $V_S = +5V$  with a  $10k\Omega$  load connected to  $V_S/2$ . The input is a 5Vp-p sinusoid. Output voltage is approximately 4.997Vp-p.

Power supply pins should be by passed with  $0.01\mu F$  ceramic capacitors.

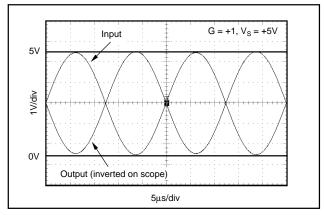


FIGURE 1. Rail-to-Rail Input and Output.

# **OPERATING VOLTAGE**

OPA342 series op amps are fully specified and guaranteed from +2.7V to +5.5V. In addition, many specifications apply from -40°C to +85°C. Parameters that vary significantly with operating voltages or temperature are shown in the Typical Performance Curves.

## **RAIL-TO-RAIL INPUT**

The input common-mode voltage range of the OPA342 series extends 300mV beyond the supply rails. This is achieved with a complementary input stage—an N-channel input differential pair in parallel with a P-channel differential pair (see Figure 2). The N-channel pair is active for input voltages close to the positive rail, typically (V+) - 1.3V to 300mV above the positive supply, while the P-channel pair is on for inputs from 300mV below the negative supply to approximately (V+) -1.3V. There is a small transition region, typically (V+) - 1.5V to (V+) - 1.1V, in which both pairs are on. This 400mV transition region can vary 300mV with process variation. Thus, the transition region (both stages on) can range from (V+) - 1.8V to (V+) - 1.4V on the low end, up to (V+) - 1.2V to (V+) - 0.8V on the high end. Within the 400mV transition region PSRR, CMRR, offset voltage, offset drift, and THD may be degraded compared to operation outside this region. For more information on designing with rail-to-rail input op amps, see Figure 3 "Design Optimization with Rail-to-Rail Input Op Amps."

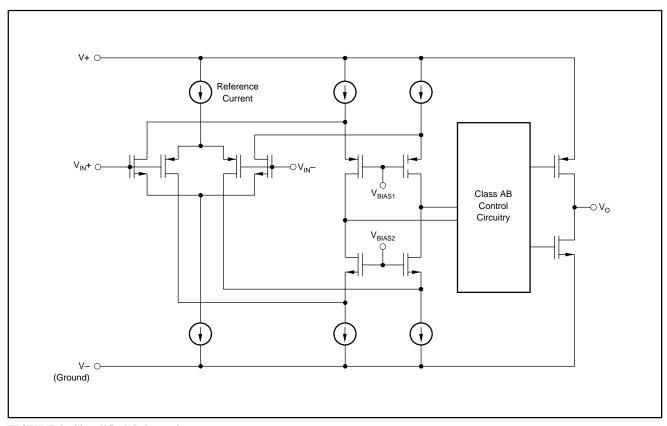


FIGURE 2. Simplified Schematic.



# **DESIGN OPTIMIZATION WITH RAIL-TO-RAIL INPUT OP AMPS**

Rail-to-rail op amps can be used in virtually any op amp configuration. To achieve optimum performance, however, applications using these special double-input-stage op amps may benefit from consideration of their special behavior.

In many applications, operation remains within the common-mode range of only one differential input pair. However some applications exercise the amplifier through the transition region of both differential input stages. Although the two input stages are laser trimmed for excellent matching, a small discontinuity may occur in this transition. Careful selection of the circuit configuration, signal levels and biasing can often avoid this transition region.

With a unity-gain buffer, for example, signals will traverse this transition at approximately 1.3V below V+ supply and may exhibit a small discontinuity at this point.

The common-mode voltage of the non-inverting amplifier is equal to the input voltage. If the input signal always remains less than the transition voltage, no discontinuity will be created. The closed-loop gain of this configuration can still produce a rail-to-rail output.

Inverting amplifiers have a constant common-mode voltage equal to  $V_{\rm B}$ . If this bias voltage is constant, no discontinuity will be created. The bias voltage can generally be chosen to avoid the transition region.

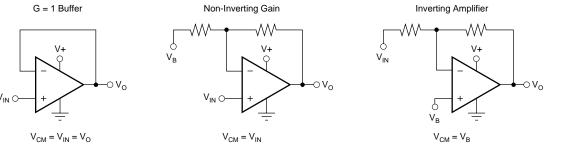


FIGURE 3. Design Optimization with Rail-to-Rail Input Op Amps.

## **COMMON-MODE REJECTION**

The CMRR for the OPA342 is specified in several ways so the best match for a given application may be used. First, the CMRR of the device in the common-mode range below the transition region ( $V_{CM} < (V+) - 1.8V$ ) is given. This specification is the best indicator of the capability of the device when the application requires use of one of the differential input pairs. Second, the CMRR at  $V_S = 5.5V$  over the entire common-mode range is specified. Third, the CMRR at  $V_S = 2.7V$  over the entire common-mode range is provided. These last two values include the variations seen through the transition region.

# INPUT VOLTAGE BEYOND THE RAILS

If the input voltage can go more than 0.3V below the negative power supply rail (single-supply ground), special precautions are required. If the input voltage goes sufficiently negative, the op amp output may lock up in an inoperative state. A Schottky diode clamp circuit will prevent this—see Figure 4. The series resistor prevents excessive current (greater than 10mA) in the Schottky diode and in the internal ESD protection diode, if the input voltage can exceed the positive supply voltage. If the signal source is limited to less than 10mA, the input resistor is not required.

# **RAIL-TO-RAIL OUTPUT**

A class AB output stage with common-source transistors is used to achieve rail-to-rail output. This output stage is capable of driving  $600\Omega$  loads connected to any potential

between V+ and ground. For light resistive loads (>  $50k\Omega$ ), the output voltage can typically swing to within 1mV from supply rail. With moderate resistive loads ( $2k\Omega$  to  $50k\Omega$ ), the output can swing to within a few tens of milli-volts from the supply rails while maintaining high open-loop gain. See the typical performance curve "Output Voltage Swing vs Output Current."

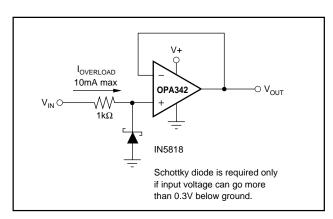


FIGURE 4. Input Current Protection for Voltages Exceeding the Supply Voltage.

# **CAPACITIVE LOAD AND STABILITY**

The OPA342 in a unity-gain configuration can directly drive up to 250pF pure capacitive load. Increasing the gain enhances the amplifier's ability to drive greater capacitive loads. See the typical performance curve "Small-Signal



Overshoot vs Capacitive Load." In unity-gain configurations, capacitive load drive can be improved by inserting a small ( $10\Omega$  to  $20\Omega$ ) resistor,  $R_S$ , in series with the output, as shown in Figure 5. This significantly reduces ringing while maintaining dc performance for purely capacitive loads. However, if there is a resistive load in parallel with the capacitive load, a voltage divider is created, introducing a dc error at the output and slightly reducing the output swing. The error introduced is proportional to the ratio  $R_S/R_L$ , and is generally negligible.

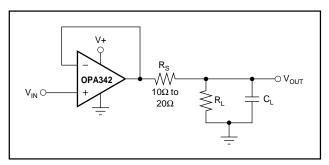


FIGURE 5. Series Resistor in Unity-Gain Configuration Improves Capacitive Load Drive.

## **DRIVING A/D CONVERTERS**

The OPA342 series op amps are optimized for driving medium-speed sampling ADCs. The OPA342 op amps buffer the ADC's input capacitance and resulting charge injection while providing signal gain.

Figures 6 shows the OPA342 in a basic noninverting configuration driving the ADS7822. The ADS7822 is a 12-bit, micro-power sampling converter in the MSOP-8 package. When used with the low-power, miniature packages of the OPA342, the combination is ideal for space-limited, low-power applications. In this configuration, an RC network at the ADC's input can be used to filter charge injection.

Figure 7 shows the OPA2342 driving an ADS7822 in a speech bandpass filtered data acquisition system. This small, low-cost solution provides the necessary amplification and signal conditioning to interface directly with an electret microphone. This circuit will operate with  $V_S = +2.7V$  to +5V with less than  $500\mu A$  quiescent current.

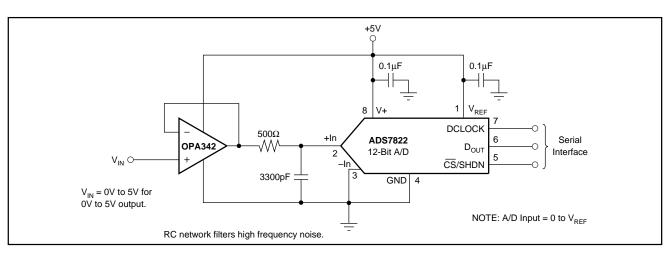


FIGURE 6. OPA342 in Noninverting Configuration Driving ADS7822.

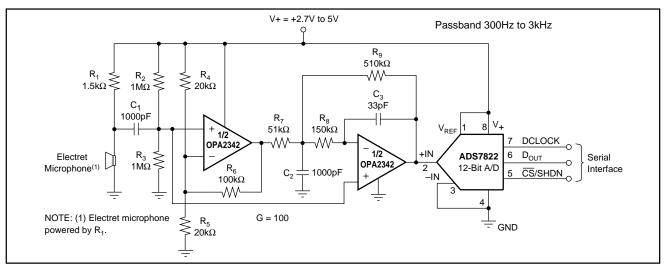


FIGURE 7. Speech Bandpass Filtered Data Acquisition System.



## **IMPORTANT NOTICE**

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TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Customers are responsible for their applications using TI components.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

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