

RJP4002ASA

Nch IGBT for Strobe Flash

REJ03G1473-0100

Rev.1.00

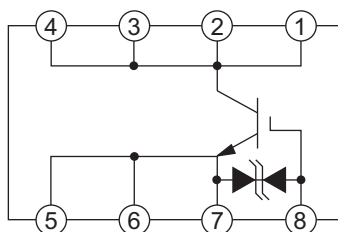
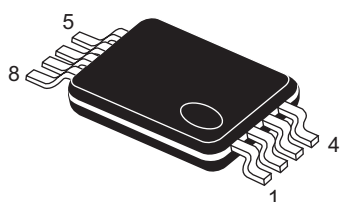
Oct 13, 2006

Features

- Small surface mount package (TSSOP-8)
- V_{CES} : 400 V
- I_{CM} : 150 A
- Drive voltage : 2.5 V

Outline

RENESAS Package code: PTSP0008JB-B
(Package name: TSSOP-8 <TTP-8DV>)



1, 2, 3, 4 : Collector
5, 6 : Emitter
7 : Emitter
(for the gate drive)
8 : Gate

Note: Pin 7 is for the gate drive only.

Note that current from the main circuit cannot flow into this section.(Please see page 3.)

Applications

Strobe flash for cameras

Maximum Ratings

(Tc = 25°C)

Parameter	Symbol	Ratings	Unit	Conditions
Collector-emitter voltage	V_{CES}	400	V	$V_{GE} = 0$ V
Gate-emitter voltage	V_{GES}	± 4	V	$V_{CE} = 0$ V
Peak gate-emitter voltage	V_{GEM}	± 6	V	$V_{CE} = 0$ V, $t_w = 10$ s
Collector current (Pulse)	I_{CM}	150	A	$C_M = 400$ μ F (see performance curve)
Junction temperature	T_j	- 40 to +150	°C	
Storage temperature	T_{stg}	- 40 to +150	°C	

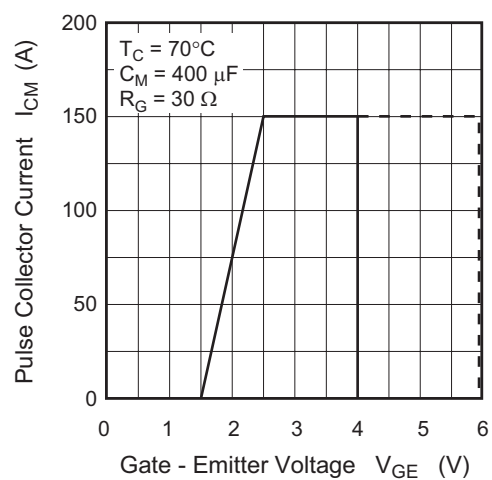
Electrical Characteristics

(T_j = 25°C)

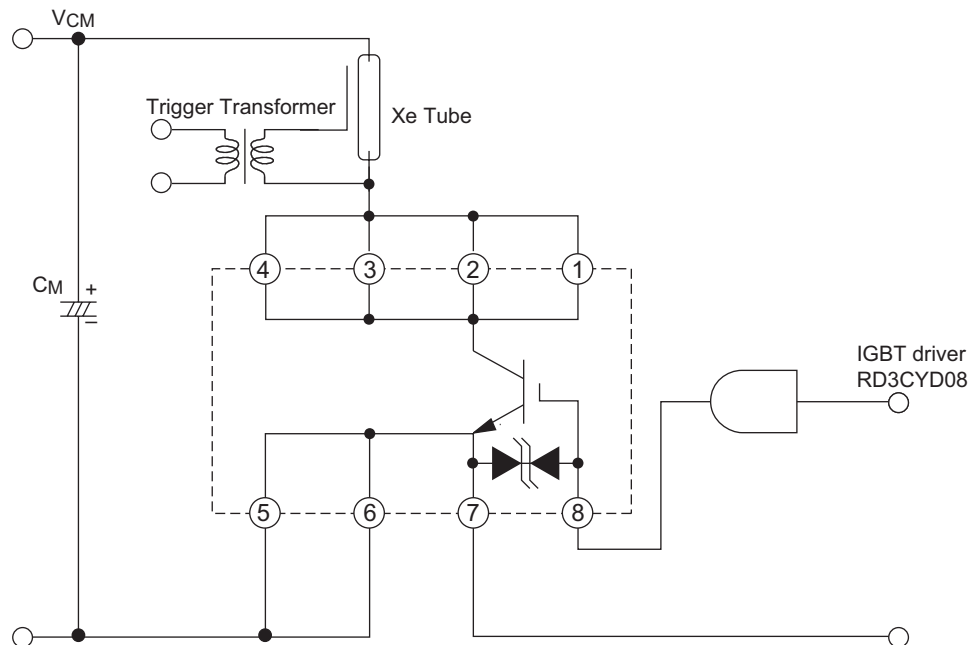
Parameter	Symbol	Min.	Typ.	Max.	Unit	Test conditions
Collector-emitter breakdown voltage	$V_{(BR)CES}$	450	—	—	V	$I_C = 1\text{ mA}$, $V_{GE} = 0\text{ V}$
Collector-emitter leakage current	I_{CES}	—	—	10	μA	$V_{CE} = 400\text{ V}$, $V_{GE} = 0\text{ V}$
Gate-emitter leakage current	I_{GES}	—	—	±10	μA	$V_{GE} = \pm 4\text{ V}$, $V_{CE} = 0\text{ V}$
Gate-emitter threshold voltage	$V_{GE(th)}$	0.4	0.6	1.2	V	$V_{CE} = 10\text{ V}$, $I_C = 1\text{ mA}$
Collector-emitter saturation voltage	$V_{CE(sat)}$	—	4.5	9.0	V	$I_C = 150\text{ A}$, $V_{GE} = 2.5\text{ V}$
Input capacitance	C_{ies}	—	6500	—	pF	$V_{CE} = 25\text{ V}$, $V_{GE} = 10\text{ V}$, $f = 1\text{ MHz}$

Performance Curves

Maximum Pulse Collector Current
(Conductive Capability in Strobe Flash Applications)



Application Example

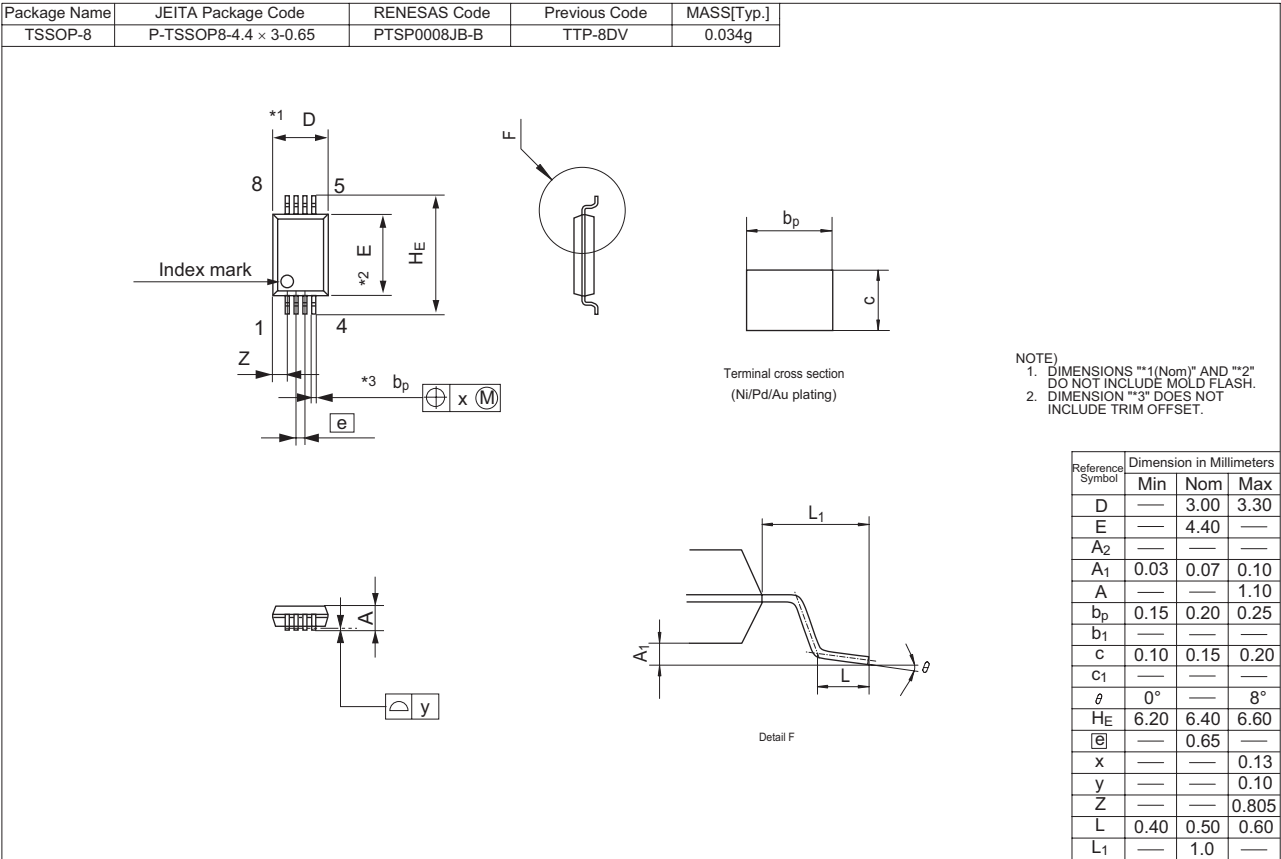


	Recommended Operation Conditions	Maximum Operation Conditions
V_{CM}	330 V	350 V
I_{CP}	130 A	150 A
C_M	300 μ F	400 μ F
V_{GE}	2.85 V	2.5 V

Precautions on Usage

1. IGBT has MOS structure and its gate is insulated by thin silicon oxide. So please handle carefully to protect the device from electrostatic charge.
2. Gate drive voltage during on-period must be applied to satisfy the rating of maximum pulse collector current. And turn-off dv/dt must become less than 400 V/ μ s. In general, when $R_{G(off)} = 30 \Omega$, it is satisfied.
3. The ground of the drive signal must be connected to pin 3 only. If the emitter terminal pins 1 and 2 in which a large currents flow are given to the device as the drive signal emitter, the device may be damaged due to large currents since the specified gate voltage is not applied to the IGBT within the device.
4. The operation life should be endured until repeated discharge of 5,000 times under the charge current ($I_{Xe} \leq 150$ A : full luminescence condition) of main capacitor. Repetition period under full luminescence condition is over 3 seconds.
5. Total operation hours applied to the gate-emitter voltage must be within 5,000 hours when V_{GE} is driven at 4 V.

Package Dimensions



Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping	3000	Type name – 00 – Q0	RJP4002ASA-00-Q0

Note: Please confirm the specification about the shipping in detail.

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450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510