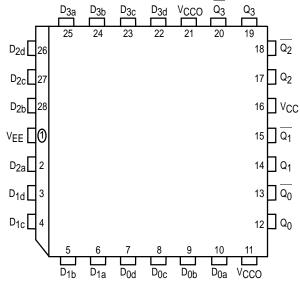
Quad 4-Input OR/NOR Gate

The MC10E/100E101 is a quad 4-input OR/NOR gate.

- 500ps Max. Propagation Delay
- Extended 100E VEE Range of 4.2V to 5.46V
- 75kΩ Input Pulldown Resistors

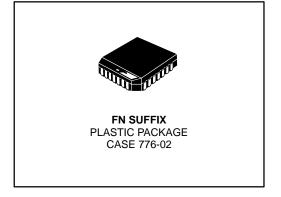
Pinout: 28-Lead PLCC (Top View)



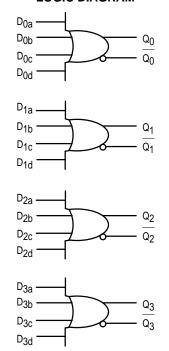
 * All VCC and VCCO pins are tied together on the die.

MC10E101 MC100E101

QUAD 4-INPUT OR/NOR GATE



LOGIC DIAGRAM



PIN NAMES

Pin	Function							
D _{0a} – D _{3d}	Data Inputs							
Q ₀ – Q3	True Outputs							
$\overline{Q_0} - \overline{Q_3}$	Inverting Outputs							

12/93

REV 2

DC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
lН	Input HIGH Current			150			150			150	μΑ	
IEE	Power Supply Current										mA	
	10E	1	30	36		30	36		30	36		
	100E		30	36		30	36		35	42		

AC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

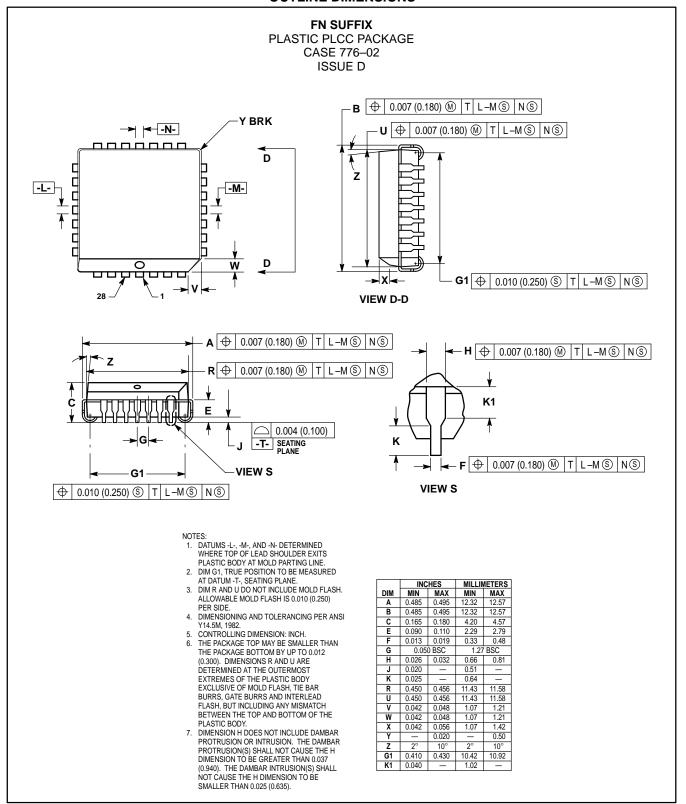
		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
^t PLH ^t PHL	Propagation Delay to Output D to Q	200	350	500	200	350	500	200	350	500	ps	
^t SKEW ^t SKEW	Within-Device Skew Within-Gate Skew		50 25			50 25			50 25		ps	1 2
t _r	Rise/Fall Time 20 - 80%	300	380	575	300	380	575	300	380	575	ps	

^{1.} Within-device skew is defined as identical transitions on similar paths through a device.

MOTOROLA 2–2

^{2.} Within-gate skew is defined as the variation in propagation delays of a gate when driven from its different inputs.

OUTLINE DIMENSIONS



MC10E101 MC100E101

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