

AN-2205 LM25118 Evaluation Board

1 Introduction

The LM25118 Evaluation Board is designed to provide the design engineer with a fully functional, Emulated Current Mode Control, buck-boost power converter to evaluate the LM25118 controller IC. The evaluation board provides a 12 V output with 3 A of output current capability. The evaluation board's wide input voltage range is from 42 V to 5 V, with operation down to 3 V with some component changes. The evaluation board operates at 300 kHz, a good compromise between conversion efficiency, tradeoffs between buck and buck-boost mode requirements, and converter size. The printed circuit board consists of 4 layers with 2 ounce copper top and bottom, and 1 ounce copper on internal layers. The board is constructed with FR4 material. This user's guide contains the evaluation board schematic, bill-of-materials (BOM) and a quick setup procedure. For more complete circuit and design information, see the *LM25118/LM25118Q Wide Voltage Range Buck-Boost Controller* ([SNVS726](#)) data sheet and quick start.

The performance of the evaluation board is as follows:

- Input Range: 42 V to less than 5 V at Full Current
- Operation to 3 V at Reduced Current and Appropriate Adjustments*
- Output Voltage: 12 V
- Output Current: 0 to 3 A
- Frequency of Operation: 300 kHz
- Board Size: 3.45 X 2.65 inches
- Load Regulation: 1%
- Line Regulation: 0.1%
- Over-Current Limiting
- Operation with V_{IN} Greater or Less than V_{OUT}

*Operation at full current to around 3 V is possible with current limit sense resistor, UVLO threshold, and corresponding C_{ramp} adjustment. Additional input capacitance may be required. For more details, see the *LM25118/LM25118Q Wide Voltage Range Buck-Boost Controller* ([SNVS726](#)) data sheet.

2 IC Features

- Integrated High and Low Side Driver
- Internal High Voltage Bias Regulator
- Wide Input Voltage Range: 5 V to 42 V
- Emulated Current Mode Control
- Single Inductor Architecture
- V_{OUT} Operation below and above V_{IN}
- Single Resistor Sets Oscillator Frequency
- Oscillator Synchronization Capability
- Programmable Soft-Start
- Ultra Low (<10 μ A) Shutdown Current
- Enable Input
- Wide Bandwidth Error Amplifier

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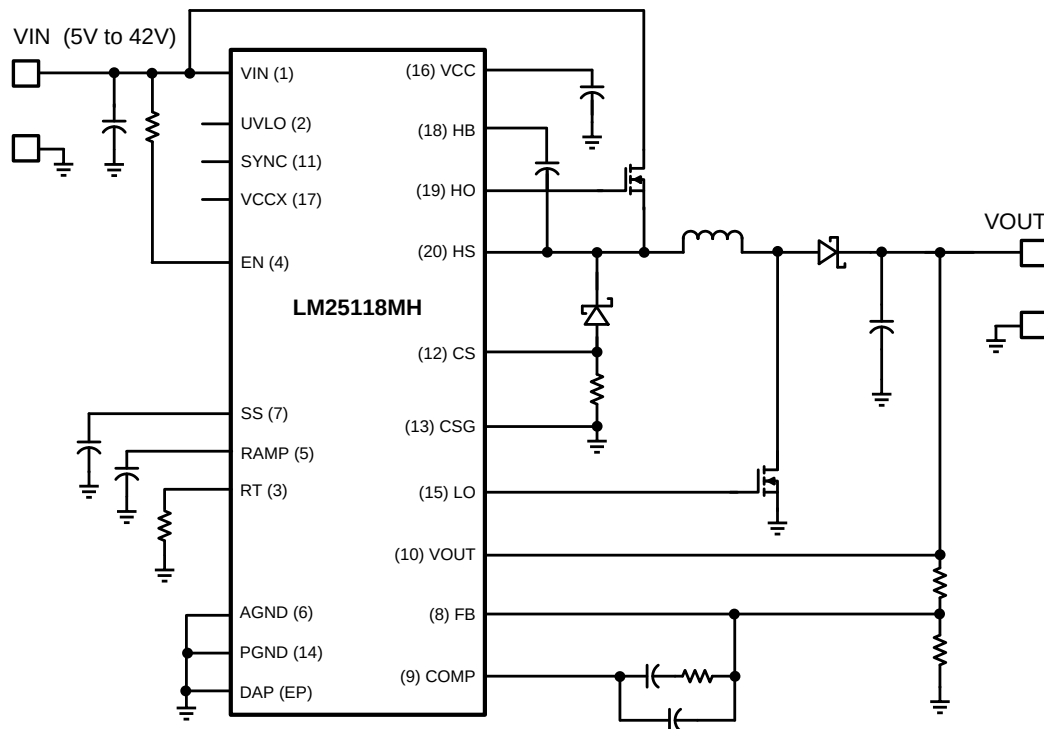
- Adjustable Output Voltage 1.23 V – 38 V
- 1.5% Feedback Reference Accuracy
- Thermal Shutdown
- No V_{IN} to V_{OUT} Connection during Fault Protection

3 Package

HTSSOP-20EP (Exposed Pad)

4 Application Circuit

See the detailed LM25118EVAL schematic at [Figure 17](#).



5 Efficiency

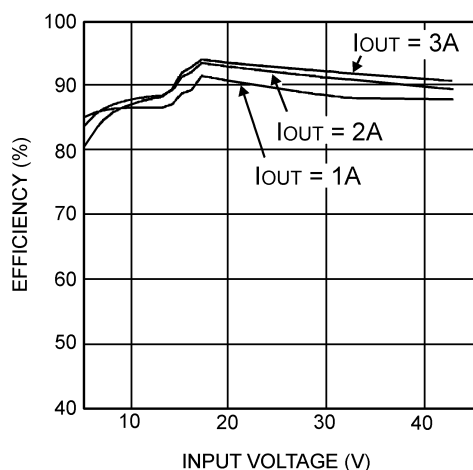


Figure 1. Efficiency

Figure 1 illustrates the efficiency of the converter vs. input voltage and output current. These curves highlight the high efficiency of the converter, especially considering the simplicity of design offered by a non synchronous implementation. Note the discontinuity in the curves at approximately 17 V and 13 V which represent mode transition boundaries. The lower efficiencies in the buck-boost region reflect additional losses at higher input and inductor currents. The decrease in efficiency at higher input voltages represents higher switching losses.

6 Air Flow

Prolonged operation without airflow at low input voltage and at full power will cause the MOSFET's and Diodes to overheat. A fan with a minimum of 200 LFM should always be provided. Figure 2 illustrates the temperature rise of various components with no airflow. The ambient was 25°C, and V_{IN} was 8 V.

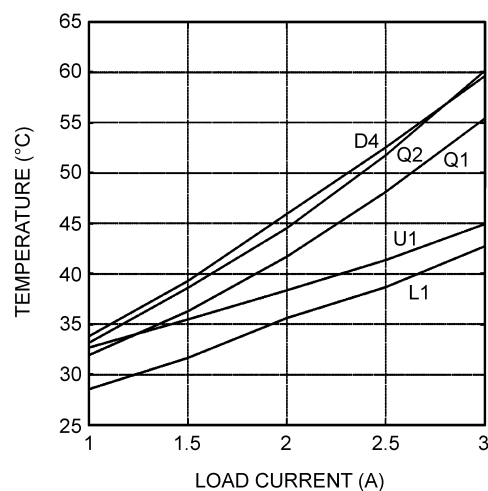


Figure 2. Temperature vs Load Current With No Airflow – 25°C Ambient

7 Powering Up

Connecting the IC's enable pin to ground will allow powering up the source supply with a minimal output load. Set the current limit of the source supply to provide about 1.5 times the anticipated wattage of the load. Note that input currents become very high at low input voltages, which requires an appropriate input supply. As you remove the connection from the enable pin to ground, immediately check for 12 V at the output.

A quick efficiency check is the best way to confirm that everything is operating properly. If something is amiss, you can be reasonable sure that it will affect the efficiency adversely. Few parameters can be incorrect in a switching power supply without creating losses and potentially damaging heat.

8 Over Current Protection

The evaluation board is configured with over-current protection. The output current is limited to approximately 4.5 A in the buck-boost mode. The 4.5 A value allows for component tolerances to specify a 3 A output current. Note this current will be almost double, or about 7 A in buck mode (V_{IN} greater than 17 V) due to the difference in peak inductor currents in the two different modes.

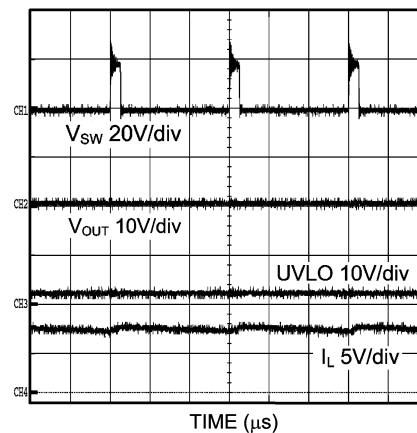


Figure 3. Short Circuit Current

9 VCCX

A place for a jumper between VOUT and VCCX is provided on the PC board. If operation below about 5 V is required, connect the jumper to allow VCCX to power the converter (the exact voltage depends on the gate drive requirements of the switching FETs). The converter does require a minimum V_{IN} of 5 V to initially start. When running, the input voltage can decrease to below 5 V at reduced current with VCCX connected to VOUT. Note that this design uses a current limit value to specify a full 3 A of output current at a minimum V_{IN} of 5 V. For operation lower than 5 V, the current limit resistor, UVLO threshold, and ramp capacitor must be re-calculated. Caution: make sure the input supply can source the required input current. Operation at low V_{IN} at full power may overheat and damage the MOSFET's and Diodes supplied on the board. Note there is a limit of 14 V applied to VCCX. Never exceed this value if operating VCCX from an external source, or operating the board with V_{OUT} greater than 12 V. To prevent oscillation, connect an additional 100 μ F or greater electrolytic capacitor across V_{IN} for input voltages less than 5 V.

10 Mode Transition

With V_{OUT} set at 12 V, the LM25118 applications board will operate in the buck mode with V_{IN} greater than about 17 V. As V_{IN} is reduced below 17 V, the converter begins to operate in a soft buck-boost mode. As V_{IN} is decreased below 14 V, the converter smoothly transitions to a pure buck-boost mode. This method of mode transition insures a smooth, glitch free operation as V_{IN} is varied over the transition region.

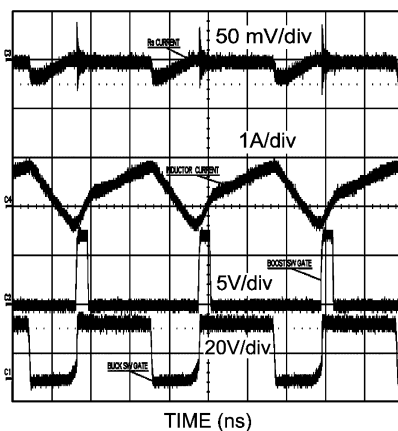


Figure 4. Mode Transition

[Figure 4](#) illustrates soft mode transition. The boost switch pulse-width is relatively narrow compared to the buck switch waveform. The boost switch pulse-width will gradually increase as V_{IN} decreases, and will eventually match and lock to the buck switch waveform. At this point, the converter enters full buck-boost operation.

11 Typical Waveforms

Note: All waveforms refer to Rev B design.

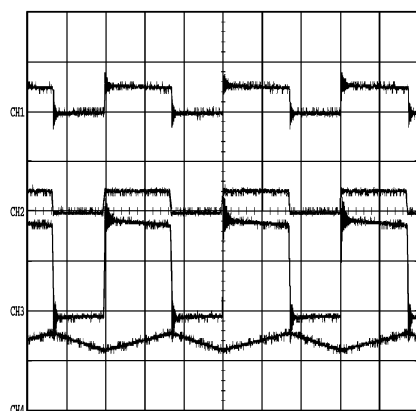


Figure 5. $V_{IN} = 10\text{ V}$, $I_{OUT} = 1\text{ A}$, illustrating Buck-Boost Operation

CH1: $V_{SW} = 20\text{V/div}$; CH2: $Q1 = 20\text{V/div}$;
CH3: $Q2 = 10\text{V/div}$; CH4: $I_L = 5\text{A/div}$

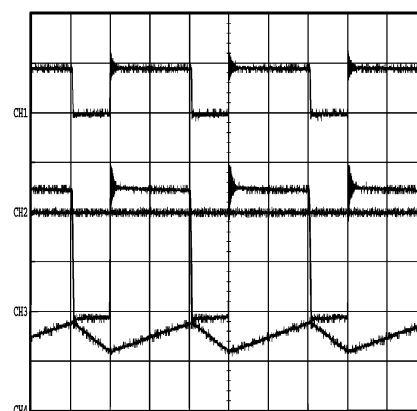


Figure 6. $V_{IN} = 18\text{ V}$, $I_{OUT} = 3\text{ A}$ Illustrating Buck Operation

CH1: $V_{SW} = 20\text{V/div}$; CH2: $Q1 = 20\text{V/div}$;
CH3: $Q2 = 10\text{V/div}$; CH4: $I_L = 2\text{A/div}$

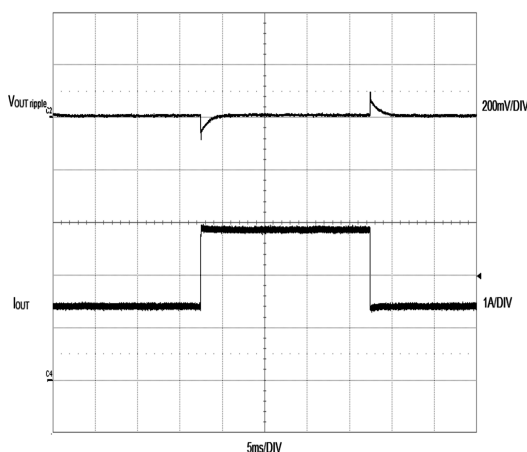


Figure 7. Buck Mode Transient Response

CH2: V_{OUT_ripple} (ac coupled); CH4: I_{OUT}

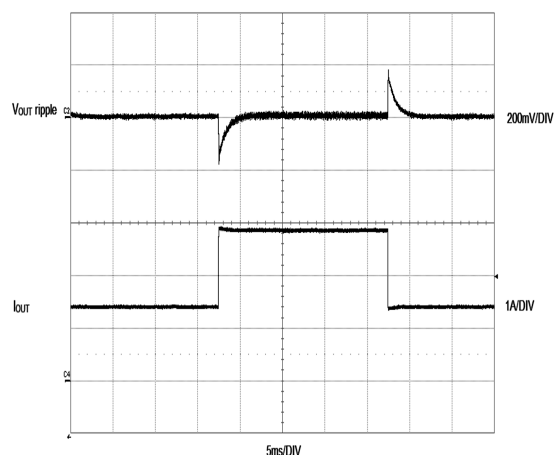


Figure 8. Buck-Boost Mode Transient Response

CH2: V_{OUT_ripple} (ac coupled); CH4: I_{OUT}

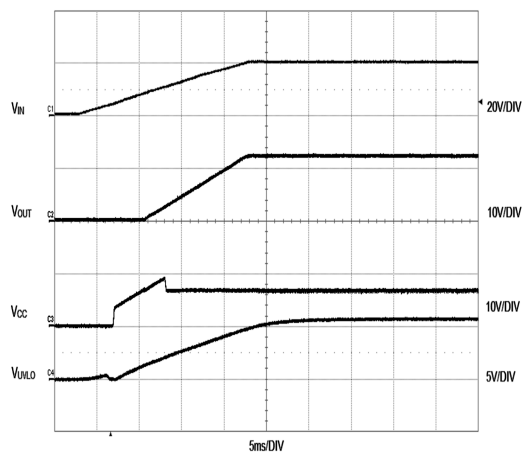


Figure 9. Start Up Waveforms

CH1: V_{IN} ; CH2: V_{OUT} ;

CH3: V_{CC} ; CH4: V_{UVLO}

12 Bill of Materials

Quant ity	Designator	Description	Manufacturer	PartNumber
5	C1, C2, C3, C4, C5	CAP, CERM, 2.2uF, 100V, +/-20%, X7R, 1812	TDK	C4532X7R2A225M
3	C6, C16, C17	CAP, CERM, 0.1uF, 100V, +/-10%, X7R, 0603	MuRata	GRM188R72A104KA35D
2	C7, C8	CAP, AL, 180uF, 16V, +/-20%, 0.026 ohm, SMD	Nippon Chemi-Con	APXH160ARA181MJ80G
2	C9, C10	CAP, CERM, 47uF, 16V, +/-10%, X5R, 1210	MuRata	GRM32ER61C476KE15L
2	C11, C12	CAP, CERM, 0.47uF, 25V, +/-10%, X7R, 1206	AVX	12063C474KAT2A
1	C13	CAP, CERM, 0.1uF, 100V, +/-10%, X7R, 0805	Kemet	C0805C104K1RACTU
1	C15	CAP, CERM, 2200pF, 100V, +/-10%, X7R, 0603	MuRata	GRM188R72A222KA01D
1	C18	CAP, CERM, 330pF, 100V, +/-5%, C0G/NP0, 0603	MuRata	GRM1885C2A331JA01D
2	C20, C21	CAP, CERM, 1uF, 25V, +/-10%, X7R, 0805	MuRata	GRM219R71E105KA88D
1	C22 (Rev A)	CAP, CERM, 4700pF, 100V, +/-10%, X7R, 0603	TDK	C1608X7R2A472K
1	C22 (Rev B)	CAP, CERM, 0.1uF, 100V, +/-10%, X7R, 0603	TDK	C1608X7S2A104K
1	D2	Diode, Schottky, 35V, 10A, DPAK	ON Semiconductor	MBRD1035CTLT4G
1	D3	Diode, Schottky, 100V, 20A, DDPAK	Vishay	VB40100C-E3/4W
4	J1, J2, J3, J4	Terminal, Turret, TH, Double	Keystone	1503-2
1	L1	Inductor, Shielded E Core, Ferrite, 10uH, 18A, 0.00186 ohm, SMD	Coilcraft	SER2915H-103KL
2	Q1, Q2	MOSFET, N-CH, 75V, 12A, PowerPAK SO-8	Vishay-Siliconix	SI7148DP
1	R2	RES, 75.0k ohm, 1%, 0.125W, 0805	Vishay-Dale	CRCW080575K0FKEA
1	R3	RES, 1.00Meg ohm, 1%, 0.125W, 0805	Vishay-Dale	CRCW08051M00FKEA
1	R4	RES, 0 ohm, 5%, 0.25W, 1206	Vishay-Dale	CRCW12060000Z0EA
1	R5	RES, 0 ohm, 5%, 0.1W, 0603	Vishay-Dale	CRCW06030000Z0EA
1	R6	RES, 0.015 ohm, 1%, 2W, 3008	Susumu Co Ltd	RL7520WT-R015-G
1	R8	RES, 2.67k ohm, 1%, 0.1W, 0603	Vishay-Dale	CRCW06032K67FKEA
1	R9	RES, 10.0 ohm, 1%, 0.1W, 0603	Vishay-Dale	CRCW060310R0FKEA
1	R10	RES, 10.0k ohm, 1%, 0.1W, 0603	Vishay-Dale	CRCW060310K0FKEA
1	R11	RES, 29.4k ohm, 0.1%, 0.1W, 0603	Yageo America	RT0603BRD0729K4L
1	R12 (Rev A)	RES, 16.2k ohm, 1%, 0.1W, 0603	Vishay-Dale	CRCW060316K2FKEA
1	R12 (Rev B)	RES, 18.2k ohm, 1%, 0.1W, 0603	Panasonic	ERJ-3EKF1822V
1	R14	RES, 309 ohm, 1%, 0.1W, 0603	Vishay-Dale	CRCW0603309RFKEA
2	TP1, TP9	Test Point, TH, Multipurpose, Black	Keystone	5011
1	TP2	Test Point, TH, Multipurpose, Red	Keystone	5010
2	TP3, TP4, TP7, TP8	Test Point, TH, Multipurpose, White	Keystone	5012
1	U1	Wide Voltage Range Buck-Boost Controller, 20-pin TSSOP-EP, Pb-Free	Texas Instrumtents	LM25118MH/NOPB
0	C14, C19, R7, R13	DNP		
0	D1	Diode, Schottky, 100V, 0.2A, SOD-123	ST Microelectronics	BAT41ZFILM
0	R1	RES, 0 ohm, 5%, 0.125W, 0805	Vishay-Dale	CRCW08050000Z0EA

13 Printed Circuit Board Layout

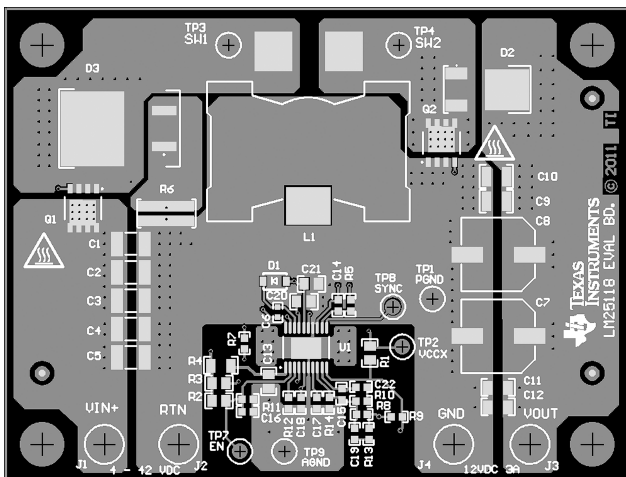


Figure 10. Top Layer as Viewed from Top

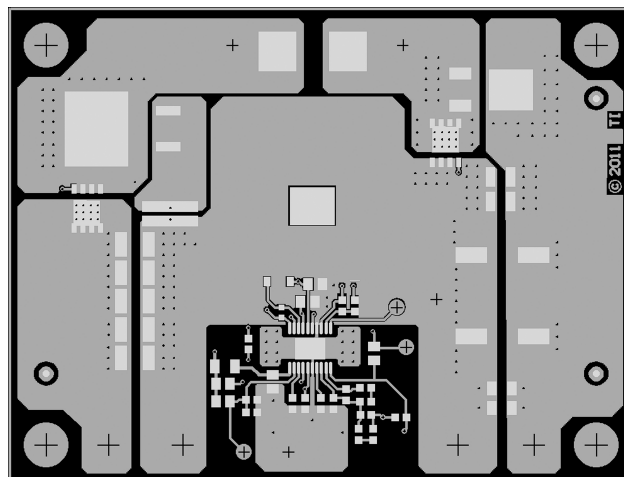


Figure 11. Copper Layer 1 (Top) as Viewed from Top

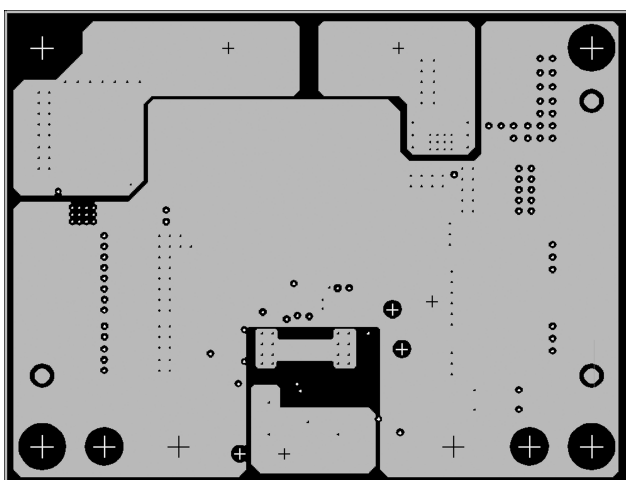


Figure 12. Copper Layer 2 (Mid-Layer 1) as Viewed from Top

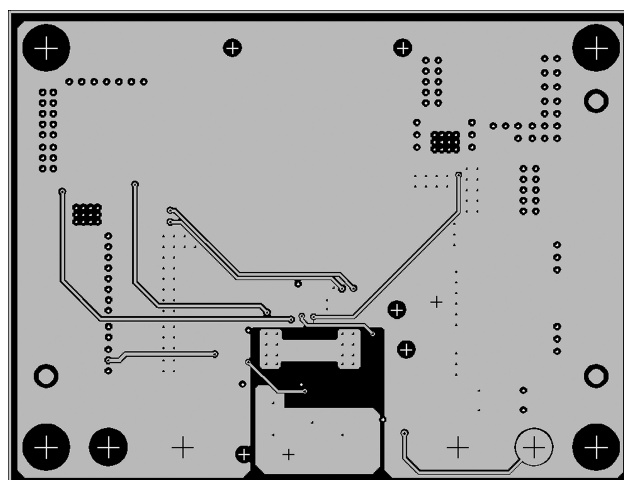


Figure 13. Copper Layer 3 (Mid-Layer 2) as Viewed from Top

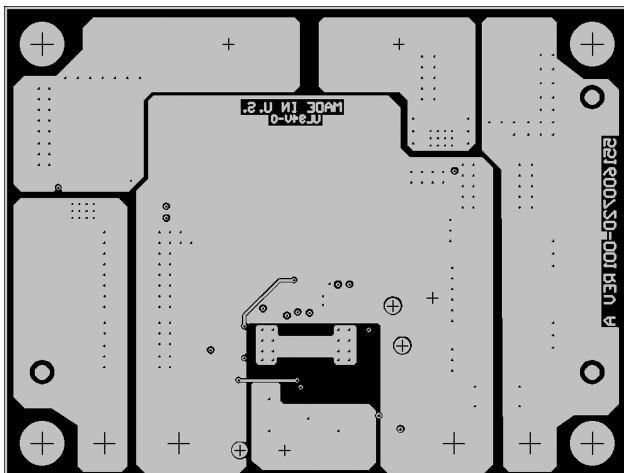


Figure 14. Copper Layer4 (Bottom) as Viewed from Top

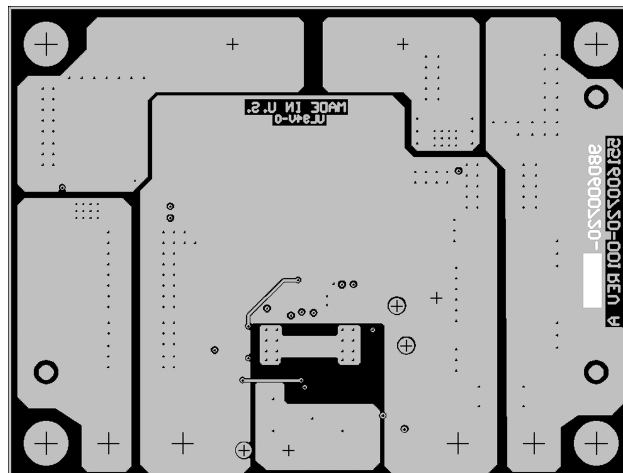
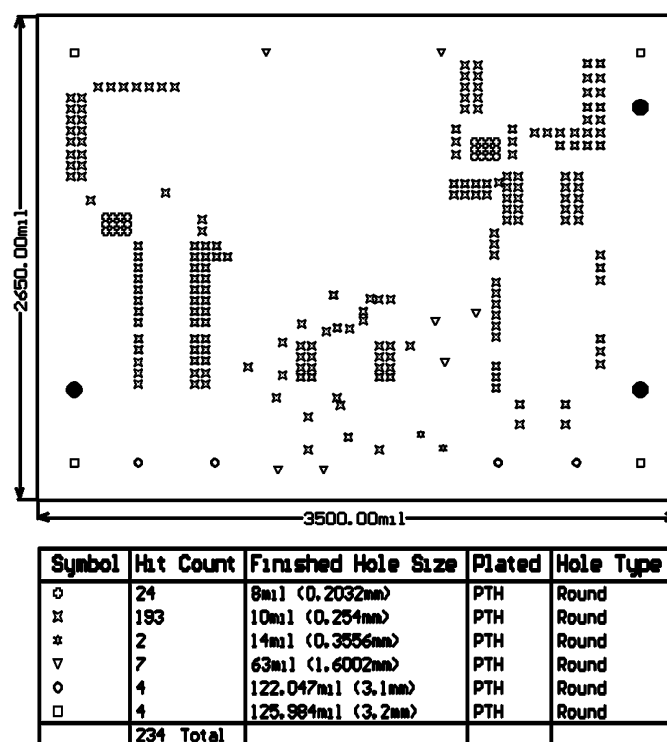


Figure 15. Bottom Layer as Viewed from Top



Drill Table

Figure 16. Drill Guide and Board Dimensions as Viewed from Top

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User Power/Frequency Use Obligations: This radio is intended for development/professional use only in legally allocated frequency and power limits. Any use of radio frequencies and/or power availability of this EVM and its development application(s) must comply with local laws governing radio spectrum allocation and power limits for this evaluation module. It is the user's sole responsibility to only operate this radio in legally acceptable frequency space and within legally mandated power limitations. Any exceptions to this are strictly prohibited and unauthorized by Texas Instruments unless user has obtained appropriate experimental/development licenses from local regulatory authorities, which is responsibility of user including its acceptable authorization.

For EVMs annotated as FCC – FEDERAL COMMUNICATIONS COMMISSION Part 15 Compliant

Caution

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

FCC Interference Statement for Class A EVM devices

This equipment has been tested and found to comply with the limits for a Class A digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instruction manual, may cause harmful interference to radio communications. Operation of this equipment in a residential area is likely to cause harmful interference in which case the user will be required to correct the interference at his own expense.

FCC Interference Statement for Class B EVM devices

This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

For EVMs annotated as IC – INDUSTRY CANADA Compliant

This Class A or B digital apparatus complies with Canadian ICES-003.

Changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

Concerning EVMs including radio transmitters

This device complies with Industry Canada licence-exempt RSS standard(s). Operation is subject to the following two conditions: (1) this device may not cause interference, and (2) this device must accept any interference, including interference that may cause undesired operation of the device.

Concerning EVMs including detachable antennas

Under Industry Canada regulations, this radio transmitter may only operate using an antenna of a type and maximum (or lesser) gain approved for the transmitter by Industry Canada. To reduce potential radio interference to other users, the antenna type and its gain should be so chosen that the equivalent isotropically radiated power (e.i.r.p.) is not more than that necessary for successful communication.

This radio transmitter has been approved by Industry Canada to operate with the antenna types listed in the user guide with the maximum permissible gain and required antenna impedance for each antenna type indicated. Antenna types not included in this list, having a gain greater than the maximum gain indicated for that type, are strictly prohibited for use with this device.

Cet appareil numérique de la classe A ou B est conforme à la norme NMB-003 du Canada.

Les changements ou les modifications pas expressément approuvés par la partie responsable de la conformité ont pu vider l'autorité de l'utilisateur pour actionner l'équipement.

Concernant les EVMs avec appareils radio

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence. L'exploitation est autorisée aux deux conditions suivantes : (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement.

Concernant les EVMs avec antennes détachables

Conformément à la réglementation d'Industrie Canada, le présent émetteur radio peut fonctionner avec une antenne d'un type et d'un gain maximal (ou inférieur) approuvé pour l'émetteur par Industrie Canada. Dans le but de réduire les risques de brouillage radioélectrique à l'intention des autres utilisateurs, il faut choisir le type d'antenne et son gain de sorte que la puissance isotrope rayonnée équivalente (p.i.r.e.) ne dépasse pas l'intensité nécessaire à l'établissement d'une communication satisfaisante.

Le présent émetteur radio a été approuvé par Industrie Canada pour fonctionner avec les types d'antenne énumérés dans le manuel d'usage et ayant un gain admissible maximal et l'impédance requise pour chaque type d'antenne. Les types d'antenne non inclus dans cette liste, ou dont le gain est supérieur au gain maximal indiqué, sont strictement interdits pour l'exploitation de l'émetteur.

【Important Notice for Users of EVMs for RF Products in Japan】

This development kit is NOT certified as Confirming to Technical Regulations of Radio Law of Japan

If you use this product in Japan, you are required by Radio Law of Japan to follow the instructions below with respect to this product:

1. Use this product in a shielded room or any other test facility as defined in the notification #173 issued by Ministry of Internal Affairs and Communications on March 28, 2006, based on Sub-section 1.1 of Article 6 of the Ministry's Rule for Enforcement of Radio Law of Japan,
2. Use this product only after you obtained the license of Test Radio Station as provided in Radio Law of Japan with respect to this product, or
3. Use of this product only after you obtained the Technical Regulations Conformity Certification as provided in Radio Law of Japan with respect to this product. Also, please do not transfer this product, unless you give the same notice above to the transferee. Please note that if you could not follow the instructions above, you will be subject to penalties of Radio Law of Japan.

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For Feasibility Evaluation Only, in Laboratory/Development Environments. Unless otherwise indicated, this EVM is not a finished electrical equipment and not intended for consumer use. It is intended solely for use for preliminary feasibility evaluation in laboratory/development environments by technically qualified electronics experts who are familiar with the dangers and application risks associated with handling electrical mechanical components, systems and subsystems. It should not be used as all or part of a finished end product.

Your Sole Responsibility and Risk. You acknowledge, represent and agree that:

1. You have unique knowledge concerning Federal, State and local regulatory requirements (including but not limited to Food and Drug Administration regulations, if applicable) which relate to your products and which relate to your use (and/or that of your employees, affiliates, contractors or designees) of the EVM for evaluation, testing and other purposes.
2. You have full and exclusive responsibility to assure the safety and compliance of your products with all such laws and other applicable regulatory requirements, and also to assure the safety of any activities to be conducted by you and/or your employees, affiliates, contractors or designees, using the EVM. Further, you are responsible to assure that any interfaces (electronic and/or mechanical) between the EVM and any human body are designed with suitable isolation and means to safely limit accessible leakage currents to minimize the risk of electrical shock hazard.
3. Since the EVM is not a completed product, it may not meet all applicable regulatory and safety compliance standards (such as UL, CSA, VDE, CE, RoHS and WEEE) which may normally be associated with similar items. You assume full responsibility to determine and/or assure compliance with any such standards and related certifications as may be applicable. You will employ reasonable safeguards to ensure that your use of the EVM will not result in any property damage, injury or death, even if the EVM should fail to perform as described or expected.
4. You will take care of proper disposal and recycling of the EVM's electronic components and packing materials.

Certain Instructions. It is important to operate this EVM within TI's recommended specifications and environmental considerations per the user guidelines. Exceeding the specified EVM ratings (including but not limited to input and output voltage, current, power, and environmental ranges) may cause property damage, personal injury or death. If there are questions concerning these ratings please contact a TI field representative prior to connecting interface electronics including input power and intended loads. Any loads applied outside of the specified output range may result in unintended and/or inaccurate operation and/or possible permanent damage to the EVM and/or interface electronics. Please consult the EVM User's Guide prior to connecting any load to the EVM output. If there is uncertainty as to the load specification, please contact a TI field representative. During normal operation, some circuit components may have case temperatures greater than 60°C as long as the input and output are maintained at a normal ambient operating temperature. These components include but are not limited to linear regulators, switching transistors, pass transistors, and current sense resistors which can be identified using the EVM schematic located in the EVM User's Guide. When placing measurement probes near these devices during normal operation, please be aware that these devices may be very warm to the touch. As with all electronic evaluation tools, only qualified personnel knowledgeable in electronic measurement and diagnostics normally found in development environments should use these EVMs.

Agreement to Defend, Indemnify and Hold Harmless. You agree to defend, indemnify and hold TI, its licensors and their representatives harmless from and against any and all claims, damages, losses, expenses, costs and liabilities (collectively, "Claims") arising out of or in connection with any use of the EVM that is not in accordance with the terms of the agreement. This obligation shall apply whether Claims arise under law of tort or contract or any other legal theory, and even if the EVM fails to perform as described or expected.

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