

**OBSOLETE**

## SPECIFICATIONS:

MATERIAL:-

BASE: LCP, NATURAL COLOR

COVER: SUS, T=0.15mm

TERMINAL: C5210-H, T=0.20mm

FINISH:-

COVER: Sn

TERMINAL: SELECTED Au/Sn-Cu

ELECTRICAL:-

CONTACT RESISTANCE: 40 mΩ MAX

INSULATION RESISTANCE: 100 MΩ MIN

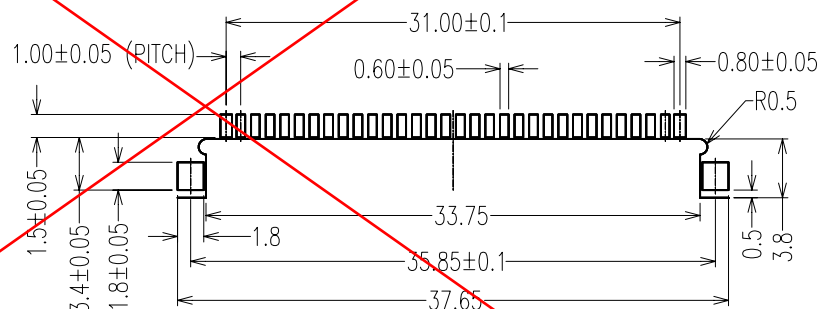
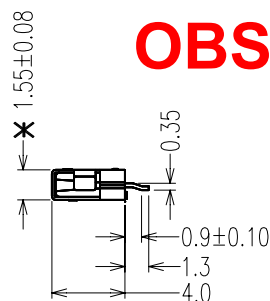
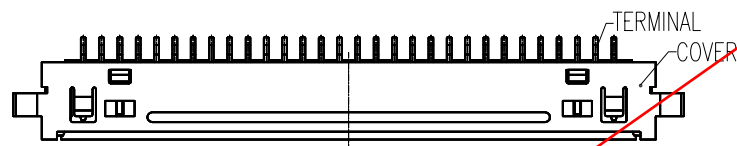
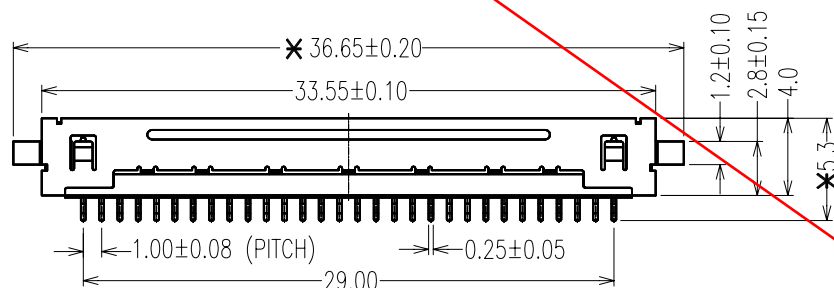
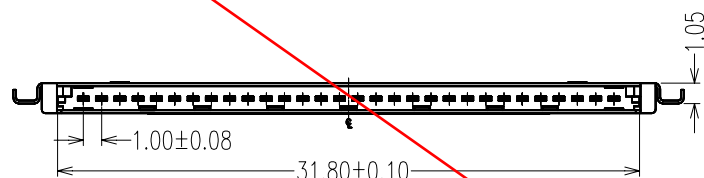
LEAD CO-PLANARITY: 0.1mm MAX

NOTES: ALL DIMENSIONS AND NOTES

MARKED \* ARE CRITICAL AND

MUST BE CHECKED IN ACCORDANCE

WITH QUALITY DOC. Q1-02A

APPLICABLE PCB DIMENSION (REF)  
(NOT TO SCALE)

## TOLERANCES

X. = ±0.25mm  
X.X = ±0.2mm  
X.XX = ±0.15mm**HARWIN**HARWIN USA  
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## TOLERANCES

X. = ±1mm  
X.X = ±0.25mm  
X.XX = ±0.10mm  
X.XXX = ±0.01mmANGLES = ±5°  
UNLESS STATED

## MATERIAL:

SEE NOTES

## FINISH:

SEE NOTES

## S/AREA:

mm<sup>2</sup>

## TITLE:

LVDS 30 PIN RECEPTACLE (REVERSE)

## DRAWING NUMBER:

M711-2103042R

SHT  
1  
OF 1

Allan	1	01.08.06	-
NAME	ISS.	DATE	C/NOTE
APPROVED: <i>Alan</i>			
CHECKED: <i>Alan</i>			
DRAWN: Allan			
CUSTOMER REF.:			
ASSEMBLY DRG:			

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