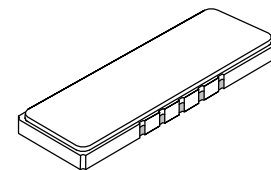


**SF1137A-1**

- **Designed for WLL Receiver Applications**
- **Low Insertion Loss**
- **Hermetic SMP-75 Surface-Mount Case**
- **Unbalanced Input and Output**

**61.0 MHz
SAW Filter****SMP-75****Absolute Maximum Ratings**

Rating	Value	Units
Maximum Incident Power in Passband	+10	dBm
Max. DC voltage between any 2 terminals	30	VDC
Storage Temperature Range	-40 to +85	°C
Max Soldering Profile	265°C for 10 s	

Electrical Characteristics

Characteristic	Sym	Notes	Min	Typ	Max	Units
Nominal Center Frequency	f_C	1	61.0			MHz
Passband	Insertion Loss at f_C	IL		13.0	15.0	dB
	1 dB Passband	BW_1	±825	±900		
	3 dB Passband	BW_3	±1000	±1050		kHz
	Group Delay Variation over f_C ±825 kHz	GDV		250	300	ns _{p-p}
Rejection	f_C -1.665 to f_C -1.5 and f_C +1.5 to f_C +1.665 MHz	1, 2, 3	20	24		dB
	f_C -2.5 to f_C -1.665 and f_C +1.665 to f_C +2.8 MHz		34	36		
	f_C -8.0 to f_C -2.5 and f_C +2.8 to f_C +8.0 MHz		37	42		
	f_C ±8.0 MHz		42	45		
	Ultimate			55		
Operating Temperature Range	T_A	1	-10		+85	°C

Impedance Matching to 50 Ω unbalanced	External L-C
Case Style	SMP-75 19. x 6.5 mm Nominal Footprint
Lid Symbolization (YY = year, WW = week)	RFM SF1137A YYWW

Notes:

1. Unless noted otherwise, all specifications apply over the operating temperature range with filter soldered to the specified demonstration board with impedance matching to 50 Ω and measured with 50 Ω network analyzer.
2. Unless noted otherwise, all frequency specifications are referenced to the nominal center frequency, f_C .
3. Rejection is measured as attenuation below the minimum IL point in the passband. Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42 for details.
4. "LRIP" or "L" after the part number indicates "low rate initial production" and "ENG" or "E" indicates "engineering prototypes."
5. The design, manufacturing process, and specifications of this filter are subject to change.
6. Either Port 1 or Port 2 may be used for either input or output in the design. However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.
7. US and international patents may apply.
8. RFM, stylized RFM logo, and RF Monolithics, Inc. are registered trademarks of RF Monolithics, Inc.
9. ©Copyright 1999, RF Monolithics Inc.
10. Electrostatic Sensitive Device. Observe precautions for handling.

**Electrical Connections**

Connection	Terminals
Port 1 Hot	10
Port 1 Gnd Return	1
Port 2 Hot	5
Port 2 Gnd Return	6
Case Ground	All others