

Blackfin® USB-LAN EZ-Extender®

Manual

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Analog Devices, Inc.
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The USB-LAN EZ-Extender is designed to be used solely in a laboratory environment. The board is not intended for use as a consumer end product or as a portion of a consumer end product. The board is an open system design which does not include a shielded enclosure and therefore may cause interference to other electrical devices in close proximity. This board should not be used in or near any medical equipment or RF devices.

The USB-LAN EZ-Extender has been certified to comply with the essential requirements of the European EMC directive 89/336/EEC amended by 93/68/EEC and therefore carries the “CE” mark.

The USB-LAN EZ-Extender has been appended to Analog Devices, Inc. Technical Construction File (TCF) referenced ‘DSPTOOLS1’ dated December 21, 1997 and was awarded CE Certification by an appointed European Competent Body as listed below.

Technical Certificate No: Z600ANA1.022

Issued by: Technology International (Europe) Limited
60 Shrivenham Hundred Business Park
Shrivenham, Swindon, SN6 8TY, UK



The EZ-KIT Lite evaluation system contains ESD (electrostatic discharge) sensitive devices. Electrostatic charges readily accumulate on the human body and equipment and can discharge without detection. Permanent damage may occur on devices subjected to high-energy discharges. Proper ESD precautions are recommended to avoid performance degradation or loss of functionality. Store unused EZ-KIT Lite boards in the protective shipping package.



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PREFACE

Thank you for purchasing the Blackfin[®] USB-LAN EZ-Extender[®], Analog Devices, Inc. extension board to the EZ-KIT Lite[®] evaluation system for ADSP-BF533, ADSP-BF537, and ADSP-BF561 Blackfin processors.

The Blackfin processors are embedded processors that support a Media Instruction Set Computing (MISC) architecture. This architecture is the natural merging of RISC, media functions, and digital signal processing characteristics towards delivering signal processing performance in a microprocessor-like environment.

The EZ-KIT Lite and USB-LAN EZ-Extender are designed to be used in conjunction with the VisualDSP++[®] development environment. VisualDSP++ offers a powerful programming tool with new flexibility that significantly decreases the time required to port software code to a processor, reducing time-to-market.

To learn more about Analog Devices development software, go to <http://www.analog.com/processors/tools/>.

Product Overview

The Blackfin USB-LAN EZ-Extender is a separately sold extension board that plugs onto the expansion interface of the ADSP-BF533, ADSP-BF537, or ADSP-BF561 EZ-KIT Lite evaluation system. The extension board aids the design and prototyping phases of the ADSP-BF533, ADSP-BF537, or ADSP-BF561 processor targeted applications.

The board extends the capabilities of the evaluation system by providing a connection between the asynchronous memory bus of the Blackfin processor (asynchronous memory bank 3) and either a USB 2.0 or a 10/100 Mbps Ethernet device.

The following is a list of the Blackfin USB-LAN EZ-Extender interfaces.

- USB 2.0 interface
 - ✓ PLX's Technology Netchip 2272 device
 - ✓ USB driver and application code
 - ✓ USB logo certified
- Ethernet interface
 - ✓ SMSC's LAN 91C111 device supported on the ADSP-BF533 EZ-KIT and ADSP-BF561 EZ-KIT Lites
 - ✓ IEEE802.3.AF compliant Power-Over-Ethernet (PoE) application on the ADSP-BF537 EZ-KIT Lite
 - ✓ SMSC's MII connector to evaluate different PHYs with the ADSP-BF537 EZ-KIT Lite
 - ✓ Ethernet stack and application code
- No power supply required
 - ✓ Derives power from EZ-KIT Lite
- CE certified

- Dimensions
 - ✓ 3.13 in (H) x 3.6 in (W)

Before using any of the interfaces, follow the setup procedure in [“USB-LAN EZ-Extender Setup” on page 1-1](#).

Example programs are available to demonstrate the capabilities of the Blackfin USB-LAN EZ-Extender board.

Purpose of This Manual

The *Blackfin USB-LAN EZ-Extender Manual* describes operation and configuration of the extension board components. A schematic and a bill of materials are provided as a reference for future Blackfin processor board designs.

Intended Audience

This manual is a user's guide and reference to the Blackfin USB-LAN EZ-Extender. Programmers who are familiar with the Analog Devices Blackfin processor architecture, operation, and development tools are the primary audience for this manual.

Programmers who are unfamiliar with VisualDSP++ or EZ-KIT Lite evaluation software should refer to the *ADSP-BF533*, *ADSP-BF537*, or *ADSP-BF561 Evaluation System Manual*, VisualDSP++ online Help, and user's or getting started guides. For the locations of these documents, refer to [“Product Information”](#).

Manual Contents

The manual consists of:

- Chapter 1, [“USB-LAN EZ-Extender Interfaces” on page 1-1](#)
Provides basic board information.
- Chapter 2, [“USB-LAN EZ-Extender Hardware Reference” on page 2-1](#)
Provides information on the hardware aspects of the board.
- Appendix A, [“USB-LAN EZ-Extender Bill Of Materials” on page A-1](#)
Provides a list of components used to manufacture the EZ-Extender board.
- Appendix B, [“USB-LAN EZ-Extender Schematic” on page B-1](#)
Provides the resources to allow EZ-KIT Lite board-level debugging or to use as a reference design. Appendix B is part of the online Help.

What’s New in This Manual

This edition of the *Blackfin USB-LAN EZ-Extender Manual* has been updated to reflect the latest revision of the board.

Technical or Customer Support

You can reach Analog Devices, Inc. Customer Support in the following ways:

- Visit the Embedded Processing and DSP products Web site at <http://www.analog.com/processors/technicalSupport>
- E-mail tools questions to processor.tools.support@analog.com
- E-mail processor questions to processor.support@analog.com (World wide support)
processor.europe@analog.com (Europe support)
processor.china@analog.com (China support)
- Phone questions to 1-800-ANALOGD
- Contact your Analog Devices, Inc. local sales office or authorized distributor
- Send questions by mail to:
Analog Devices, Inc.
One Technology Way
P.O. Box 9106
Norwood, MA 02062-9106
USA

Supported Products

The Blackfin USB-LAN EZ-Extender is designed as an extender board to the ADSP-BF533, ADSP-BF537, and ADSP-BF561 EZ-KIT Lite evaluation systems.

Product Information

You can obtain product information from the Analog Devices Web site, from the product CD-ROM, or from the printed publications (manuals).

Analog Devices is online at www.analog.com. Our Web site provides information about a broad range of products—analog integrated circuits, amplifiers, converters, and digital signal processors.

For information on product related development software, see the following publications.

Table 1. Related Processor Publications

Title	Description
• <i>ADSP-BF533 Embedded Processor Datasheet</i> • <i>ADSP-BF536/ADSP-BF537 Embedded Processor Datasheet</i> • <i>ASP-BF561 Blackfin Embedded Symmetric Multi-Processor Datasheet</i>	General functional description, pinout, and timing
• <i>ADSP-BF533 Blackfin Processor Hardware Reference</i> • <i>ADSP-BF537 Blackfin Processor Hardware Reference</i> • <i>ASP-BF561 Blackfin Processor Hardware Reference</i>	Description of internal processor architecture and all register functions
<i>Blackfin Processor Instruction Set Reference</i>	Description of all allowed processor assembly instructions

All documentation is available online. Most documentation is available in printed form.

Visit the Technical Library Web site to access all processor and tools manuals and data sheets:

<http://www.analog.com/processors/technicalSupport/technicalLibrary/>.

Table 2. Related VisualDSP++ Publications

Title	Description
• <i>ADSP-BF533 EZ-KIT Lite Evaluation System Manual</i> • <i>ADSP-BF537 EZ-KIT Lite Evaluation System Manual</i> • <i>ADSP-BF561 EZ-KIT Lite Evaluation System Manual</i>	Description of the EZ-KIT Lite features and usage. Note: For the ADSP-BF537 EZ-KIT Lite, there is additional <i>Getting Started with ADSP-BF537 EZ-KIT Lite</i>.
<i>VisualDSP++ User's Guide</i>	Description of VisualDSP++ features and usage
<i>VisualDSP++ Assembler and Preprocessor Manual</i>	Description of the assembler function and commands
<i>VisualDSP++ C/C++ Compiler and Library Manual for Blackfin Processors</i>	Description of the compiler function and commands for Blackfin processors
<i>VisualDSP++ Linker and Utilities Manual</i>	Description of the linker function and commands
<i>VisualDSP++ Loader and Utilities Manual</i>	Description of the loader function and commands

Notation Conventions

Text conventions used in this manual are identified and described as follows.

Example	Description
Close command (File menu)	Titles in reference sections indicate the location of an item within the VisualDSP++ environment's menu system (for example, the Close command appears on the File menu).
{this that}	Alternative required items in syntax descriptions appear within curly brackets and separated by vertical bars; read the example as this or that. One or the other is required.
[this that]	Optional items in syntax descriptions appear within brackets and separated by vertical bars; read the example as an optional this or that.

Notation Conventions

Example	Description
[this,...]	Optional item lists in syntax descriptions appear within brackets delimited by commas and terminated with an ellipse; read the example as an optional comma-separated list of this.
.SECTION	Commands, directives, keywords, and feature names are in text with letter gothic font.
<i>filename</i>	Non-keyword placeholders appear in text with italic style format.
	Note: For correct operation, ... A Note provides supplementary information on a related topic. In the online version of this book, the word Note appears instead of this symbol.
	Caution: Incorrect device operation may result if ... Caution: Device damage may result if ... A Caution identifies conditions or inappropriate usage of the product that could lead to undesirable results or product damage. In the online version of this book, the word Caution appears instead of this symbol.
	Warning: Injury to device users may result if ... A Warning identifies conditions or inappropriate usage of the product that could lead to conditions that are potentially hazardous for the devices users. In the online version of this book, the word Warning appears instead of this symbol.

1 USB-LAN EZ-EXTENDER INTERFACES

This chapter provides the setup procedures for both the Blackfin USB-LAN EZ-Extender and EZ-KIT Lite (ADSP-BF533, ADSP-BF537 or ADSP-BF561) and describes each of the interfaces the extender supports.

The information is presented in the following order.

- [“USB-LAN EZ-Extender Setup” on page 1-1](#)
- [“USB 2.0 Interface” on page 1-3](#)
- [“Ethernet Interface” on page 1-4](#)
- [“Optional ADSP-BF537 EZ-KIT Lite Interfaces” on page 1-5](#)

USB-LAN EZ-Extender Setup

It is very important to set up all components of the system containing the USB-LAN EZ-Extender before applying power to that system. The following procedure is recommended for the correct setup.

Power your system when these steps are completed:

1. Read the applicable design interface section in this chapter—the text provides an overview of the capabilities of the interface.
2. Read [“System Architecture” on page 2-2](#) to understand the physical connections of the extension board. For detailed information, refer to [“USB-LAN EZ-Extender Schematic” on page B-1](#).

USB-LAN EZ-Extender Setup

3. Remove any rubber feet that may be attached to the EZ-KIT Lite. In place of these rubber feet, install the four nylon feet and screws provided with the USB-LAN EZ-Extender. Install the nylon feet in the mounting holes of the EZ-KIT Lite's printed circuit board (PCB). Flip the EZ-KIT Lite upside down so that the three expansion headers (J1–3) are facing up.
4. Set the switches and jumpers on the USB-LAN EZ-Extender board. Use the block diagram in [Figure 2-1 on page 2-2](#) in conjunction with “[Jumper Settings](#)” on [page 2-3](#) and “[Switch Settings](#)” on [page 2-6](#).
5. Set the switches and jumpers on EZ-KIT Lite board. If not already, familiarize yourself with the documentation and schematics of the EZ-KIT Lite (see “[Product Information](#)”). Compare the expansion interface signals of the USB-LAN EZ-Extender board with the signals of the EZ-KIT Lite board to ensure there is no contention. For example, it may be necessary to disable other devices connected to the expansion interface of the processor and disable the push buttons on the EZ-KIT Lite.
6. Install the USB-LAN EZ-Extender on the EZ-KIT Lite via the three-connector expansion interface.
7. Configure any other interfacing boards, for example, another EZ-Extender board.

USB Software

For information on the USB software (host-side and device-side), refer to the USB Software Readme.txt file located in the
...\\VisualDSP++ install path\\Blackfin\\Examples\\USB-LAN EZ-EXTENDER\\USB directory.

Ethernet Software

For information on the LAN software, refer to the LAN Software Readme.txt file located in the ...\\VisualDSP++ *install-path*\\Blackfin\\Examples\\USB-LAN EZ-EXTENDER\\LAN directory.

USB 2.0 Interface

The USB-LAN EZ-Extender allows you to connect a USB 2.0 chip to a Blackfin processor without any other programmable logic required. PLX's (formerly Netchip) Net2272 device ties directly to the asynchronous memory bank 3 of the Blackfin processor. You can read from and write to the USB 2.0 controller by directly addressing the named memory bank.

You can reset the Net2272 processor by asserting LOW these flag pins: PF11 on the ADSP-BF533 processor, PF6 on the ADSP-BF537 processor, and PF11 on the ADSP-BF561 processor.

The flag pins can be used for push buttons or LEDs on the respective EZ-KIT Lite; consequently, the user must make the proper changes to that EZ-KIT Lite. The switch settings required for each of the respective EZ-KIT Lites are described in the USB Software Readme.txt file in the ...\\Blackfin\\Examples\\USB-LAN EZ-EXTENDER\\USB subdirectory of the VisualDSP++ installation directory. The readme file describes the USB software, source code, drivers, and explains how to run a USB-based application.



For correct switch settings, refer to the schematics drawing of the respective device.

Ethernet Interface

The USB IRQ line of the extender connects to PF10 on the ADSP-BF533/ADSP-BF561 processors and PF7 on the ADSP-BF537 processors. The flag pins can be used for push buttons or LEDs on the respective EZ-KIT Lites; consequently, the user must make the proper changes to that EZ-KIT Lite.

When writing to and reading from the USB device using the EZ-KIT Lites, use memory addresses from [Table 1-1](#).

Table 1-1. USB Device Memory

Device Connects to	Starting Address	Ending Address
ADSP-BF533 EZ-KIT Lite	0x2030 0000	0x2030 007F
ADSP-BF537 EZ-KIT Lite	0x2030 0000	0x2030 007F
ADSP-BF561 EZ-KIT Lite	0x2C00 0000	0x2C00 007F

Ethernet Interface

The USB-LAN EZ-Extender allows you to connect a 10/100 Mbps Ethernet chip to a Blackfin processor. SMSC's LAN91C111 device ties directly to the asynchronous memory bank 3 of the Blackfin processor. You can read from and write to the Ethernet controller by directly addressing the named memory bank.

You can reset the Ethernet processor by asserting the board reset on the ADSP-BF533 and ADSP-BF561 EZ-KIT Lites. The reset connects to a supervisory reset circuit managed by the Analog Devices ADM708 IC device. ADM708 also asserts a reset to the Ethernet chip at power-up.

The Ethernet IRQ line connects to the PF9 flag pin of the ADSP-BF533 processor and PF9 of the ADSP-BF561 processor. The flag pins may be used for push buttons or LEDs on the respective EZ-KIT Lite; consequently, the user must remember to make the proper changes to that EZ-KIT Lite. The switch settings required for each of the respective

EZ-KIT Lites are described in the LAN Software Readme.txt file in the ...\\Blackfin\\Examples\\USB-LAN EZ-EXTENDER\\LAN subdirectory of the VisualDSP++ installation directory. The readme file describes the LAN software, source code, drivers, and explains how to run an Ethernet application.

 For correct switch settings, refer to the schematics drawing of the respective device.

When writing to and reading from the Ethernet device using the EZ-KIT Lites, use memory ranges from [Table 1-2](#).

Table 1-2. LAN Device Memory

Device Connects to	Starting Address	Ending Address
ADSP-BF533 EZ-KIT Lite	0x2031 0000	0x2031 FFFF
ADSP-BF561 EZ-KIT Lite	0x2C01 0000	0x2C01 FFFF

Optional ADSP-BF537 EZ-KIT Lite Interfaces

Optional ADSP-BF537 EZ-KIT Lite interfaces are:

- [“Power-Over-Ethernet”](#)
- [“MII Interface”](#)

Power-Over-Ethernet

The Blackfin USB-LAN EZ-Extender, when used in conjunction with the ADSP-BF537 EZ-KIT Lite, allows a user to power both the EZ-KIT Lite and the extender via a 10/100 Mbps switch or a Midspan device that supports IEEE802.3.AF.

Optional ADSP-BF537 EZ-KIT Lite Interfaces

The EZ-KIT Lite and extender must not be powered via the 7.5V supply when in Power-over-Ethernet (PoE) mode. A user can make the appropriate jumper changes to the EZ-KIT Lite and extender and then connect the kit to the extender to power both boards via the switch or Midspan device. The user must use a switch or Midspan device that supports power via the data pairs. If a user uses a Midspan device that powers only over the spare pins, then the PoE circuitry will not work.

[Table 1-3](#) shows a recommended Midspan device that supports both power over the spare pins and power over the data pairs.

Table 1-3. Midspan Device

Power Sourcing Equipment	Manufacturer	Part Number
Midspan device	PowerDsine 6	PD-8006/AC

Use the Ethernet connector on the EZ-KIT Lite for the PoE application. Power-over-Ethernet does not work properly if the Ethernet connector is used on the extender board. When in PoE mode, the blinking LEDs (LED1-6)] confirm successful power-up. Since PoE is intended as a simple demonstration of the power circuitry, you are not able to bring up a VisualDSP ++ session while in the PoE mode.

Refer to “[Power Select Jumper \(JP1\)](#)” on [page 2-4](#) for the extender’s PoE settings. Refer to the *ADSP-BF537 EZ-KIT Lite Evaluation System Manual* for the EZ-KIT Lite’s PoE settings.

MII Interface

The Media Independent Interface (MII) allows you to evaluate different PHY devices with the ADSP-BF537 EZ-KIT Lite. A separately purchased PHY evaluation board connects directly to the USB-LAN EZ-Extender. You also need to purchase the J2 connector and solder it to be able to connect the two boards together. The part numbers of the SMSC’s evaluation boards and J2 connector are shown in [Table 1-4](#).

Table 1-4. PHY Devices

Part Description	Manufacturer	Manufacturer Part #
J2 20 x 2 connector	AMP/TYCO ELECTRONICS	787170-4
MII evaluation board	SMSC	EVB185
MII evaluation board	SMSC	EVB183

Optional ADSP-BF537 EZ-KIT Lite Interfaces

2 USB-LAN EZ-EXTENDER HARDWARE REFERENCE

This chapter describes the hardware design of the USB-LAN EZ-Extender.

The following topics are covered.

- [“System Architecture” on page 2-2](#)
Describes the board configuration and explains how the board components interface with the processor and EZ-KIT Lite.
- [“Jumper Settings” on page 2-3](#)
Describes the on-board configuration jumpers.
- [“Switch Settings” on page 2-6](#)
Describes the on-board switches.

System Architecture

A block diagram of the Blackfin USB-LAN EZ-Extender is shown in [Figure 2-1](#).

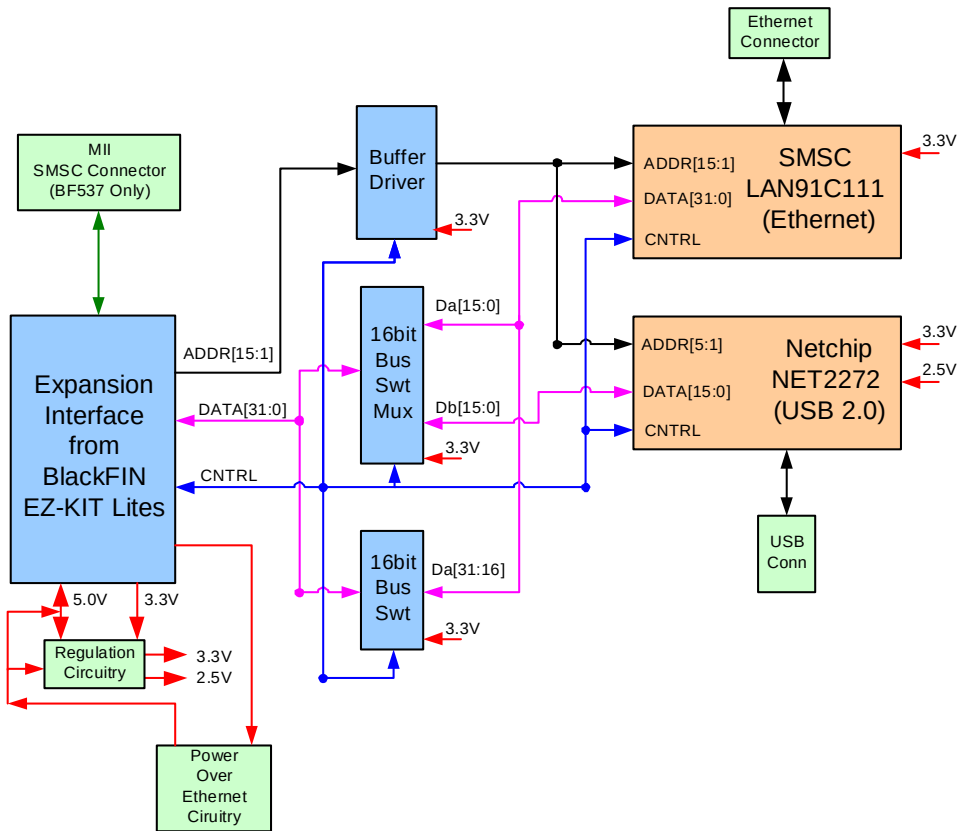


Figure 2-1. Block Diagram

Jumper Settings

Before using the Blackfin USB-LAN EZ-Extender, follow the steps in [“USB-LAN EZ-Extender Setup”](#) on page 1-1.

Figure 2-2 shows the locations of all jumper headers. A two-pin jumper can be placed on the respective jumper header for different functionality. The following sections describe all possible jumper settings and associated functionality.

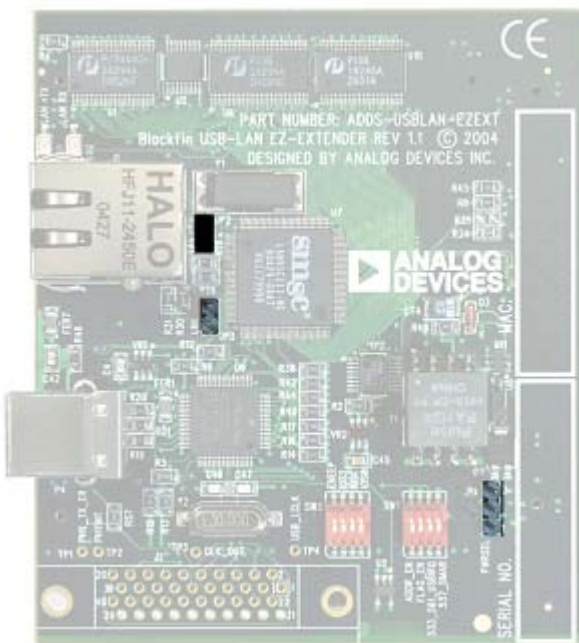


Figure 2-2. Jumper Locations

Power Select Jumper (JP1)

By default, the power select jumper, JP1, must have no jumpers on any of its pins. The jumpers can be used only when the extender is plugged into an ADSP-BF537 EZ-KIT Lite (see [Table 2-1](#)).

Table 2-1. JP1 Settings

Source of 5V Power	JP1 Setting
EZ-KIT Lite power	No jumpers (default)
USB test mode (do not use)	JP1 . 1 and JP1 . 2
Power-over-Ethernet	JP1 . 2 and JP1 . 3

When using an ADSP-BF533 or ADSP-BF561 EZ-KIT Lite, you must not place any jumpers on JP1. Placing a jumper on JP1 can damage the extender card and/or the EZ-KIT Lite. You must power the ADSP-BF533/ADSP-BF561 EZ-KIT Lite and USB-LAN EZ-Extender with the 7.5V power supply provided with the EZ-KIT Lite.

When using an ADSP-BF537 EZ-KIT Lite with the Power-over-Ethernet feature, you must place a jumper between JP1 pin 2 and JP1 pin 3. You must power both the EZ-KIT Lite and USB-LAN EZ-Extender with the CAT5E Ethernet cable, which provides power over the signal pairs. The Ethernet cable must be plugged into the Ethernet connector of the ADSP-BF537 EZ-KIT Lite, but not the USB-LAN EZ-Extender. Use the 7.5V power supply provided with the EZ-KIT Lite.

When using an ADSP-BF537 EZ-KIT Lite without the Power-over-Ethernet, you must not place any jumpers on JP1. You must power both the EZ-KIT Lite and extender with the 7.5V power supply provided with the EZ-KIT Lite.

For an overview of the Power-over-Ethernet interface, refer to [“Power-Over-Ethernet” on page 1-5](#).

LAN Power Jumper (JP2)

The LAN power jumper, JP2, is used to power the SMSC's 91C111 device with 3.3V (see [Table 2-2](#)). By default and in general, the jumper is plugged in for extra flexibility. You must make changes to JP2 only when no power is applied to the USB-LAN EZ-Extender and/or the EZ-KIT Lite.

Table 2-2. JP2 Settings

Functionality	JP2 Setting
No power to the LAN91C111	No jumper
All other cases	JP2.1 and JP2.2 (default)

Link Jumper (JP3)

The link jumper, JP3, connects directly to the link status pin of the SMSC's 91C111 device (see [Table 2-3](#)). The default setting is to keep JP3 unpopulated. When JP3 is populated, it sends a logic 0 or LOW to the input port used to convey the LINK status (EPHSR bit 14). For more information about populating the link jumper, refer to the SMSC LAN91C111 data sheet.

Table 2-3. JP3 Settings

Functionality	JP3 Setting
Logic low on 91C111 link status pin	JP3.1 and JP3.2
All other cases	No jumper (default)

Switch Settings

Before using the Blackfin USB-LAN EZ-Extender, follow the steps in “USB-LAN EZ-Extender Setup” on page 1-1.

Figure 2-3 shows the locations of all switches. The following sections describe all possible switch settings and associated functionality.

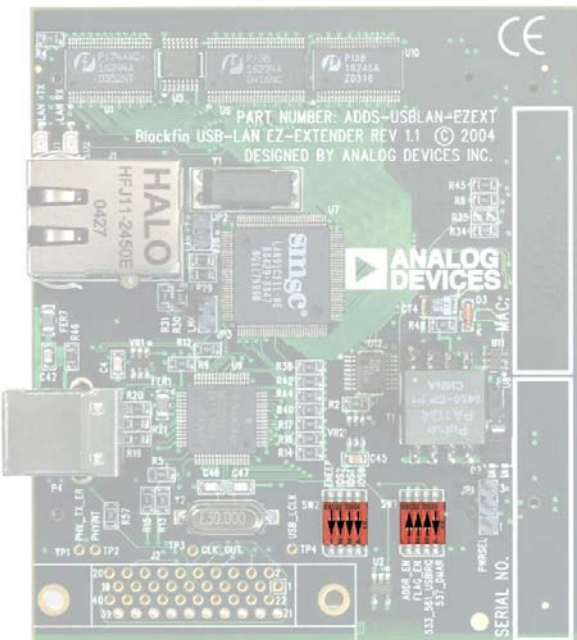


Figure 2-3. Switch Locations

ADDR Enable Switch (SW1.1)

The address enable switch, SW1.1, is used to control the output of the Blackfin address bus buffer (see [Table 2-4](#)). By default, the switch is set to ON. When SW1.1 is OFF, you cannot communicate to the USB or the

Ethernet processor. The address enable switch adds flexibility to the processors because you can turn the switch OFF when capacitive loading is an issue present with other peripherals on the EZ-KIT Lite.

Table 2-4. SW1.1 Settings

Functionality	SW1.1 Setting
Blackfin address buffer (U1) enabled	ON (default)
Blackfin address buffer (U1) disabled	OFF

FLAGS Enable Switch (SW1.2)

The flags enable switch, sw1.2, is used to control the output of the Blackfin flags multiplexer (see [Table 2-5](#)). By default, the sw1.2 switch is set to ON. When SW1.2 is OFF, you cannot communicate to the USB or Ethernet processor. The flags enable switch adds flexibility to the extender—when the switch is OFF, the flags can be used for other peripherals of the EZ-KIT Lite.

Table 2-5. SW1.2 Settings

Functionality	SW1.2 Setting
FLAGS (U12) enabled	ON (default)
Blackfin FLAGS (U12) disabled	OFF

USB IRQ Enable Switch (SW1.3)

The USB IRQ enable switch, sw1.3, is used to control the connection between the Netchip 2272 IRQ line and respective flag pin on the Blackfin processor (see [Table 2-6](#)). The switch connects the USB_IRQ line with PF10 on the ADSP-BF533/ADSP-BF561 processors, and PF7 on the ADSP-BF537 processor. By default, the USB IRQ enable switch is ON.

Switch Settings

When SW1.3 is OFF, communication with the USB processor cannot be established. The SW1.3 adds flexibility to the extender—when the switch is OFF, the flag can be used for other peripherals of the EZ-KIT Lite.

Table 2-6. SW1.3 Settings

Functionality	SW1.3 Setting
USB IRQ enabled	ON (default)
USB IRQ disabled	OFF

Test Mode Enable Switch (SW1.4)

The test mode enable switch, SW1.4, is an internal test pin and should not be used (see [Table 2-7](#)). By default SW1.4 is OFF.

Table 2-7. SW1.4 Settings

Functionality	SW1.4 Setting
Test mode enabled	ON
Test mode disabled	OFF (default)

Serial ROM Enable Switch (SW2.1)

The serial ROM enable switch, SW2.1, is used to control the connection between the LAN91C111 Ethernet processor and its serial ROM (u3) (see [Table 2-8](#)). When the switch is disabled, the Ethernet processor loads its Media Access Control (MAC) address from the serial ROM. By default SW2.1 is OFF. When the switch is ON, you cannot communicate with the provided Ethernet application code. The switch adds flexibility to the extender—you can modify the application code and generate another MAC address when SW2.1 is ON.

Table 2-8. SW2.1 Settings

Functionality	SW2.1 Setting
Serial ROM disabled	ON
Serial ROM enabled	OFF (default)

IOS[2:0] Switch (SW2.2, SW2.3, SW2.4)

The `ios[2:0]` bits on the USB-LAN EZ-Extender are connected directly to the `ios[2:0]` pins of the LAN91C111 Ethernet processor. By default, the switches are OFF. The `ios[2:0]` pins are used in conjunction with the [“Serial ROM Enable Switch \(SW2.1\)”](#) to select between predefined EEPROM configurations. For more information about the switches, refer to the SMSC LAN91C111 data sheet.

Switch Settings

A USB-LAN EZ-EXTENDER BILL OF MATERIALS

The bill of materials corresponds to “[USB-LAN EZ-Extender Schematic](#)” on [page B-1](#).

Ref.	Qty.	Description	Reference Designator	Manufacturer	Part Number
1	1	25MHZ OSC005	Y1	EPSON	MA-505 25.0000 MHZ
2	1	SN74AHC1G00 SOT23-5	U2	TI	SN74AHC1G00DBVR
3	1	PI74AVC+16244 TSSOP48	U1	PERICOM SEMI	PI74AVC+16244AE
4	1	93LC46B SOIC8	U3	MICROCHIP	93LC46B/SNG
5	1	LAN91C111 TQFP128	U7	SMSC	LAN91C111-NU
6	1	NET2272 TQFP64	U9	NET CHIP	NET2272REV1A-LF
7	3	PI3B16245 TSSOP48	U10,U16-17	PERICOM SEMI	PI3B16245AE
8	1	PI3B3257 TSSOP16	U12	PERICOM SEMI	PI3B3257LE
9	1	74LVC139 TSSOP16	U5	PHILIPS	74LVC139PW
10	1	30MHZ OSC010	Y2	ECLIPTEK	E2SAA10-30.000M
11	1	PA1134 ICS005	T1	PULSE	PA1134NL
12	1	PS2911-1 ICS006	U8	NEC	PS2911-1-F3-A
13	1	SI3440DV TSOP6	U11	VISHAY	Si3440DV-T1-E3

Ref.	Qty.	Description	Reference Designator	Manufacturer	Part Number
14	1	TLV431A SOT23-3	U13	ON-SEMI	TLV431ASN1T1G
15	1	LTC4267 SSOP16	U4	LINEAR TECH	LTC4267CGN#PBF
16	1	ADP3330ARTZ-33 SOT23-6	VR2	ANALOG DEVICES	ADP3330ARTZ3.3-RL7
17	1	ADP3330ARTZ-25 SOT23-6	VR1	ANALOG DEVICES	ADP3330ARTZ-2.5-R7
18	1	USB 4PIN CON009	J3	MILL MAX	897-43-004-90-000000
19	3	0.05 45x2 CON018	P1-3	SAMTEC	TFC-145-32-F-D
20	2	DIP4 SWT018	SW1-2	ITT	TDA04HOSB1
21	1	RJ45 8PIN CON_RJ45B	J1	HALO ELEC- TRONIC	HFJ11-2450E-RL
22	2	IDC 2X1 IDC2X1	JP2-3	FCI	90726-402HLF
23	1	IDC 3X1 IDC3X1	JP1	FCI	90726-403HLF
24	1	0 1/4W 5% 1206	R56	KOA	0.0ECTrk7372BTED
25	2	YELLOW LED001	LED1-2	PANASONIC	LN1461C
26	7	0.01UF 100V 10% 0805	C1,C3,C6,C8,C11, C13,C16	AVX	08051C103KAT2A
27	36	0.1UF 50V 10% 0805	C2,C7,C9-10,C12, C19-27,C30-41, C43,C48-56	AVX	08055C104KAT
28	32	10K 1/10W 5% 0805	R1-4,R8,R10-18, R36-45,R48,R57, R60-65	VISHAY	CRCW080510K0JNEA
29	1	33 1/10W 5% 0805	R55	VISHAY	CRCW080533R0JNEA
30	1	4.7K 1/10W 5% 0805	R34	VISHAY	CRCW08054K70JNEA

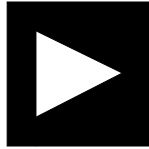
USB-LAN EZ-Extender Bill Of Materials

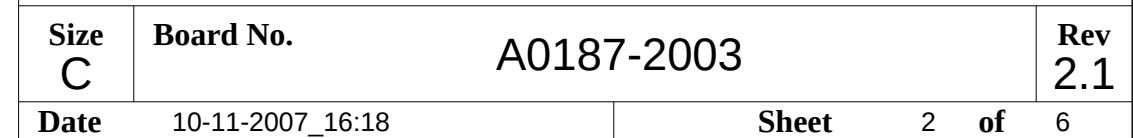
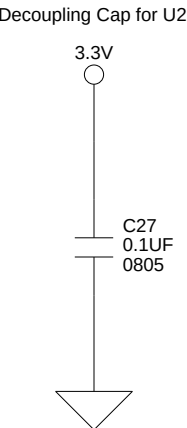
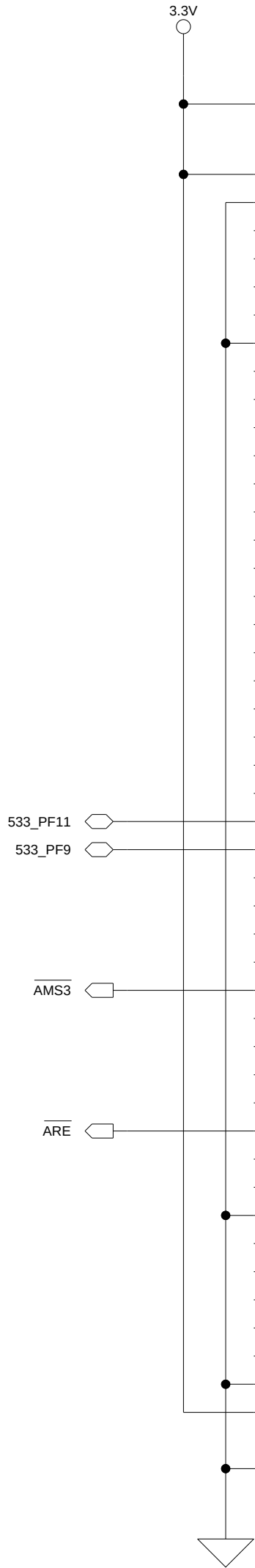
Ref.	Qty.	Description	Reference Designator	Manufacturer	Part Number
31	1	1M 1/10W 5% 0805	R6	VISHAY	CRCW08051M00JNEA
32	1	1.5K 1/10W 5% 0805	R20	VISHAY	CRCW08051K50FKEA
33	3	10UF 16V 10% B	CT1-3	AVX	TAJB106K016R
34	1	300MA LL4148 DL35	D3	DIODES INC	LL4148-13
35	5	600 100MHZ 500MA 1206	FER1-4,FER7	STEWART	HZ1206B601R-10
36	1	11.0K 1/8W 1% 1206	R25	VISHAY	CRCW120611K0FKEA
37	2	30PF 100V 5% 1206	C14-15	AVX	12061A300JAT2A
38	1	47.0K 1/10W 1% 0805	R7	VISHAY	CRCW080547K0FKEA
39	4	0 1/10W 5% 0805	R28-29,R46,R54	VISHAY	CRCW08050000Z0EA
40	1	3.32K 1/10W 1% 0805	R53	PANASONIC	ERJ-6ENF3321V
41	2	42 100MHZ 4A 0805	FER5-6	DIGI-KEY	587-1768-2-ND
42	2	39.0 1/10W 1% 0805	R19,R21	DIGI-KEY	311-39.0CRTR-ND
43	4	0.47UF 16V 10% 0805	C4-5,C44-45	AVX	0805YC474KAT2A
44	3	1UF 10V 10% 0805	C17-18,C42	AVX	0805ZC105KAT2A
45	1	680UF 6.3V 10% E	CT6	AVX	TPSE687K006R0045
46	1	100.0 1/10W 1% 0805	R51	DIGI-KEY	311-100CRCT-ND

Ref.	Qty.	Description	Reference Designator	Manufacturer	Part Number
47	2	10PF 50V 5% 0805	C46-47	AVX	08055A100JAT2A
48	4	24.9 1/10W 1% 0805	R26-27,R30-31	DIGI-KEY	311-24.9CRTR-ND
49	2	49.9 1/10W 1% 0805	R32-33	DIGI-KEY	311-49.9CRCT-ND
50	1	2.43K 1/10W 1% 0805	R5	DIGI-KEY	311-2.43KCRTR-ND
51	1	40A SMAJ58A DIO003	D1	DIODES INC	SMAJ58A-13-F
52	1	10A SBM1040 DIO004	D2	DIODES INC	SBM1040-13-F
53	1	0.8A HD01 MDIP4	D4	DIODES INC	HD01-T
54	1	68.1 1/10W 1% 0805	R47	VISHAY	CRCW080568R1FNEA
55	1	6.81K 1/10W 1% 0805	R49	VISHAY	CRCW08056K81FNEA
56	1	0.12 1/10W 1% 0603	R50	PANASONIC	ERJ-3RSFR12V
57	2	330 1/8W 5% 1206	R22-23	DALE	CRCW1206330RJNEA
58	1	10.0K 1/8W 1% 1206	R52	DALE	CRCW120610K0FKEA
59	1	4.7UF 10V 20% B	CT4	DIGI- KEY	399-3724-2-ND
60	1	1K 1/8W 5% 0805	R9	DIGI-KEY	311-1.0KARTR-ND

BLACKFIN USB-LAN EZ-EXTENDER

SCHEMATIC

 ANALOG DEVICES 20 Cotton Road Nashua, NH 03063 PH: 1-800-ANALOGD			
Title		BLACKFIN USB-LAN EZ-EXTENDER TITLE	
Size C	Board No. A0187-2003		Rev 2.1
Date	10-11-2007_16:18		Sheet 1 of 6



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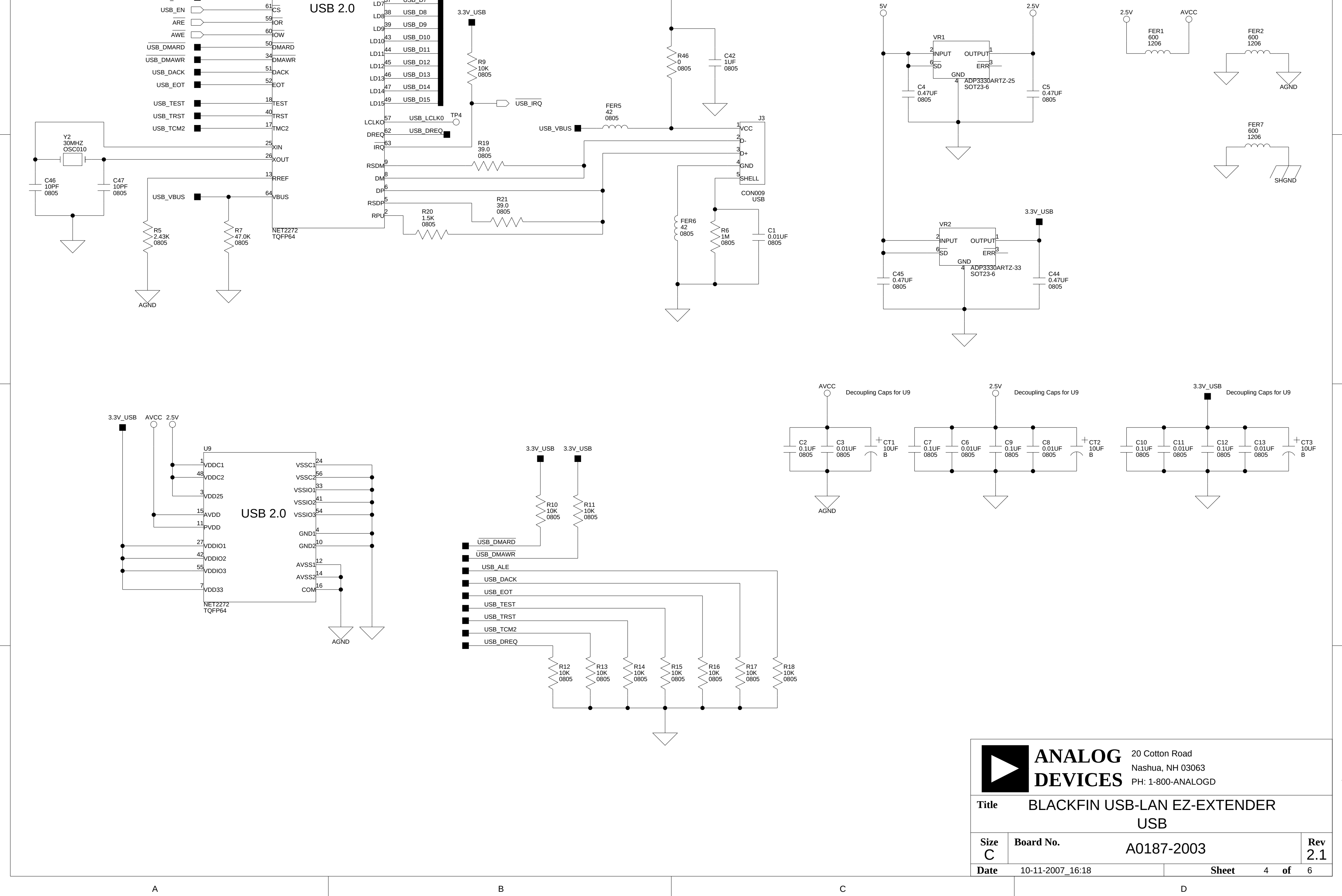
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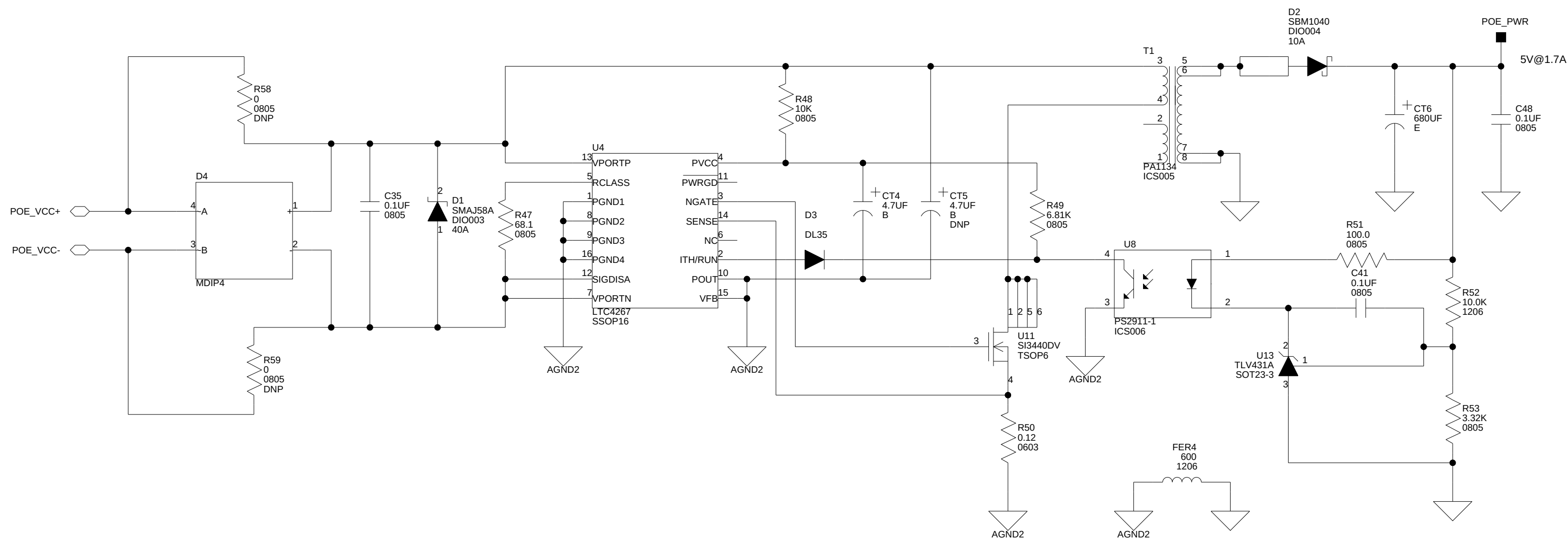
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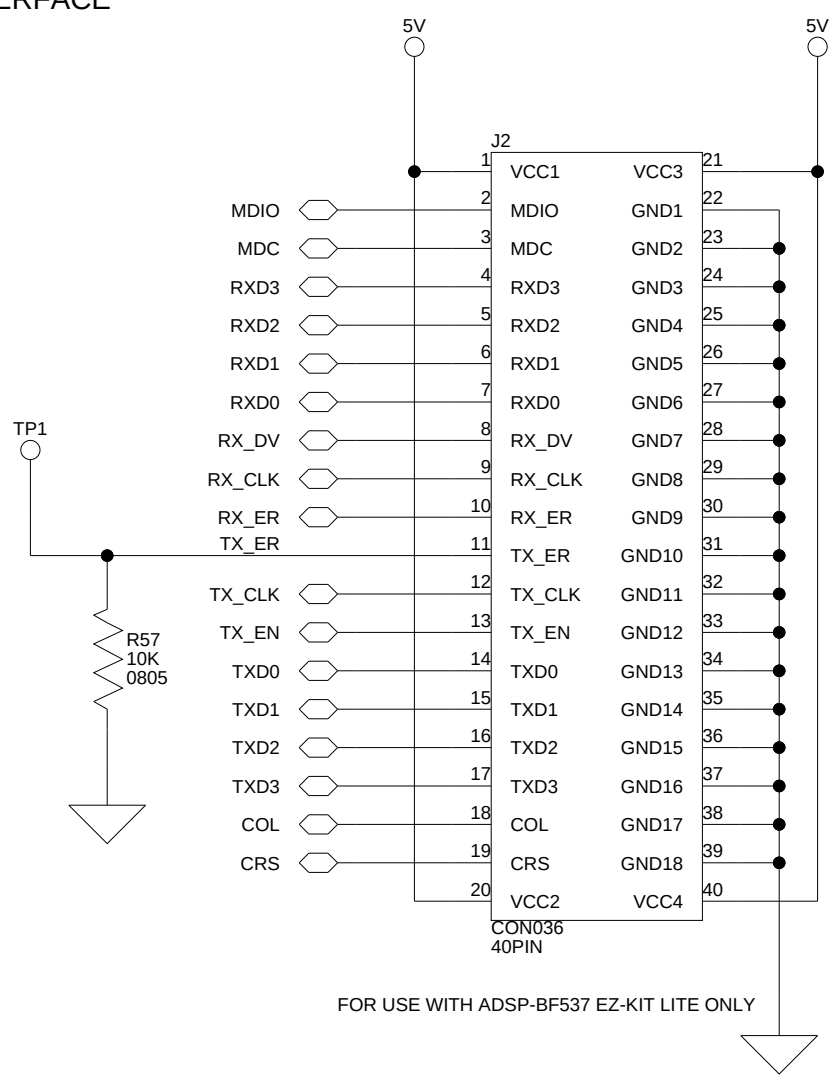
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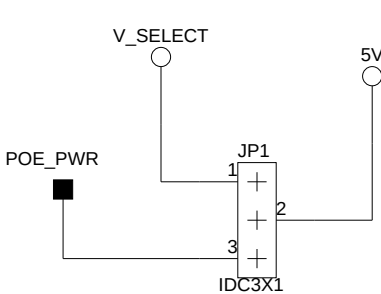
POWER OVER ETHERNET CIRCUITRY
FOR USE WITH ADSP-BF537 EZ-KIT LITE ONLY



MII INTERFACE



WARNING: THE EZ-KIT LITE MUST NOT BE POWERED
WHEN THESE JUMPERS ARE POPULATED.



SOURCE OF 5V PWR	JP1 SETTING
EZ-KIT POWER	* NO JUMPER
USB TEST MODE	JP1 and JP2
POE POWER ADSP-BF537 EZ-KIT LITE ONLY	JP2 and JP3

*DENOTES DEFAULT SETTING



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DEVICES**

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Title **BLACKFIN USB-LAN EZ-EXTENDER
POE, MII, PWR SELECTION**

Size
C

Board No.

A0187-2003

Rev
2.1

Date 10-11-2007_16:18

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Product Overview

Development tools provide easier and more robust methods for engineers to develop and optimize DSP systems and shorten product development cycles for faster time-to-market. The Development Tools components include:

Evaluation Kits

- **EZ-KIT Lite®**

Desktop evaluation board includes an evaluation suite of VisualDSP++® development environment. The evaluation suite of VisualDSP++ has limited memory only.

- **EZ-Extender®**

EZ-Extender daughter boards give developers access and ability to connect various peripherals from Analog Devices and third parties to the expansion interface of the EZ-KIT Lite evaluation kits.

Software

- **VisualDSP++®**

Software development environment includes a C++ compiler, assembler, and linker, enhanced user interface, advanced plotting tools and statistical profiling to easily identify programming bottlenecks.

- **VisualAudio® Designer**

Works in conjunction with VisualDSP++ integrated software development and debugging environment and includes a variety of ready-to-use software building blocks required for audio system design and development and provides the ability to generate product-ready code (optimized for memory and MIPS usage). Supports ADSP-BF533 and ADSP-BF537 Blackfin Processors and the ADSP-21262, ADSP-21364 and ADSP-21369 SHARC Processors.

- **Starter Kit**

Provides everything needed to get started on an application. Starter Kits contain a Blackfin EZ-KIT Lite, EZ-Extender daughter board(s), and the Software Development Kit (SDK) which contains sample code, "how to" documents, and various encoders/decoders that make getting started on an application easy and shorten the learning curve.

Emulators

- Rapid on-chip debugging allows developers to load code, set breakpoints, and observe variables,

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“ADI's Blackfin processor provides the processing performance we needed to quickly stream music from a computer to a stereo system.”

Anthony Wood, Founder, Roku

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memory, registers, etc.

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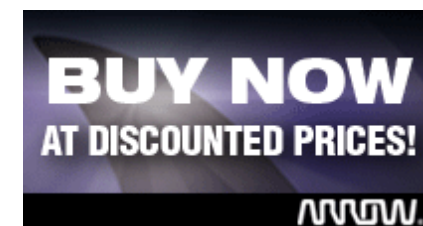
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