

XC61F Series

Voltage Detectors (Delay Circuit Built-In)

General Description

The XC61F series are highly accurate, low power consumption voltage detectors, manufactured using CMOS and laser trimming technologies.

A delay circuit is built-in to each detector.

Detect voltage is extremely accurate with minimal temperature drift.

Both CMOS and N channel open drain output configurations are available.

Since the delay circuit is built-in, peripherals are unnecessary and high density mounting is possible.

Features

Highly Accurate: Detect voltage $\pm 2\%$

Low Power Consumption: TYP $1.0\mu A$ [$V_{IN}=2.0V$]

Detect Voltage Range: 1.6V to 6.0V in 0.1V increments

Operating Voltage Range: 0.7V to 10.0V

Detect Voltage Temperature Characteristics: TYP $\pm 100ppm/^{\circ}C$

Built-In Delay Circuit:

1ms to 50ms, 50ms to 200ms, 80ms to 400ms

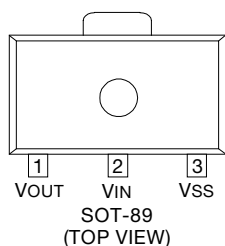
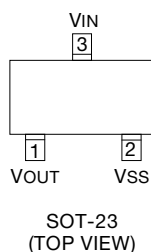
Output Configuration: N-channel open drain or CMOS

Ultra Small Packages: SOT-23 (150mW) mini-mold

SOT-89 (500mW) mini-power mold

* No parts are available with an accuracy of $\pm 1\%$

Pin Configuration

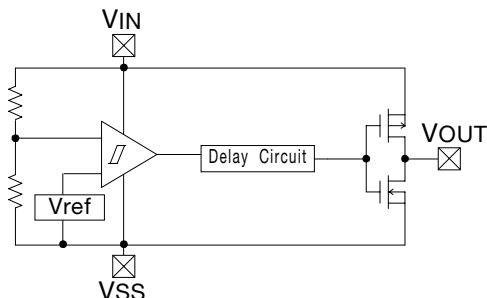


Pin Assignment

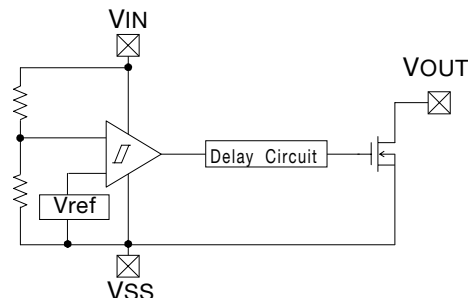
PIN NUMBER		PIN NAME	FUNCTION
SOT-23	SOT-89		
3	2	VIN	Supply Voltage Input
2	3	VSS	Ground
1	1	VOUT	Output

Block Diagram

(1) CMOS output



(2) N-channel open drain output



Ordering Information

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a b c d e f

DESIGNATOR	DESCRIPTION	DESIGNATOR	DESCRIPTION
a	Output Configuration : C = CMOS N = Nch open drain	e	Package Type : M = SOT-23 P = SOT-89
b	Detect Voltage (VDF) : 25 = 2.5V 38 = 3.8V		
c	Output Delay : 1 = 50ms to 200ms 4 = 80ms to 400ms 5 = 1ms to 50ms	f	Device Orientation : R = Embossed Tape (Right) L = Embossed Tape (Left)
d	Detect Accuracy : 2 = within $\pm 2.0\%$		