

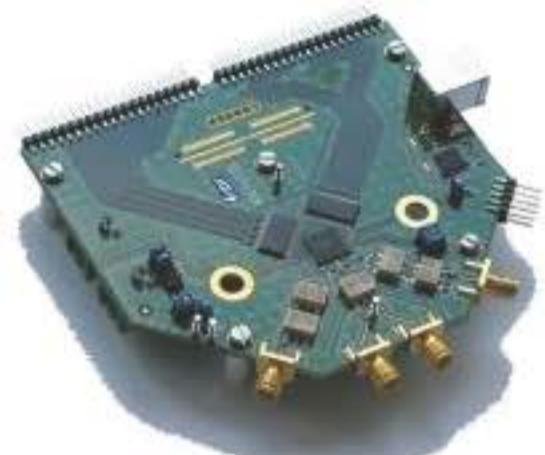
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## ADC1412D125F1

### ADC1412D125F1 demo board; CMOS version; SPI, Regulators and CMOS buffer on board

 Status: **Obsolete**

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#### Features

- SMA connector for clock and analog inputs signal
- CMOS (buffered outputs)

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ADC1412D125F1-DB	Obsolete	BD0	BOARD	0	I	No	<a href="#">Check Availability</a>

#### Technical Documentation

Title	Type	Format	File Size	Date
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#### User Guides & Manuals

<a href="#">Demo Board Software ADC_1_r04</a>	Manual - Software	ZIP	2.33 MB	07/19/2012
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Product ID	Title
ADC1412D125HN	Dual 14-bit ADC 125 Msps CMOS or LVDS DDR digital outputs

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