

INTELLIGENT POWER HIGH SIDE SWITCH

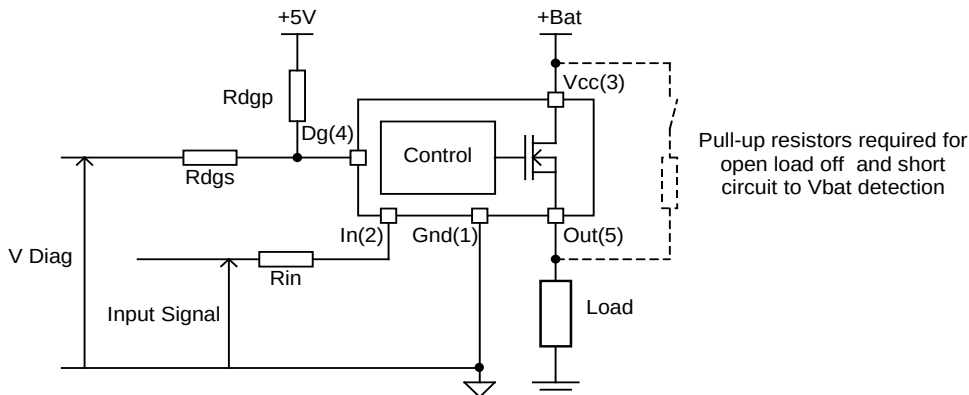
Features

- Over temperature shutdown (with auto-restart)
- Short circuit protection (current limit)
- Reverse battery protection (turns On the MOSFET)
- Full diagnostic capability (short circuit to battery)
- Active clamp
- Open load detection in On and Off state
- Ground loss protection
- Logic ground isolated from power ground
- ESD protection
- Lead Free and RoHS compliant

Description

The AUIPS6021(S)(R) is a five terminal Intelligent Power Switch (IPS) for use in a high side configuration. It features short circuit, over-temperature, ESD protection, inductive load capability and diagnostic feedback. The output current is limited to the I_{lim} value. The current limitation is activated until the thermal protection acts. The over-temperature protection turns off the device if the junction temperature exceeds the $T_{shutdown}$ value. It will automatically restart after the junction has cooled 7°C below the $T_{shutdown}$ value. The reverse battery protection turns On the MOSFET. A diagnostic pin provides different voltage levels for each fault condition. The double level shifter circuitry will allow large offsets between the logic and load ground.

Typical Connection



Product Summary

$R_{ds(on)}$	30m Ω max.
V_{clamp}	39V
I_{Limit}	32A
Open load	3V / 1.1A

Packages



TO-220
AUIPS6021



D²Pak
AUIPS6021S



D-Pak
AUIPS6021R

Qualification Information[†]

Qualification Level		Automotive (per AEC-Q100 ^{††})	
		Comments: This family of ICs has passed an Automotive qualification. IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.	
Moisture Sensitivity Level		D2PAK-5L	MSL1, 260°C (per IPC/JEDEC J-STD-020)
		TO-220	Not applicable (non-surface mount package style)
		DPAK-5L	MSL1, 260°C (per IPC/JEDEC J-STD-020)
ESD	Machine Model	Class M2 (+/-150V) ^{†††} (per AEC-Q100-003)	
	Human Body Model	Class H1C (+/-1500V) ^{†††} (per AEC-Q100-002)	
	Charged Device Model (DPAK,D2PAK)	Class C4 (+/-900V) ^{†††} (per AEC-Q100-011)	
	Charged Device Model (TO220)	Class C3B (+/-750V) ^{†††} (per AEC-Q100-011)	
IC Latch-Up Test		Class II, Level A (per AEC-Q100-004)	
RoHS Compliant		Yes	

† Qualification standards can be found at International Rectifier's web site <http://www.irf.com/>

†† Exceptions to AEC-Q100 requirements are noted in the qualification report.

††† Passing voltage level

Absolute Maximum Ratings

Absolute maximum ratings indicate sustained limits beyond which damage to the device may occur. All voltage parameters are referenced to Ground lead. $T_j = -40^{\circ}\text{C}..150^{\circ}\text{C}$, $V_{cc}=6..35\text{V}$ (unless otherwise specified).

Symbol	Parameter	Min.	Max.	Units
Vout	Maximum output voltage	$V_{cc}-35$	$V_{cc}+0.3$	V
Voffset	Maximum logic ground to load ground offset	$V_{cc}-35$	$V_{cc}+0.3$	
Vin	Maximum input voltage	-0.3	5.5	
Vcc max.	Maximum Vcc voltage	—	36	
Vcc cont.	Maximum continuous Vcc voltage	—	28	
Vcc sc.	Maximum Vcc voltage with short circuit protection	—	28	
Iin max.	Maximum IN current	-3	10	mA
Idg max.	Maximum diagnostic output current	-3	10	
Vdg	Maximum diagnostic output voltage	-0.3	5.5	V
Pd	Maximum power dissipation (internally limited by thermal protection)	—	—	W
	Rth=5°C/W AUIPS6021	—	25	
	Rth=40°C/W AUIPS6021S 1" sqrt. footprint	—	3.1	
	Rth=50°C/W AUIPS6021R 1" sqrt. footprint	—	2.5	
Tj max.	Max. storage & operating temperature junction temperature	-40	150	°C
Tsoldering	Soldering temperature (10 seconds)	—	300	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Units
Rth1	Thermal resistance junction to ambient AUIPS6021 TO220 free air	50	—	°C/W
Rth2	Thermal resistance junction to case AUIPS6021 TO220	2.6	—	
Rth1	Thermal resistance junction to ambient AUIPS6021S D ² Pak std. footprint	60	—	
Rth2	Thermal resistance junction to ambient AUIPS6021S D ² Pak 1" sqrt. Footprint	40	—	
Rth3	Thermal resistance junction to case AUIPS6021S D ² Pak	2.6	—	
Rth1	Thermal resistance junction to ambient AUIPS6021R D-Pak std. footprint	70	—	
Rth2	Thermal resistance junction to ambient AUIPS6021R D-Pak 1" sqrt. Footprint	50	—	
Rth3	Thermal resistance junction to case AUIPS6021R D-Pak	2.6	—	

Recommended Operating Conditions

These values are given for a quick design. For operation outside these conditions, please consult the application notes.

Symbol	Parameter	Min.	Max.	Units
VIH	High level input voltage	4	5.5	
VIL	Low level input voltage	0	0.9	
Iout	Continuous drain current, Tambient=85°C, Tj=125°C, Vin=5V	—	—	A
	Rth=5°C/W AUIPS6021	—	12	
	Rth=40°C/W AUIPS6021S 1" sqrt. footprint	—	4.3	
	Rth=50°C/W AUIPS6021R 1" sqrt. footprint	—	3.9	
Rin	Recommended resistor in series with IN pin	4	10	kΩ
Rdgs	Recommended resistor in series with DG pin for reverse battery protection	4	20	
Rdgp	Recommended pull-up resistor for DG	4	20	
Rol	Recommended pull-up resistor for open load detection	5	100	
F max.	Max. switching frequency	—	1.5	kHz

Static Electrical Characteristics

T_j=-40°C..150°C, V_{cc}=6..28V (unless otherwise specified), typical values are given for V_{cc}=14V and T_j=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
R _{ds(on)}	ON state resistance T _j =25°C	—	24	30	mΩ	V _{in} =5V, I _{out} =10A
	ON state resistance T _j =150°C	—	42	52		V _{in} =5V, I _{out} =10A
	ON state resistance T _j =25°C, V _{cc} =6V	—	29	36		V _{in} =5V, I _{out} =5A
	ON state resistance during reverse battery T _j =25°C	—	31	39		V _{cc} -Gnd=-14V
V _{cc op.}	Operating voltage range with short circuit protection	6	—	28	V	
V _{clamp 1}	V _{cc} to Out clamp voltage 1	37	39	43		I _{out} =40mA
V _{clamp 2}	V _{cc} to Out clamp voltage 2	—	40	—		I _{out} =8A (see Fig. 1)
I _{cc Off}	Supply current when Off and V _{out} connected to ground with R<4Ω	—	4	9	μA	V _{in} =0V, V _{out} =0V, T _j =25°C, V _{cc} =14V
I _{cc On}	Supply current when On	—	2.2	5	mA	V _{in} =5V, V _{cc} =14V
V _{ih}	Input high threshold voltage	—	2.5	3	V	
V _{il}	Input low threshold voltage	1.5	2	—		
I _{n hyst.}	Input hysteresis	0.2	0.5	1		
I _{in On}	Input current when device is On	—	40	100	μA	V _{in} =5V
I _{dg}	Dg leakage current	—	0.1	10		V _{dg} =5V
V _{dg}	Low level DG voltage	—	0.25	0.4	V	I _{dg} =1.6mA

Switching Electrical Characteristics

V_{cc}=14V, Resistive load=6Ω, V_{in}=5V, T_j=-40°C..150°C, typical values are given for T_j=25°C

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Tdon	Turn-on delay time	—	14	40	μs	see Fig. 3
Tr1	Rise time to Vout=Vcc-5V	—	10	35		
Tr2	Rise time to Vout=0.9 x Vcc	—	18	65		
dV/dt (On)	Turn On dV/dt	—	0.8	—	V/μs	
EOn	Turn On energy	—	250	—	μJ	
Tdoff	Turn-off delay time	—	40	80	μs	
Tf	Fall time to Vout=0.1 x Vcc	—	15	35		
dV/dt (Off)	Turn Off dV/dt	—	1.5	—		
EOff	Turn Off energy	—	100	—	μJ	

Protection Characteristics

$T_j = -40^{\circ}\text{C} \dots 150^{\circ}\text{C}$, $V_{cc} = 6 \dots 28\text{V}$ (unless otherwise specified), typical values are given for $V_{cc} = 14\text{V}$ and $T_j = 25^{\circ}\text{C}$

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Ilim	Internal current limit	19	32	50	A	$V_{out} = 0\text{V}$, $T_j = 25^{\circ}\text{C}$
Tsd+	Over temperature high threshold	150(1)	165	—	$^{\circ}\text{C}$	See fig. 2
Tsd-	Over temperature low threshold	—	158	—		
Vsc	Short-circuit detection voltage(2)	2	3	4	V	
UV+	Under voltage protection Vcc going up	—	5	6.2		
UV-	Under voltage protection Vcc going down	—	4.5	5.8		
VOL Off	Open load detection threshold	2	3	4	A	$T_j = -40 \dots 25^{\circ}\text{C}$
IOL On	Open load detection threshold	0.3	0.8	1.25		$T_j = 25 \dots 150^{\circ}\text{C}$
		0.3	0.7	1.1		

(1) Guaranteed by design

(2) Reference to Vcc

True Table

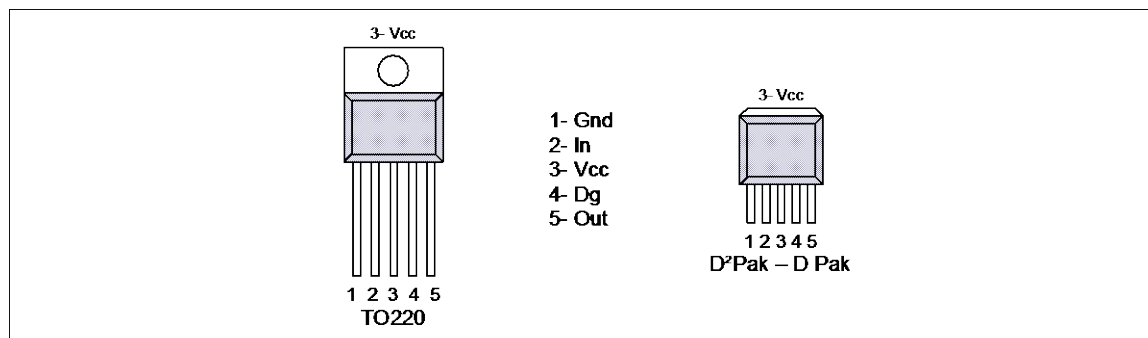
Operating Conditions	IN	OUT	DG
Normal	H	H	H
Normal	L	L	H
Open Load	H	H	L
Open Load (3)	L	H	L
Short circuit to Gnd	H	L	L
Short circuit to Gnd	L	L	H
Short circuit to Vcc	H	H	L (4)
Short circuit to Vcc (5)	L	H	L
Over-temperature	H	L	L
Over-temperature	L	L	H

(3) With a pull-up resistor connected between the output and Vcc.

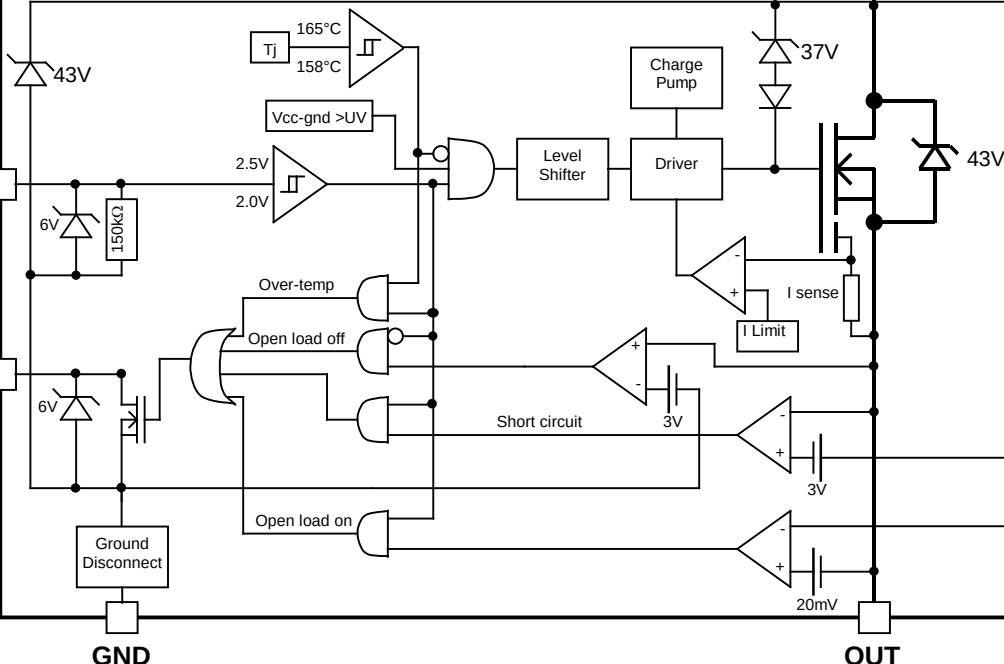
(4) V_{ds} lower than 10mV.

(5) Without a pull-up resistor connected between the output and Vcc.

Lead Assignments



All values are typical



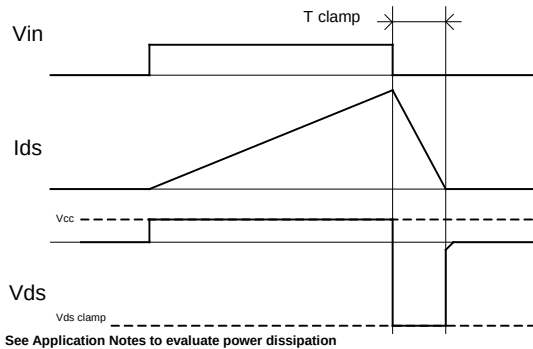


Figure 1 – Active clamp waveforms

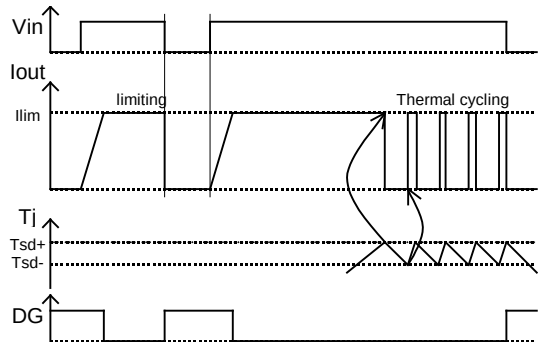


Figure 2 – Protection timing diagram

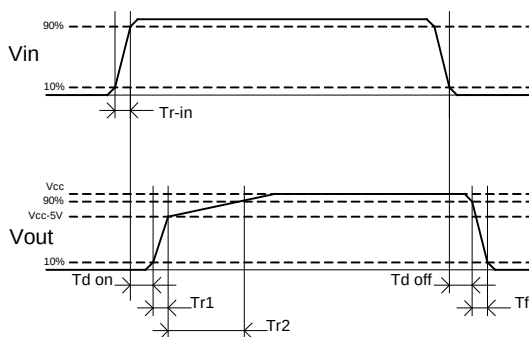


Figure 3 – Switching times definitions

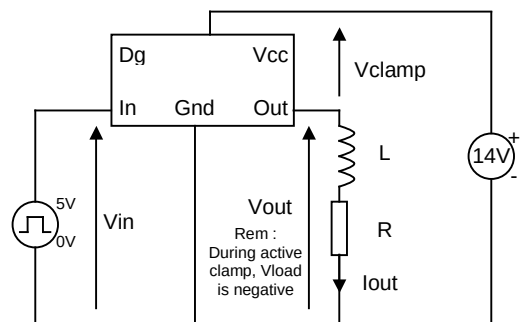


Figure 4 – Active clamp test circuit

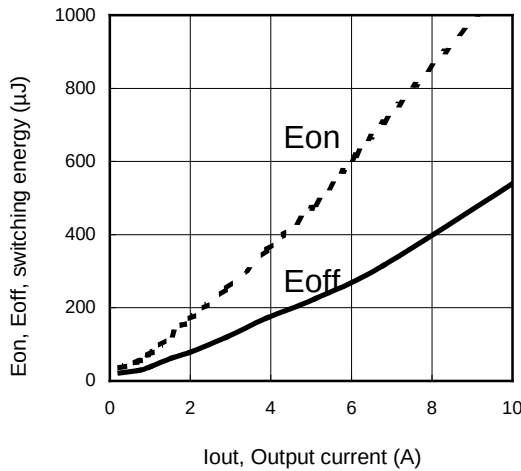


Figure 5 – Switching energy (μJ) Vs Output current (A)

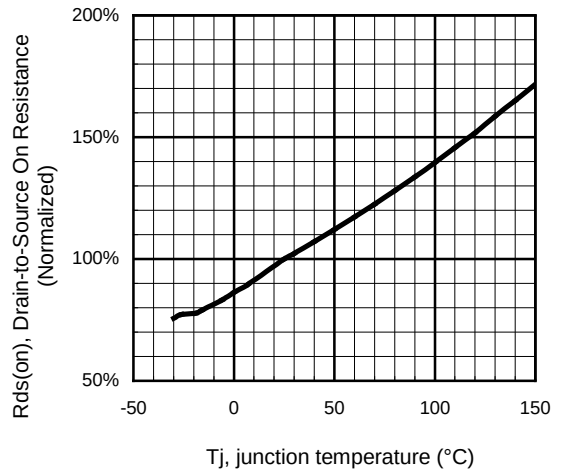


Figure 6 - Normalized Rds(on) (%) Vs Tj (°C)

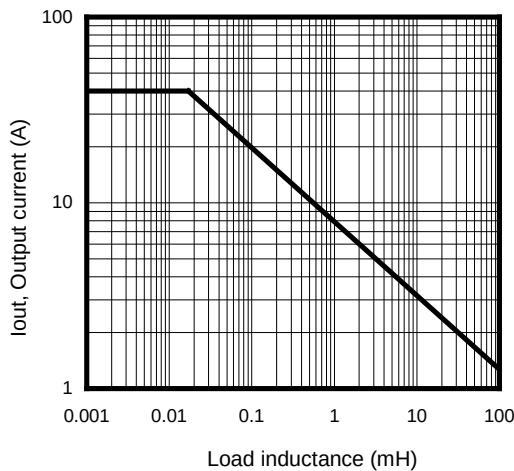


Figure 7 – Max. Output current (A) Vs Load inductance (mH)

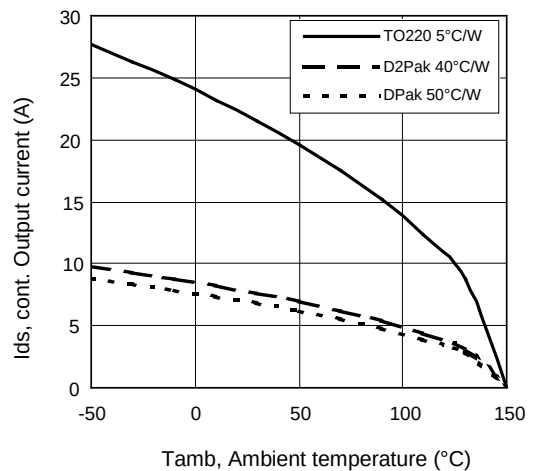
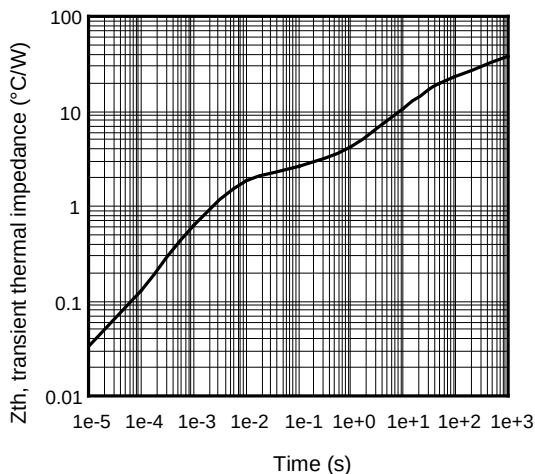
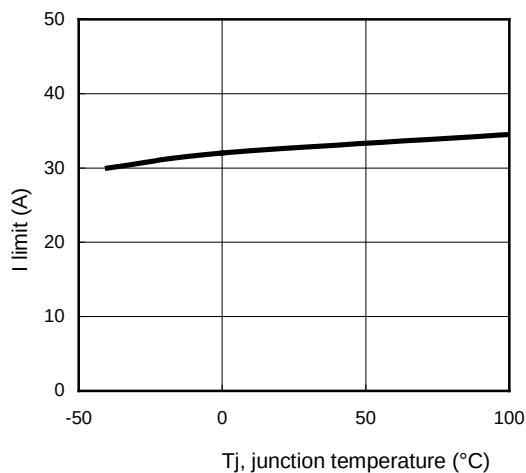


Figure 8 – Max. output current (A) Vs Ambient temperature (°C)



**Figure 9 – Transient thermal impedance (°C/W)
Vs time (s)**



**Figure 10 – I limit (A)
Vs junction temperature (°C)**

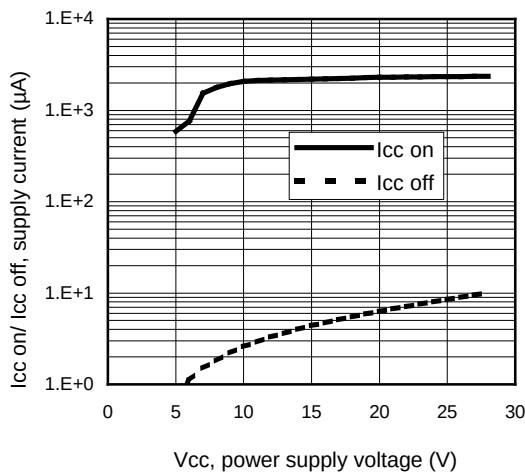


Figure 11 – I_{cc on}/ I_{cc off} (µA) Vs V_{cc} (V)*

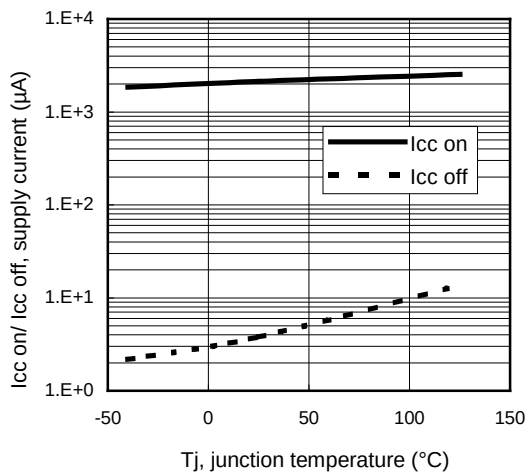
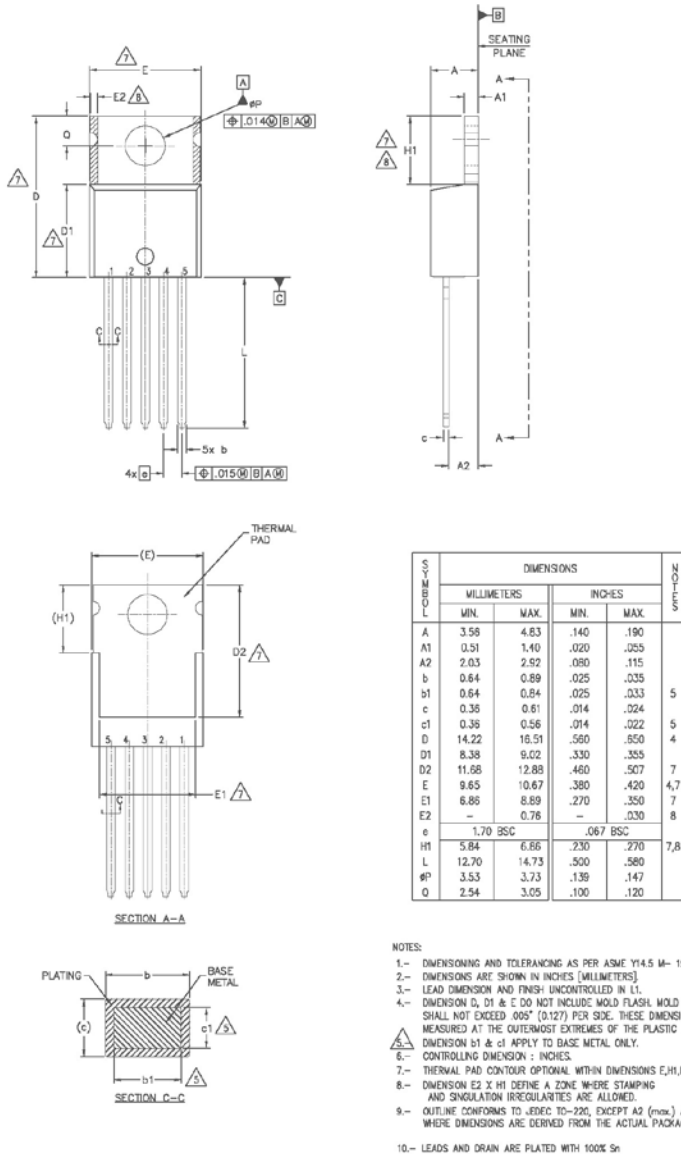


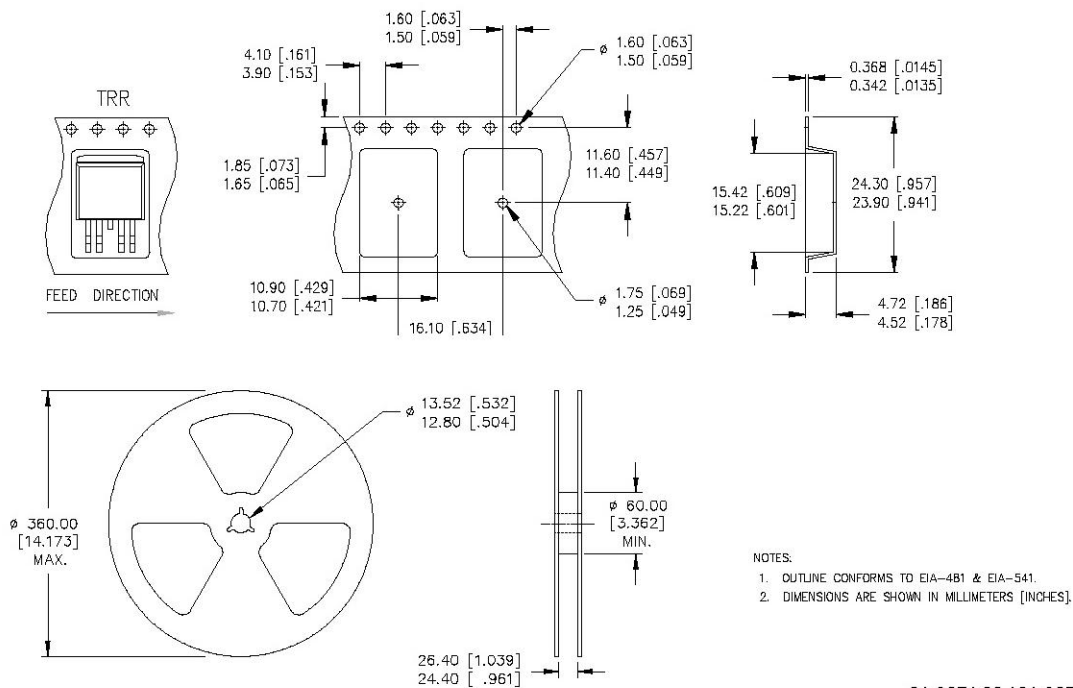
Figure 12 – I_{cc on}/ I_{cc off} (µA) Vs T_j (°C)*

*V_{out} connected to ground with R<4Ω

Case Outline - TO220 (5 leads)

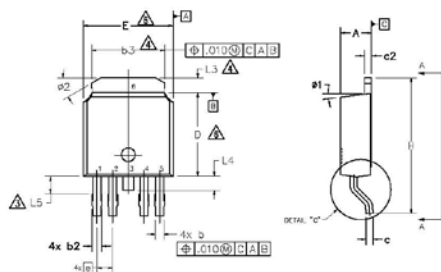
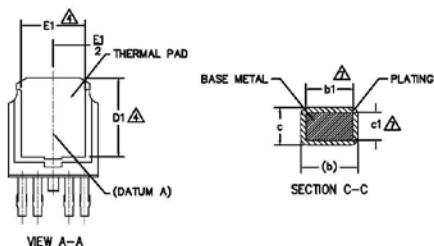
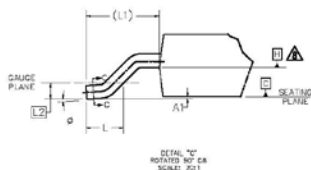


Tape & Reel D2PAK - 5 Leads



01-3071 00 / 01-3072 00

Case Outline DPAK - 5 Leads

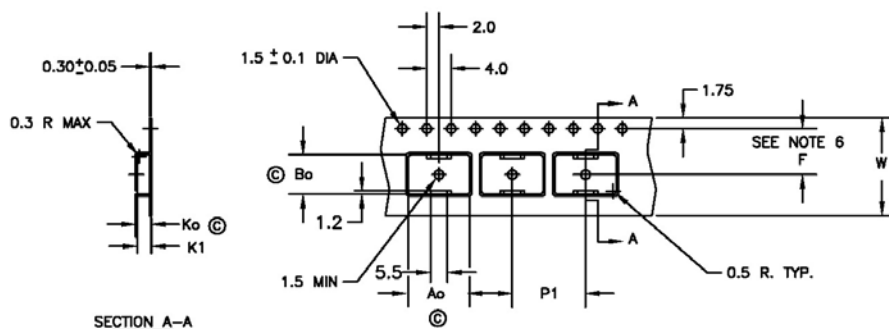


SYMBOL	DIMENSIONS				NOTES
	MILLIMETERS		INCHES		
	MIN.	MAX.	MIN.	MAX.	
A	2.18	2.39	.086	.094	2
A1	—	0.13	—	.005	
b	0.56	0.79	.022	.031	2
b1	.056	0.74	.022	.029	
b2	0.65	0.89	.026	.035	2
b3	4.95	5.46	.195	.215	
c	0.46	0.61	.018	.024	2
c1	0.41	0.56	.016	.022	
c2	0.46	0.89	.018	.035	3
D	5.97	6.22	.235	.245	
D1	5.21	—	.205	—	3
E	6.35	6.73	.250	.265	
E1	4.32	—	.170	—	3
e	1.14 BSC		.045 BSC		
H	9.40	10.41	.370	.410	3
L	1.40	1.78	.055	.070	
L1	2.74 BSC		.108 REF.		3
L2	0.51 BSC		.020 BSC		
L3	0.89	1.27	.035	.050	3
L4	—	1.02	—	.040	
L5	1.14	1.52	.045	.060	3
ø	0"	10"	0"	10"	
ø1	0"	15"	0"	15"	3
ø2	28"	32"	28"	32"	

NOTES:

- 1.— DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M-1994
- 2.— DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS]
- 3.— LEAD DIMENSION UNCONTROLLED IN L5.
- 4.— DIMENSION D1, E1, L3 & b3 ESTABLISH A MINIMUM MOUNTING SURFACE FOR THERMAL PAD.
- 5.— SECTION C-C DIMENSIONS APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN .005 AND 0.10 [0.13 AND 0.25] FROM THE LEAD TIP.
- 6.— DIMENSION D & E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED .005 [0.13] PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTMOST EXTREMES OF THE PLASTIC BODY.
- 7.— DIMENSION b1 & c1 APPLIED TO BASE METAL ONLY.
- 8.— DATUM A & B TO BE DETERMINED AT DATUM PLANE H.
- 9.— OUTLINE CONFORMS TO JEDEC OUTLINE TO-252.
- 10.— LEADS AND DRAIN ARE PLATED WITH 100% Sn

Tape & Reel DPAK - 5 Leads

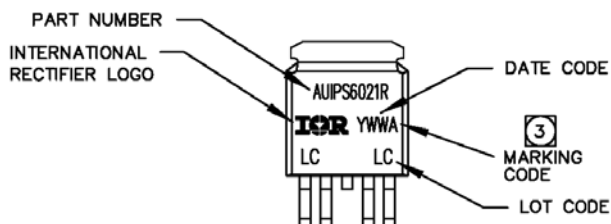
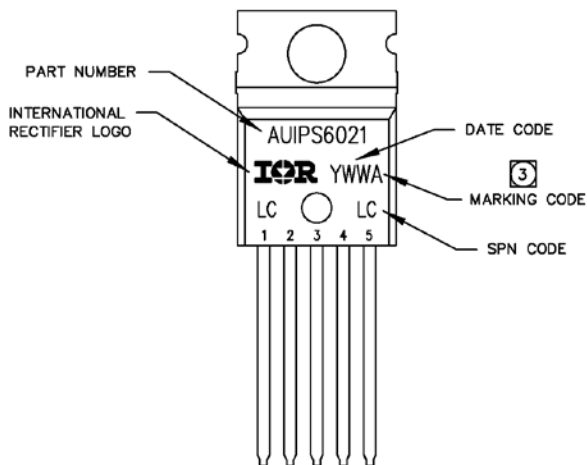
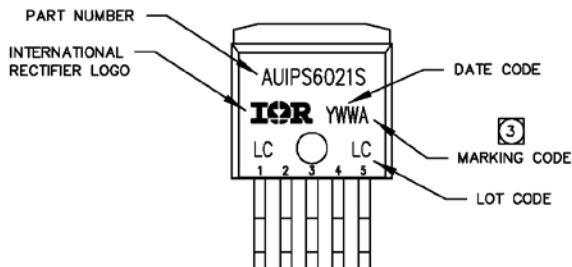


$A_o = 10.5 \text{ mm}$
 $B_o = 7.0 \text{ mm}$
 $K_o = 2.8 \text{ mm}$
 $K_1 = 2.4 \text{ mm}$
 $F = 7.5 \text{ mm}$
 $P_1 = 12.0 \text{ mm}$
 $W = 16.0 \pm .3 \text{ mm}$

NOTES:

1. 10 SPROCKET HOLE PUNCH CUMULATIVE TOLERANCE $\pm .02$
2. CAMBER NOT TO EXCEED 1mm IN 100mm
3. MATERIAL: CONDUCTIVE BLACK POLYSTYRENE
4. A_o AND B_o MEASURED ON A PLANE 0.3mm ABOVE THE BOTTOM OF THE POCKET
5. K_o MEASURED FROM A PLANE ON THE INSIDE BOTTOM OF THE POCKET TO THE TOP SURFACE OF THE CARRIER
6. POCKET POSITION RELATIVE TO THE SPROCKET HOLE MEASURED AS TRUE POSITION OF POCKET, NOT POCKET HOLE
7. VENDOR: (OPTIONAL)
8. MUST ALSO MEET REQUIREMENTS OF EIA STANDARD #EIA-481A, TAPING OF SURFACE-MOUNT COMPONENTS FOR AUTOMATIC PLACEMENT.
9. TOLERANCE TO BE MANUFACTURER STANDARD
10. SURFACE RESISTIVITY OF MOLDED MATL: MUST MEASURE LESS THAN OR EQUAL TO 10^4 OHMS PER SQUARE, MEASURED IN ACCORDANCE TO PROCEDURE GIVEN IN ASTM D-257 & ASTM D-991 (REF. C-9000 SPEC.)
11. TOTAL LENGTH PER REEL MUST BE 79 METERS
12. © CRITICAL DIMENSION

Part Marking Information



Ordering Information

Base Part Number	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIPS6021	TO220-5-Leads	Tube	50	AUIPS6021
AUIPS6021S	D2-Pak-5-Leads	Tube	50	AUIPS6021S
		Tape and reel left	800	AUIPS6021STRL
		Tape and reel right	800	AUIPS6011STRR
AUIPS6021R	D-Pak-5-Leads	Tube	75	AUIPS6021R
		Tape and reel	2000	AUIPS6021RTR
		Tape and reel left	3000	AUIPS6021RTRL
		Tape and reel right	3000	AUIPS6021RTRR

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For technical support, please contact IR's Technical Assistance Center
<http://www.irf.com/technical-info/>

WORLD HEADQUARTERS:

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Revision History

Revision	Date	Notes/Changes
B	September, 12th 2011	AU release
C	May 15, 2012	Add the test condition for the ICC (off) parameters
D	Tuesday, October 16, 2012	Update the date in the front page