

MITSUBISHI HVIGBT MODULES
CM900HB-90H

2nd-Version HVIGBT (High Voltage Insulated Gate Bipolar Transistor) Modules

HIGH POWER SWITCHING USE
 INSULATED TYPE

CM900HB-90H



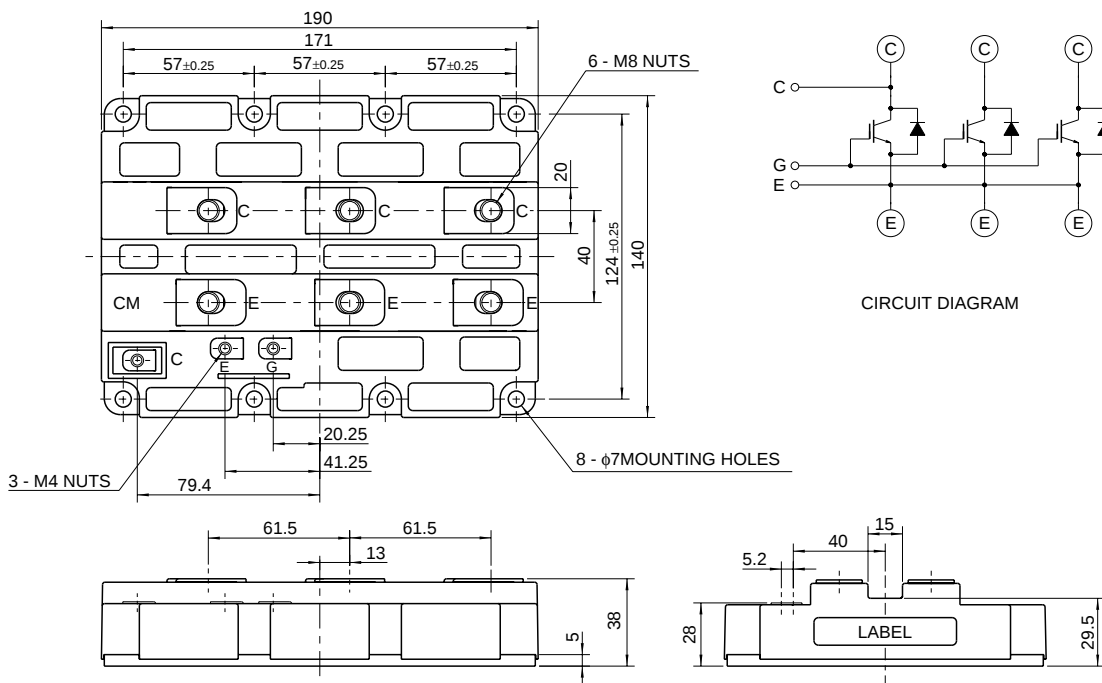
- IC.....900A
- VCES 4500V
- Insulated Type
- 1-element in a pack

APPLICATION

Inverters, Converters, DC choppers, Induction heating, DC to DC converters.

OUTLINE DRAWING & CIRCUIT DIAGRAM

Dimensions in mm



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Mar. 2003

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MAXIMUM RATINGS (T_j = 25°C)

Symbol	Item	Conditions	Ratings	Unit
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	4500	V
V _{GES}	Gate-emitter voltage	V _{CE} = 0V	±20	V
I _C	Collector current	DC, T _C = 85°C	900	A
I _{CM}		Pulse (Note 1)	1800	A
I _E (Note 2)	Emitter current		900	A
I _{EM} (Note 2)		Pulse (Note 1)	1800	A
P _C (Note 3)	Maximum collector dissipation	T _C = 25°C, IGBT part	11100	W
T _j	Junction temperature	—	−40 ~ +125	°C
T _{stg}	Storage temperature	—	−40 ~ +125	°C
V _{iso}	Isolation voltage	Charged part to base plate, rms, sinusoidal, AC 60Hz 1min.	6000	V
—	Mounting torque	Main terminals screw M8	6.67 ~ 13.00	N·m
		Mounting screw M6	2.84 ~ 6.00	N·m
		Auxiliary terminals screw M4	0.88 ~ 2.00	N·m
—	Mass	Typical value	2.2	kg

ELECTRICAL CHARACTERISTICS (T_j = 25°C)

Symbol	Item	Conditions	Limits			Unit
			Min	Typ	Max	
I _{CES}	Collector cutoff current	V _{CE} = V _{CES} , V _{GE} = 0V	—	—	18	mA
V _{GE(th)}	Gate-emitter threshold voltage	I _C = 90mA, V _{CE} = 10V	4.5	6.0	7.5	V
I _{GES}	Gate-leakage current	V _{GE} = V _{GES} , V _{CE} = 0V	—	—	0.5	μA
V _{CE(sat)}	Collector-emitter saturation voltage	T _j = 25°C	—	3.00	3.90	V
		T _j = 125°C	—	3.30	—	
C _{ies}	Input capacitance	V _{CE} = 10V V _{GE} = 0V	—	162	—	nF
C _{oes}	Output capacitance		—	12.0	—	nF
C _{res}	Reverse transfer capacitance		—	3.6	—	nF
Q _G	Total gate charge	V _{CC} = 2250V, I _C = 900A, V _{GE} = 15V	—	—	—	μC
t _d (on)	Turn-on delay time	V _{CC} = 2250V, I _C = 900A	—	—	2.40	μs
t _r	Turn-on rise time	V _{GE1} = V _{GE2} = 15V	—	—	2.40	μs
t _d (off)	Turn-off delay time	R _G = 10Ω	—	—	6.00	μs
t _f	Turn-off fall time	Resistive load switching operation	—	—	1.20	μs
V _{EC} (Note 2)	Emitter-collector voltage	I _E = 900A, V _{GE} = 0V	—	4.00	5.20	V
t _{rr} (Note 2)	Reverse recovery time	I _E = 900A,	—	—	1.80	μs
Q _{rr} (Note 2)	Reverse recovery charge	die / dt = −1800A / μs (Note 1)	—	360	—	μC
R _{th(j-c)Q}	Thermal resistance	Junction to case, IGBT part	—	—	0.009	K/W
R _{th(j-c)R}		Junction to case, FWDi part	—	—	0.018	K/W
R _{th(c-f)}	Contact thermal resistance	Case to fin, conductive grease applied	—	0.007	—	K/W

Note 1. Pulse width and repetition rate should be such that the device junction temp. (T_j) does not exceed T_{jmax} rating.

2. I_E, V_{EC}, t_{rr}, Q_{rr} & die/dt represent characteristics of the anti-parallel, emitter to collector free-wheel diode.

3. Junction temperature (T_j) should not increase beyond 125°C.

4. Pulse width and repetition rate should be such as to cause negligible temperature rise.

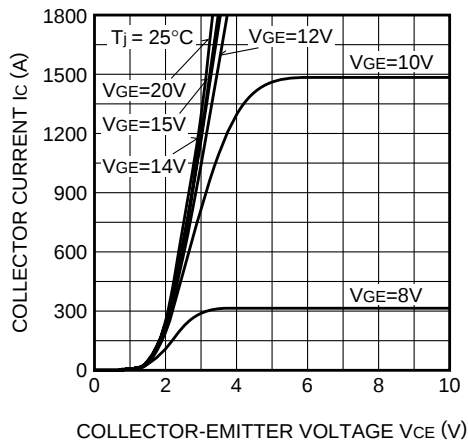
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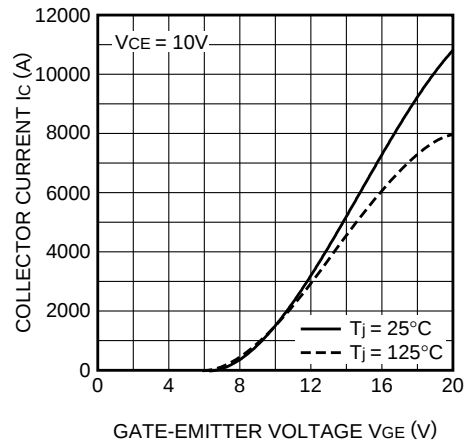
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PERFORMANCE CURVES

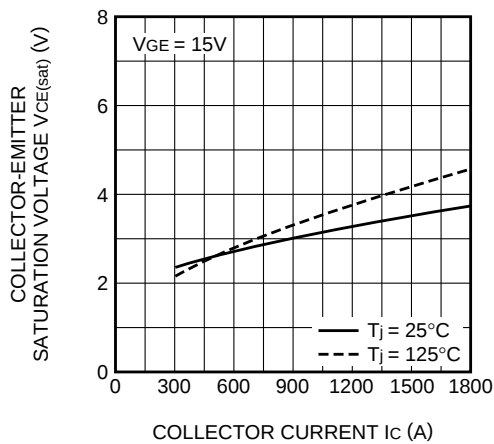
OUTPUT CHARACTERISTICS
(TYPICAL)



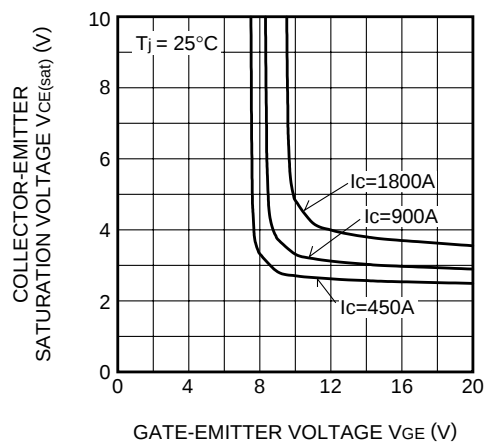
TRANSFER CHARACTERISTICS
(TYPICAL)



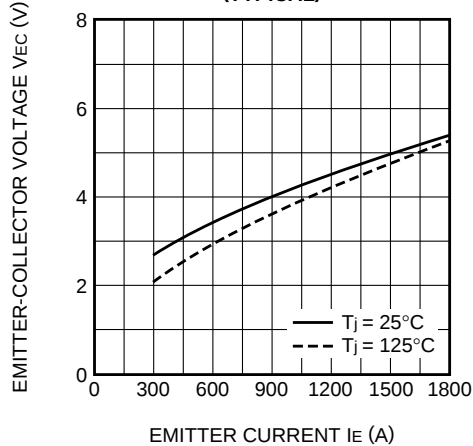
COLLECTOR-EMITTER SATURATION
VOLTAGE CHARACTERISTICS
(TYPICAL)



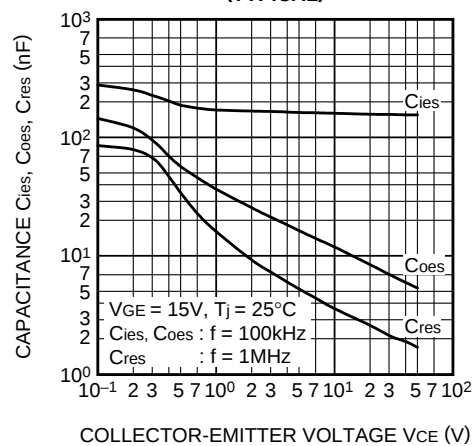
COLLECTOR-EMITTER SATURATION
VOLTAGE CHARACTERISTICS
(TYPICAL)



FREE-WHEEL DIODE
FORWARD CHARACTERISTICS
(TYPICAL)



CAPACITANCE CHARACTERISTICS
(TYPICAL)



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