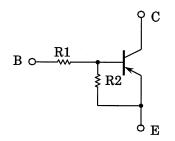
TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process)

RN2007,RN2008,RN2009

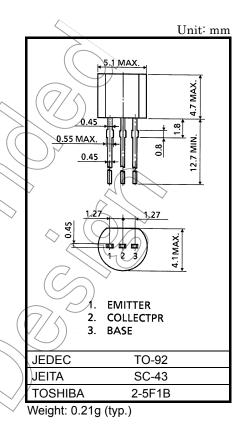
Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- Complementary to RN1007~RN1009

Equivalent Circuit and Bias Resister Values



	\ \ /
Type No.	R1 (kΩ) R2 (kΩ)
RN2007	10 47
RN2008	22 47
RN2009	22



Absolute Maximum Ratings (Ta = 25°C)

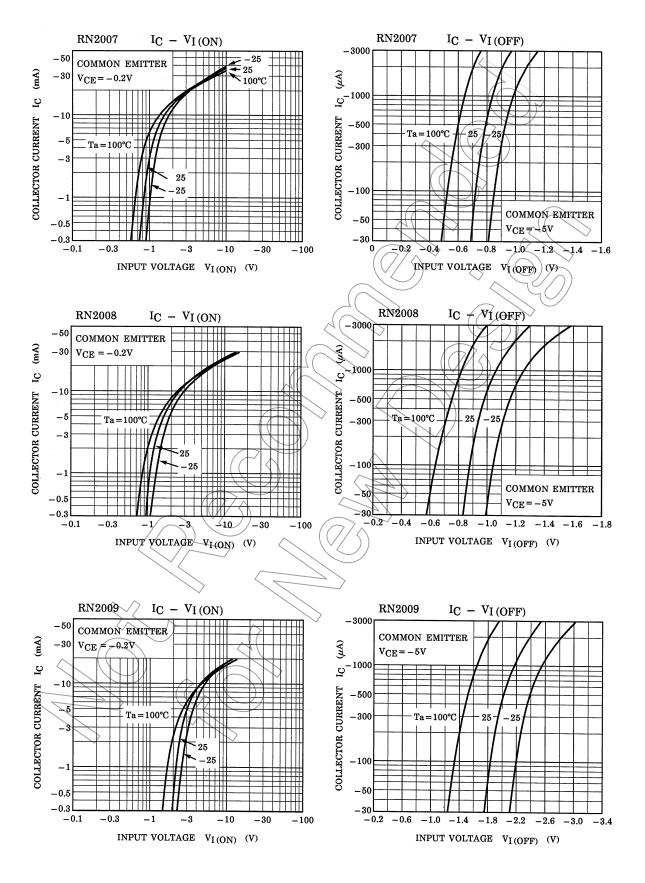
Characteristic		Symbol	Rating	Unit	
Collector-base voltage	V _{CB} O//	-50	V		
Collector-emitter voltage	VCEO	-50	V		
	RN2007		-6		
Emitter-base voltage	RN2008	VEBO	-7	V	
	RN2009		-15		
Collector current	$\langle \langle \langle \rangle \rangle \rangle$	Ic	-100	mA	
Collector power dissipation		PC	400	mW	
Junction temperature	Tj	150	°C		
Storage temperature range	T _{stg}	-55~150	°C		

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

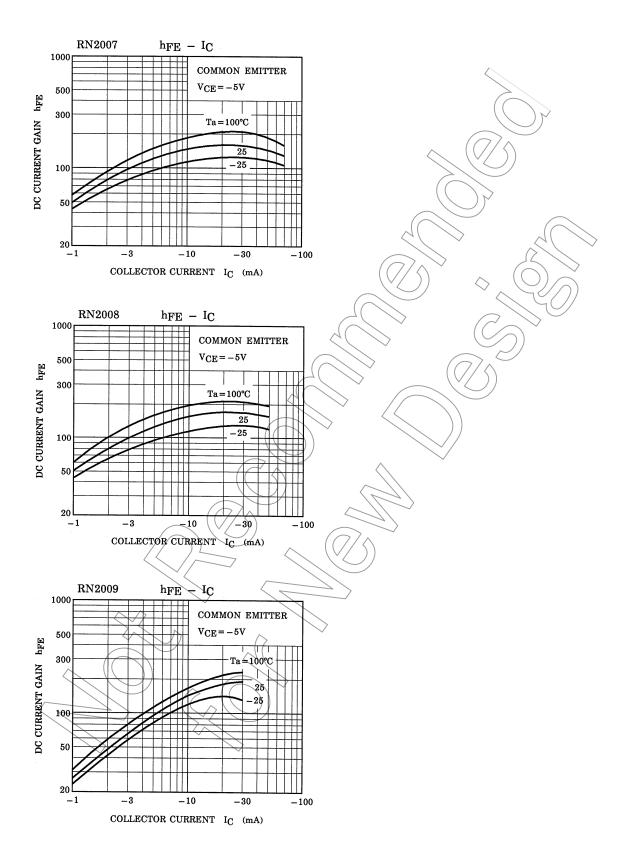
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Electrical Characteristics (Ta = 25°C)

Characteri	stic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	_	$V_{CB} = -50V, I_{E} = 0$	_		-100	nA
		I _{CEO}		$V_{CE} = -50V, I_B = 0$	_	_	-500	
Emitter cut-off current	RN2007	I _{EBO}	_	$V_{EB} = -6V, I_C = 0$	-0.081	_	-0.15	
	RN2008			V _{EB} = -7V, I _C = 0	-0.078	1	-0.145	mA
	RN2009			V _{EB} = −15V, I _C = 0	-0.167)	-0.311	
DC current gain	RN2007	h _{FE}	_	V _{CE} = -5V, I _C = -10mA	80)	_	
	RN2008				(80)) _	1	
	RN2009				70	_	_	
Collector-emitter satura	ation voltage	V _{CE} (sat)	_	I _C = -5mA, I _B = -0.25mA	<u> </u>	-0.1	-0.3	V
Input voltage (ON)	RN2007	VI (ON)	_	V _{CE} = -0.2V, I _C = -5mA	-0.7	7(-1.8	V
	RN2008				-1.0	2	-2.6	
	RN2009				<u>~2.2</u>	$(\bigcirc)_{/}$	-5.8	
Input voltage (OFF)	RN2007	V _{I (OFF)}	- (V _{CE} = -5V, C = -0.1mA	-0.5	757	//-1.0	V
	RN2008				-0.6		-1.16	
	RN2009		<		1.5)) —	-2.6	
Transition frequency		f _T		V _C E = -10V, I _C = -5mA	7h	200	_	MHz
Collector Output capac	itance	C _{ob}	(_/	$V_{CB} = -10V, I_{E} = 0,$ f = 1MH _z	<u></u>	3	6	pF
Input resistor	RN2007				7	10	13	
	RN2008	R1))_		15.4	22	28.6	kΩ
	RN2009				32.9	47	61.1	
Resistor ratio	RN2007				0.191	0.213	0.232	
	RN2008	R1/R2			0.421	0.468	0.515	
	RN2009		(77/^	1.92	2.14	2.35	



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