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LIQUID CRYSTAL DISPLAY MODULE MODEL: NMTB-S000255FYHSGY-10A Customer's No.:

Acceptance					

Microtips Technology Inc. 12F. No.31 Lane 169, Kang Ning St., His-Chih, Taipei Hsien, Taiwan, R.O.C. FAX: 886-2-26958625

Approved and Checked by						

Approved by	Check	Made by	
微端	微端	微端 2006/5/16 連俊傑	微端
2006/5/16	2006/5/16		2006/5/16
張秀美	蔡宜夢		陳雅靖



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Revise Records

Rev.	Date	Contents	Written	Approved
A	2006/5/16	Preliminary Spec	Sherry Chen	Debbie Chang
			_	

Special Notes

Note1.	With Touch Panel attached.
Note2.	The LCD module is RoHS compliant
Note3.	
Note4.	
Note5.	



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1. General Specifications

Operating Temperature : Min. -20° C \sim Max. 70° C

Storage Temperature : Min. -30° C \sim Max. 80° C

Dot Pixels : 128 (W) x 128 (H) dots

Dot Size : 0.40 (W) x 0.40 (H) mm

Dot Pitch : 0.43 (W) x 0.43 (H) mm

Viewing Area : 60.0 (W) x 58.4.0 (H) mm

Outline Dimensions : 69.5 (W) x 138.2* (H) x 5.3** max. (D) mm

* With LED B/L & T/P Tail

** Without B/L holder and A,K pad

Weight : N/A.

LCD Type : STN/ Yellow-Green, Positive-Mode / Transflective

Viewing Direction : 6:00

Data Transfer : 8-bit parallel data transfer

Backlight : LED (Yellow-Green) Edge-Type

LCD LSI : NJU6679A

Drawings : As attached drawings



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2. <u>Electrical Specifications</u>

2.1 Absolute Maximum Ratings

 $V_{SS} = 0V$

Parameter	Symbol	Conditions	Min.	Max.	Units
Supply Voltage (Logic)	$ m V_{DD}$ - $ m V_{SS}$	_	- 0.3	5.0	V
Supply Voltage (LCD Drive)	V_{DD} - V_{EE}	_	V _{DD} - 18.0	V _{DD} + 0.3	V
Input Voltage	V _I	_	- 0.3	$V_{\rm DD} + 0.3$	V

2.2 DC Characteristics

 $Ta = 25^{\circ}C, V_{SS} = 0V$

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Supply Voltage (Logic)	$ m V_{DD}$ - $ m V_{SS}$	_	2.4	_	3.6	V
Supply Voltage (LCD Drive)	$ m V_{DD}$ - $ m V_{EE}$		Shown in 3	3.1		V
High Level Input Voltage	$ m V_{IH}$	$V_{DD} = 3.3V \pm 10\%$	$0.8~\mathrm{V_{DD}}$	_	V_{DD}	V
Low Level Input Voltage	V_{IL}	$V_{DD} = 3.3V \pm 10\%$	$V_{\rm SS}$	_	$0.2~\mathrm{V_{DD}}$	V
High Level Output Voltage	V_{OH}	$I_{OH} = 0.50 \text{mA}$	$0.8~\mathrm{V_{DD}}$	_	V_{DD}	V
Low Level Output Voltage	V_{OL}	$I_{OL} = 0.50 \text{mA}$	$V_{\rm SS}$	_	0.2 V _{DD}	V
Supply Current	I_{DD}	$V_{DD} - V_{SS} = 3.3V$	_	0.05	5.0	μΑ
	$ m I_{EE}$	$V_{DD} - V_{EE} = 15.0V$	_	40.0	80.0	μΑ



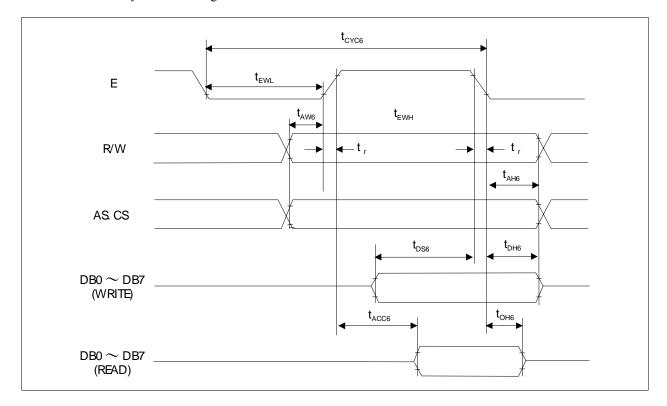
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2.3 AC Characteristics (Read/Write operation sequence - 68 Type MPU)

 $V_{DD} = 3.0V \pm 10\%$

	Paramete	r	Symbol	Min.	Тур.	Max.	Condition	Units
Address Ho	old Time		t _{AH6}	10				ns
Address Set Up Time		A0,CS,R/W	$t_{ m AW6}$	0				ns
System Cycle	e Time(W)	Terminals	t _{CYC6} (W)	270	220			ns
System Cyc	cle Time(R)		t _{CYC6} (R)	350				ns
	Read "H"		$t_{\rm EWH}$	200				ns
Enable Write "F	Write "H"	F. T	-E WII	50				ns
Width	Read "L"	E Terminal	$t_{ m EWL}$	220	160			ns
	Write "L"		-EWE	150				ns
Data Set Up	Time		$t_{ m DS6}$	35				ns
Data Hold	Гіте	D0 to D7	t _{DH6}	15				ns
Access Tim	ne	Terminals	t _{ACC6}			200	CL=100pF	ns
Output Disable Time			t _{OH6}	0		50	CL-100pr	ns
Rise Time, Fall Time		A0, CS, R/W, E, D0 to D7 Terminals	$t_{r,} t_{f}$			15		ns

All timing are based on 20% and 80% of All timing are based on 20% and 80% of VDD voltage level.





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2.4 Lighting Specifications

2.4.1 Absolute Maximum Ratings

Ta = 25°C

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Forward Current	I_F	 .			56	mA
Reverse Voltage	V_R				5.0	V
LED Power Dissipation	P_{D}				252	mW

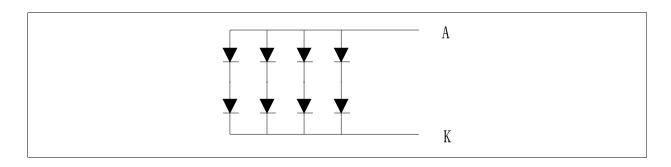
2.4.2 Operating Characteristics

 $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Forward Voltage	VF			4.2		V
Luminance of Backlight Surface*	L		13	18		cd/m ²
Spectrum radiation bandwidth	Δ_{λ}	$I_F = 40 \text{ mA}$		30		nm
Peak emission wavelength	λ_{P}		569	573	5767	nm
Luminance Tolerance*				30		%

^{*}Measured from the surface of backlight.

2.4.3 Schematics Related





^{*}Luminance Tolerance = (Max. - Min./Max.) x 100%

^{*}The specification descried above are based on the measurement of Backlight A,K.

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2.5 Spec. for touch panel

Item	Unit	S	Standard valu	e	Note
nem	Unit	Min.	Тур.	Max.	Note
Linearity	%	-1.5		1.5	
Terminal resistance	Ω	250		550	(Film side)
Terminar resistance	Ω	350		650	(Glass side)
Rate Voltage	V	-	5.0	5.5	
Insulation resistance	ΜΩ	20			DC25V
Chattering	ms			15	
Transparency	%	80			JIS K-7105
Operating force	g			80	Stylus pen (R0.8mm) or with finger (R8.0mm)
Surface hardness	Н	-	2		150gf, 45deg.
Input Life					1,000,000 times (with R8, 60°)
Pen sliding durability					100,000 times (with R0.8, 60°)
Newton ring		unusual interference finger must show when n through the surface sheet.			Detail criterion for inspection refer attached inspection sheet.

Note: Touch panel is with clear film.



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3. Optical Specifications

3.1 LCD Driving Voltage Recommended

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
LCD Driving Voltage Note 1		Ta = -20 °C	18.7	19.3		V
	$V_{ m DD}$ - $V_{ m O}$	Ta = 25 °C	16.2	16.7	17.2	V
		Ta = 70 °C		15.9	16.4	V

Note 1: Voltage (Applied actual waveform to LCD Module) for the best contrast. The range of minimum and maximum shows tolerance of the operating voltage. The specified contrast ratio and response time are not guaranteed over the entire range.

3.2 Optical Characteristics

Ta=25 °C, 1/128 Duty, 1/12 Bias, $V_{DD} = 3.3V$ (Note 4), $\theta = 0^{\circ}$, $\phi = 270^{\circ}$

Parameter		Symbol	Conditions	Min.	Тур.	Max.	Units
Contrast Ratio Note 1		С	$\theta = 0^{\circ}, \phi = 0^{\circ}$		9.0	_	
Viewing Ar	ngle. CR≧2	Front-Back	θ_f - $\theta_{b,}$ $\phi = 0^\circ$	+30	to	-25	deg.
(Shown in 3	3.3)	Left-Right	θ_l - $\theta_{r,}$ $\phi = 0^{\circ}$	+30	to	-30	deg.
Response	Rise Note 2	T _{ON}		_	140	280	msec
Time	Decay Note 3	T_{OFF}		_	140	280	msec

Note 1: Contrast ratio is defined as follows.

 $CR = L_{OFF} / L_{ON}$

 L_{ON} : Luminance of the ON segments, L_{OFF} : Luminance of the OFF segments

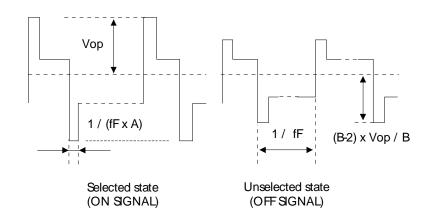
Note 2: The time that the luminance level reaches 90% of the saturation level from 0% when ON signal is applied.

Note 3: The time that the luminance level reaches 10% of the saturation level from 100% when OFF signal is

Note 4: Definition of Driving Voltage V_D . Assuming that the typical driving waveforms shown below are applied to the LCD Panel at /A Duty - 1/B Bias (A: Duty Number, B: Bias Number). Driving voltage V_D is defined s follows: $V_D = (Vth1+Vth2)/2$

Vth1: The voltage VO-P that should provide 50% of the saturation level in the luminance at the segment which the ON signal is applied to.

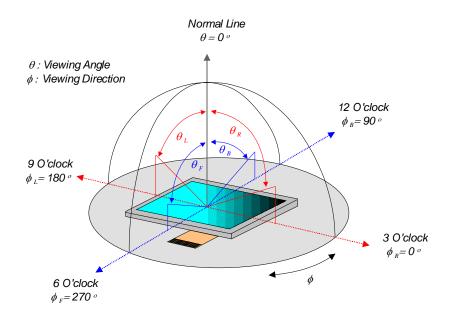
Vth2: The voltage VO-P that should provide 50% of the saturation level in the luminance at the segment which the OFF signal is applied to.



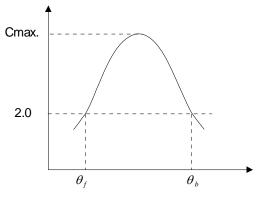
3.3 Definition of Viewing Angle and Optimum Viewing Area



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3.4 Definition of Viewing Angle θ_f and θ_b

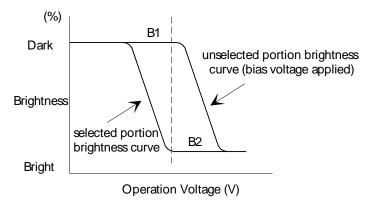


Viewing angles θ (ϕ fixed)

Optimum viewing angle with the naked eye and viewing angle θ at Cmax. Above are not always the same.

3.5 Definition of Contrast C

C= Brightness of selected dot (B1)/ Brightness of unselected dot (B2)





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4. <u>I/O Terminal</u>

4.1 Pin Assignment

LCD

No.	Symbol	Level	Function				
1	V1	_	LCD driving voltage supplying terminal				
2	V2	_	LCD driving voltage supplying terminal				
3	V3	_	LCD driving voltage supplying terminal				
4	V4		LCD driving voltage supplying terminal				
5	V5		LCD driving voltage supplying terminal				
6	VSS		GND (VSS=0V)				
7	D7	I/O	Data bit 7 in parallel operation				
8	D6	I/O	Data bit 6 in parallel operation				
9	D5	I/O	Data bit 5 in parallel operation				
10	D4	I/O	Data bit 4 in parallel operation				
11	D3	I/O	Data bit 3 in parallel operation				
12	D2	I/O	Data bit 2 in parallel operation				
13	D1	I/O	Data bit 1 in parallel operation				
14	D0	I/O	Data bit 0 in parallel operation				
15	Е	I	When connect to the 68 type MPU, the enable signal is "H" active				
16	R/W	I	When connect to the 68 type MPU, R/W="H" active				
17	A0	I/O	When A0="H", display data="L", instruction				
18	/CS	I	Chip select terminal. (Data I/O available during /CS="L"				
19	/RES	I	Reset terminal. When /RES goes to "L", the initialization is performed				
20	VDD		Power supply for logic (VDD=3.3V)				

LED Backlighting

No.	Symbol	Level Function			
1.	LEDA (LED+)	Power Supply for LED Backlight Anode (+)			
2.	LEDK (LED-)		LED Backlight Power Supply Cathode (-)Ground Potential		

TOUCH PANEL

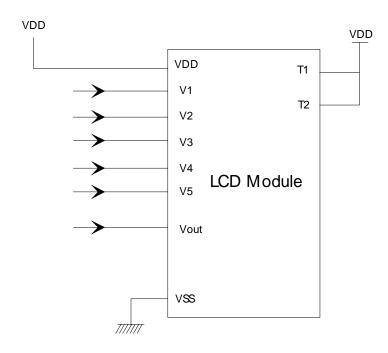
Pin No.	Symbol	Level	Function
1.	XR(X2)		Touch Panel Signal
2.	YU(Y2)		Touch Panel Signal
3.	XL(X1)		Touch Panel Signal
4.	YL(Y1)		Touch Panel Signal



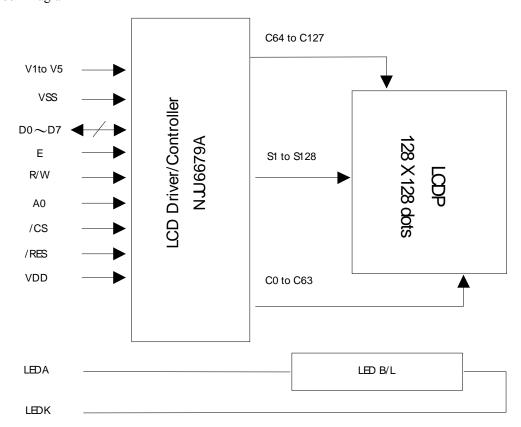
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4.2 Example of Power Supply

External power is applied for this model: All of V1 to V5 and Vout supply from outside internal power supply (Instruction) (T1,T2) = (H,H).



4.3 Block Diagram





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5. Reliability Test

5.1 Test Item

No change on display and in operation under the following test condition.

No.	Test Item	Description	Condition	Note
1.	High Temperature (Operation)	Durability test under long time high temperature with electrical stress (voltage, current)	70°C ± 2°C 96hrs	
2.	High Temperature (Storage)	Durability test under long time high temperature storage	80°C ± 2°C 96hrs	4
3.	Low Temperature (Operation)	Durability test under long time low temperature with electrical stress (voltage, current)	-20°C ± 2°C, 96hrs	3
4.	Low Temperature (Storage)	Durability test under long time low temperature storage	-30°C ± 2°C, 96hrs	3, 4
5.	Damp Proof Test	Durability test under long time high temperature and high humidity	40°C± 2°C, 90% RH 96hrs	3,4
6.	Vibration Test	Total fixed amplitude: 1.5mm Vibration frequency: 10~55Hz One cycle 60 seconds to 3 directions of X, Y, Z for each 15 minutes		5
7.	Drop Test	To be measured after dropping from 60cm high in packing state. F	nod corner dropping nce ne: once	

Note 1: Unless otherwise specified, tests will be conducted under the following condition,

Temperature : $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Humidity : $65\% \pm 5\%$

Note 2: Unless otherwise specified, tests will be not conducted under functioning state.

Note 3: No dew condensation to be observed.

Note 4: The function test shall be conducted after 4 hours storage at the normal temperature and humidity after removed from the test chamber.

Note 5: Vibration test will be conducted to the product itself without putting it in a container.

5.2 Judgment Standard



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Failure Mode			7	Test Iter	n			Judgment Standard
1 4.1.4.2 1.1.2 4.0	1	2	3	4	5	6	7	vuuginviiv suuruuru
Orientation	*	*	*	*	*			No remarkable degradation of appearance under bias/ non-bias condition
Current Value (IAC)	*	*	*	*	*			No remarkable increase
Contrast	*		*	*	*			No remarkable poor contrast
Domain	*	*	*	*	*			Less than 20% of all dots have reverse tilt of more than on third of one dot area.
Bubble (Inside Cell)	*	*	*	*	*	*		As per "Appearance Standard" (Note. including one which disappear after 25°C 2H)
Polarizer	*				*	*		As per "Appearance Standard" no remarkable appearance change
Glass Damage							*	As per "Appearance Standard"

Note. 1. * is strong linkage between Failure Mode and Test Item.

- 2. Number of Test Item should be referred to former page.
- 3. Judgment and Standard value should be fixed by other inspection standard and criteria samples.

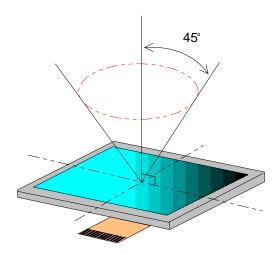
6. Appearance Standards

6.1 Inspection Conditions

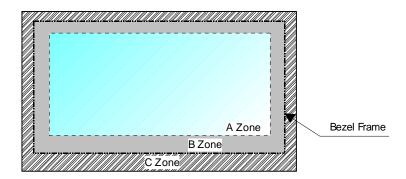


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The LCD shall be inspected under 40W white fluorescent light. The distance between the eyes and the sample shall be more than 30cm. All directions for inspecting the sample should be within 45° against perpendicular line.



6.2 Definition of Applicable Zones



A Zone : Active display area

B Zone: Area from outside of "A Zone" to validity viewing area

C Zone: Rest parts

A Zone + B Zone = Validity viewing area

6.3 Standards

No.	Parameter	Criteria



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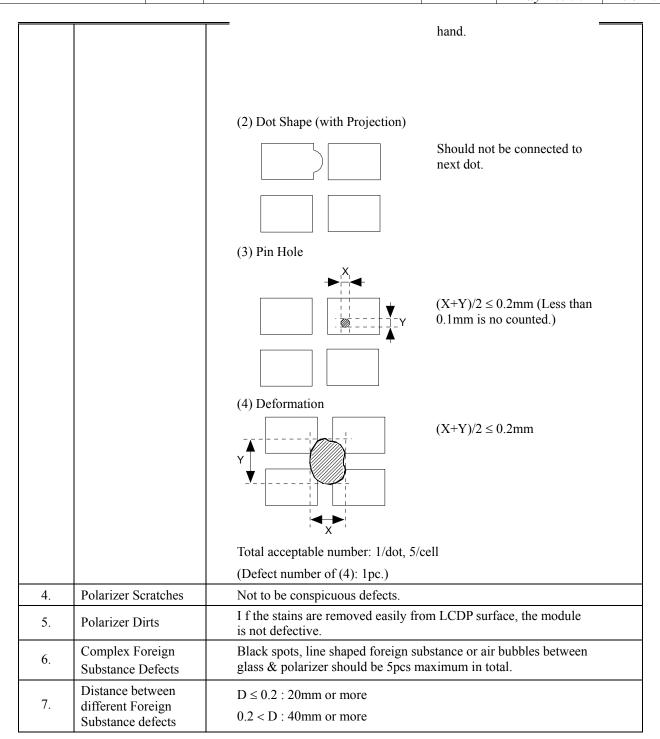
(1) P 1 (1			
			<u>. </u>
		-	
Dimension (mm)	A	В	С
D ≤ 0.2	*	*	*
$0.2 < D \le 0.3$	3	5	*
$0.3 < D \le 0.4$	2	3	*
$0.4 < D \le 0.5$	0	1	*
0.5 < D	0	0	*
$D = \frac{\text{(Long + Short)/2}}{\text{(Long + Short)/2}} *: Dis$	sregard		
(2) Line Shape			
Zone	Acceptable Number		
X (mm) Y (mm)	A	В	С
0.03 ≥ W	*	*	*
$2.0 \geq L \mid 0.05 \geq W \mid$	3	3	*
$1.0 \geq L 0.1 \geq W$	3	3	*
0.1 < W	In t	he same way	(1)
X: Length Y: Width *: Di	isregard		
Total defects shall not exceed	5.		
Zone	Acc	ceptable Num	ber
Dimension (mm)	A	В	С
D < 0.3	*	*	*
0.3 < D \le 0.4	3	*	*
0.4 < D < 0.6	2	3	*
0.6 < D	0	0	*
*: Disregard	l		
Total defects shall not exceed	3.		
	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

To be continued.....

No.	Parameter	Criteria			
		(1) Dot Shape (with Dent)			
		0.15≥ →			
		As per the sketch of left			



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7. Handling and Precautions

The Following precautions will guide you in handling our product correctly.

- 1 Liquid crystal display devices
 - 1.1 The liquid crystal display device panel used in the liquid crystal display module is made of



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- plate glass. Avoid any strong mechanical shock. Should the glass break handle it with care.
- 1.2 The polarizer adhering to the surface of the LCD is made of a soft material. Guard against scratching it.
- 2 Care of the liquid crystal display module against static electricity discharge.
 - 2.1 When working with the module, be sure to ground your body and any electrical equipment you may be using. We strongly recommend the use of anti static mats (made of rubber), to protect work tables against the hazards of electrical shock.
 - 2.2 Avoid the use of work clothing made of synthetic fibers. We recommend cotton clothing or other conductivity-treated fibers.
 - 2.3 Slowly and carefully remove the protective film from the LCD module, since this operation can generate static electricity.
- When the LCD module alone must be stored for long periods of time:
 - 3.1 Protect the modules from high temperature and humidity.
 - 3.2 Keep the modules out of direct sunlight or direct exposure to ultra-violet rays.
 - 3.3 Protect the modules from excessive external forces.
- 4 Use the module with a power supply that is equipped with an over current protector circuit, since the module is not provided with this protective feature.
- Do not ingest the LCD fluid itself should it leak out of a damaged LCD module. Should hands or clothing come in contact with LCD fluid, wash immediately with soap.
- Conductivity is not guaranteed for models that use metal holders where solder connections between the metal holder and the PCB are not used. Please contact us to discuss appropriate ways to assure conductivity.

8. Warranty

This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used



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in any of the above applications, we will need to enter into a separate product liability agreement.

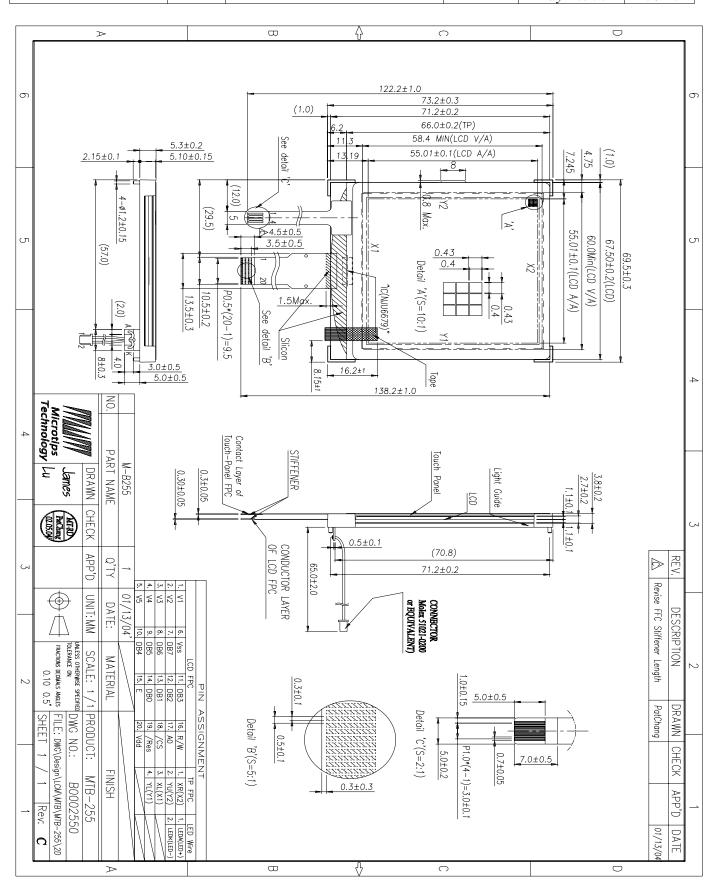
- We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
- We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- We cannot accept responsibility for industrial property, which may arise through the use of your product, with exception to those issues relating directly to the structure or method of manufacturing of our product. Microtips-origin longer than one year from Microtips production.

9. Dimensional Outlines

• See the next page......



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