

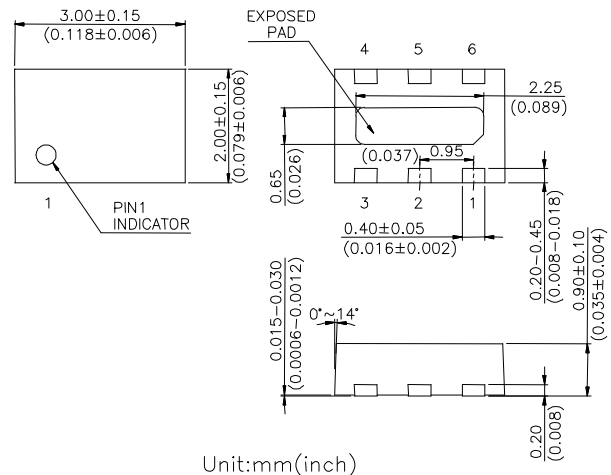
Features

- **Low Insertion Loss** : 0.50 dB @ 2.7 GHz
0.60 dB @ 3.9 GHz
- **Isolation**: 25.0 dB @ 2.7 GHz
25.0 dB @ 3.9 GHz
- **Low DC Power Consumption**
- **Miniature SON6L (2x3 mm) Plastic Lead (Pb) Free Package, RoHS Compliant**
- **PHEMT process**

Description

The HWS466 is a GaAs PHEMT MMIC SPDT switch operating at DC-6 GHz in a low cost miniature SON6L (2 x 3 mm) plastic lead (Pb) free package. The HWS466 features low insertion loss and high linearity with very low DC power consumption. This switch can be used in WiMAX or IEEE 802.11a/b/g WLAN PC card and access point applications as transmit/receive switch, antenna diversity switch, or band-selection switch.

SON6L (2 x 3 mm)



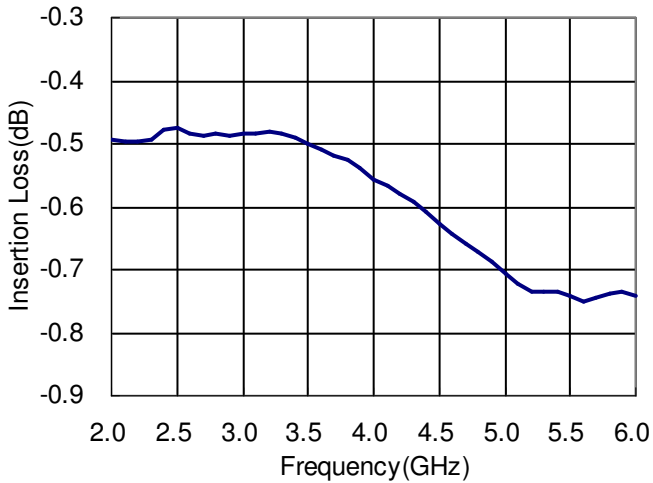
Electrical Specifications at 25 °C with 0, +3V Control Voltages

Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Insertion Loss	0.10-6.00 GHz		0.75		dB
	2.30-2.70 GHz		0.50	0.70	dB
	3.30-3.90 GHz		0.60	0.90	dB
	5.15-5.88 GHz		0.75	1.10	dB
Isolation	0.10-6.00 GHz		25.0		dB
	2.30-2.70 GHz	20.0	25.0		dB
	3.30-3.90 GHz	20.0	25.0		dB
	5.15-5.88 GHz	22.0	27.5		dB
Return Loss	0.10-6.00 GHz		12		dB
Input Power for One dB Compression	2.00-6.00 GHz				
	@+3V @+5V		36 38		dBm dBm
Second and Third Harmonics	Pin=20 dBm		-75		dBc
Input Third Order Intermodulation Intercept Point	25 dBm Per Tone, 3.80 GHz				
	@+3V @+5V		57 60		dBm dBm
Switching Time			50		nsec
Control Current	@+3V		5	100	uA

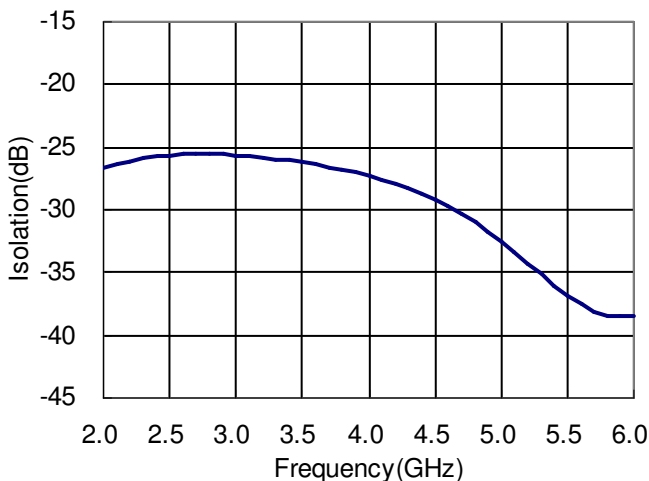
Note: All measurements made in a 50 ohm system with 0/+3.0V control voltages, unless otherwise specified.

Typical Performance Data with 8pF Capacitors @ +25°C

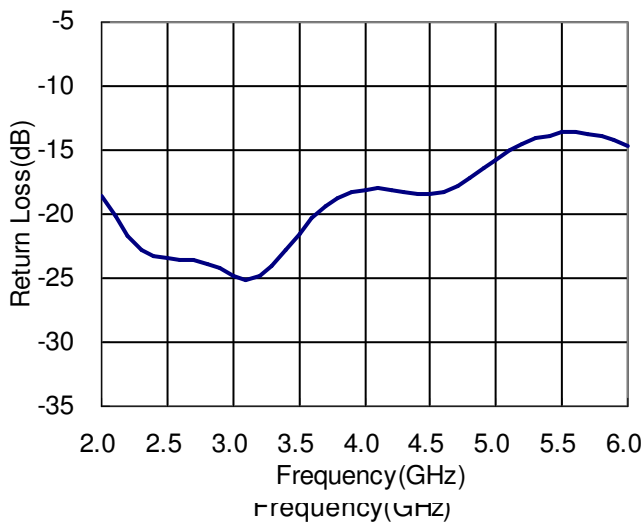
Insertion Loss vs Frequency



Isolation vs Frequency



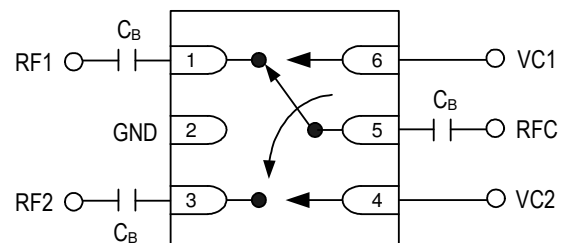
Return Loss vs Frequency



Absolute Maximum Ratings

Parameter	Absolute Maximum
RF Input Power	+36 dBm @ +3V
Control Voltage	+6V
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C

Pin Out (Top View)



Note:

- DC blocking capacitors $C_B=8\text{pF}$ are required on all RF ports.
- Exposed pad in the bottom must be connected to ground by via holes.

Logic Table for Switch On-Path

VC1	VC2	RFC-RF1	RFC-RF2
1	0	On	Off
0	1	Off	On

'1' = +2.7V to +5V

'0' = 0V to +0.2V