

PNP Low-Saturation Transistor

FMMT549

This device is designed with high-current gain and low-saturation voltage with collector currents up to 2 A continuous. Sourced from process PB.

ABSOLUTE MAXIMUM RATINGS

($T_A = 25^\circ\text{C}$ unless otherwise noted.) (Notes 1, 2)

Symbol	Parameter	Value	Unit
V_{CEO}	Collector-Emitter Voltage	-30	V
V_{CBO}	Collector-Base Voltage	-35	V
V_{EBO}	Emitter-Base Voltage	-5	V
I_C	Collector Current Continuous Peak Pulse Current	-1 -2	A
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to +150	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

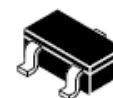
- These ratings are based on a maximum junction temperature of 150°C .
- These are steady-state limits. onsemi should be consulted on applications involving pulsed or low-duty-cycle operations.

THERMAL CHARACTERISTICS

($T_A = 25^\circ\text{C}$ unless otherwise noted.) (Note 3)

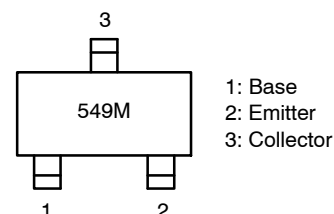
Symbol	Parameter	Max	Unit
P_D	Total Device Dissipation, by $R_{\theta JA}$ Derate Above 25°C	500 4	mW mW/ $^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	250	$^\circ\text{C}/\text{W}$

- Device is mounted on FR-4 PCB 4.5 inch x 5 inch, mounting pad 0.02 in² of 2 oz copper.



SOT-23/SUPERSOT™-23
CASE 527AG

MARKING DIAGRAM



549 = Specific Device Code
M = Date Code

ORDERING INFORMATION

Device	Package	Shipping†
FMMT549	SOT-23 (Pb-Free, Halide Free)	3,000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

FMMT549

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted.)

Symbol	Parameter	Test Conditions	Min	Max	Unit
BV_{CEO}	Collector–Emitter Breakdown Voltage	$I_C = -10\text{ mA}, I_B = 0$	-30	–	V
BV_{CBO}	Collector–Base Breakdown Voltage	$I_C = -100\text{ }\mu\text{A}, I_E = 0$	-35	–	V
BV_{EBO}	Emitter–Base Breakdown Voltage	$I_E = -100\text{ }\mu\text{A}, I_C = 0$	-5.0	–	V
I_{CBO}	Collector Cut–Off Current	$V_{CB} = -30\text{ V}, I_E = 0$	–	-100	nA
		$V_{CB} = -30\text{ V}, I_E = 0, T_A = 100^\circ\text{C}$	–	-10	μA
I_{EBO}	Emitter Cut–Off Current	$V_{EB} = -4.0\text{ V}, I_C = 0$	–	-100	nA
h_{FE}	DC Current Gain (Note 4)	$V_{CE} = -2.0\text{ V}, I_C = -50\text{ mA}$	70	–	
		$V_{CE} = -2.0\text{ V}, I_C = -500\text{ mA}$	100	300	
		$V_{CE} = -2.0\text{ V}, I_C = -1\text{ A}$	80	–	
		$V_{CE} = -2.0\text{ V}, I_C = -2\text{ A}$	40	–	
$V_{CE(sat)}$	Collector–Emitter Saturation Voltage (Note 4)	$I_C = -1\text{ A}, I_B = -100\text{ mA}$	–	-500	mV
		$I_C = -2\text{ A}, I_B = -200\text{ mA}$	–	-750	
$V_{BE(sat)}$	Base–Emitter Saturation Voltage (Note 4)	$I_C = -1\text{ A}, I_B = -100\text{ mA}$	–	-1.25	V
$V_{BE(on)}$	Base–Emitter On Voltage (Note 4)	$I_C = -1\text{ A}, V_{CE} = -2.0\text{ V}$	–	-1.0	V
f_T	Current Gain Bandwidth Product	$I_C = -100\text{ mA}, V_{CE} = -5\text{ V}, f = 100\text{ MHz}$	100	–	MHz
C_{obo}	Output Capacitance	$V_{CB} = -10\text{ V}, I_E = 0, f = 1\text{ MHz}$	–	25	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Pulse test: pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2.0\%$.

TYPICAL PERFORMANCE CHARACTERISTICS

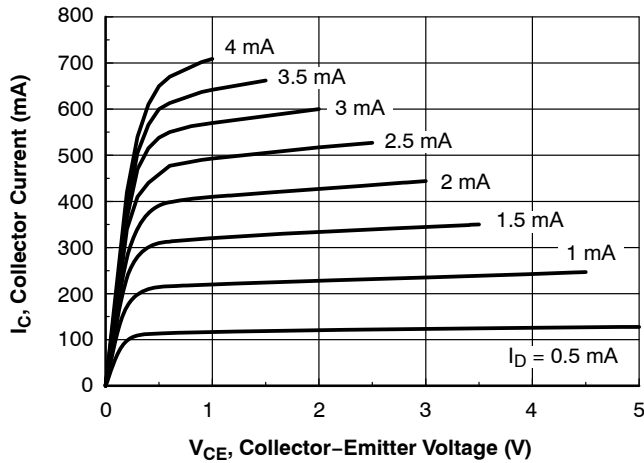


Figure 1. Collector-Emitter Voltage vs. Collector Current

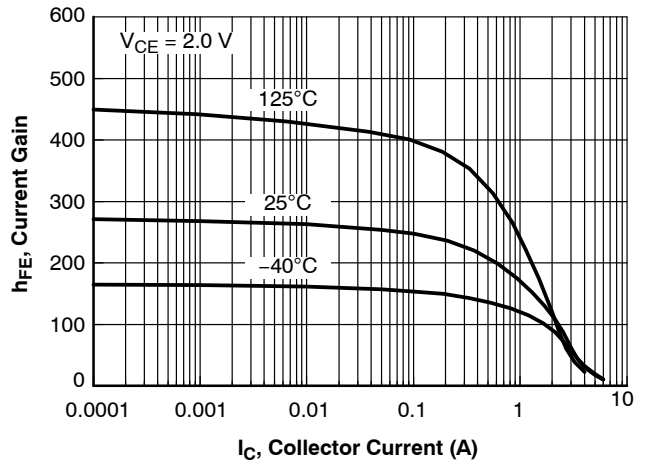


Figure 2. Current Gain vs. Collector Current

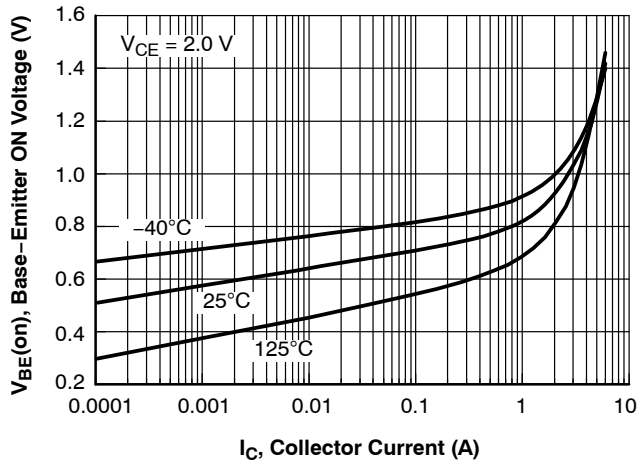


Figure 3. Base-Emitter On Voltage vs. Collector Current

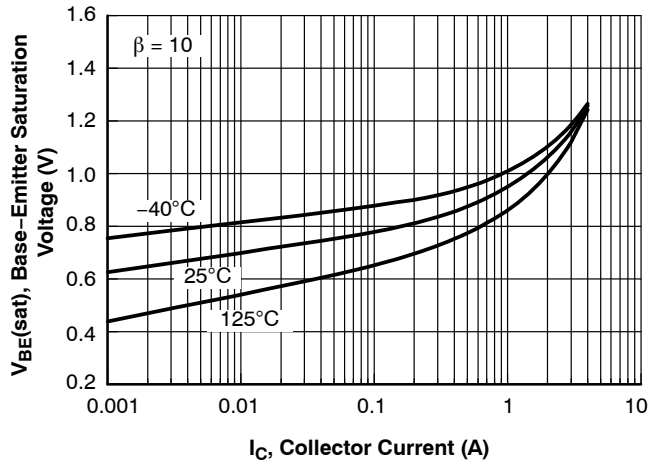


Figure 4. Base-Emitter Saturation Voltage vs. Collector Current

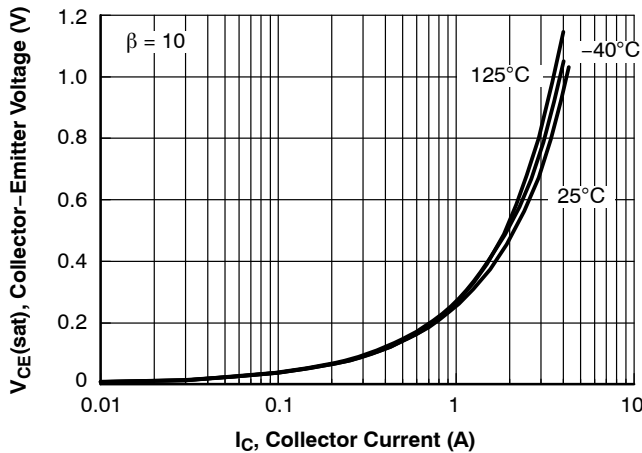


Figure 5. Collector-Emitter Saturation Voltage vs. Collector Current

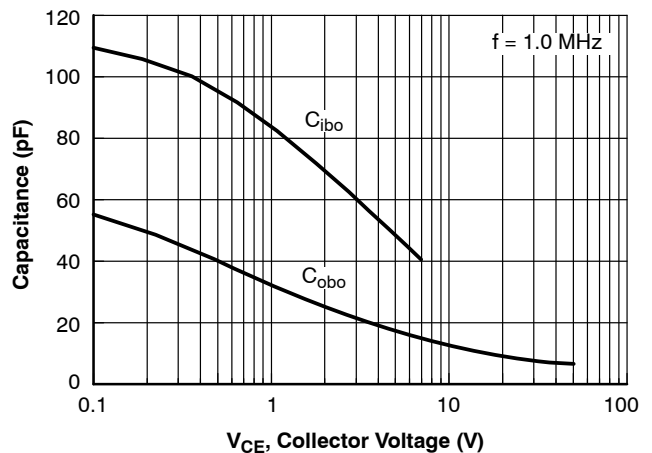
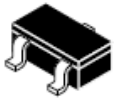
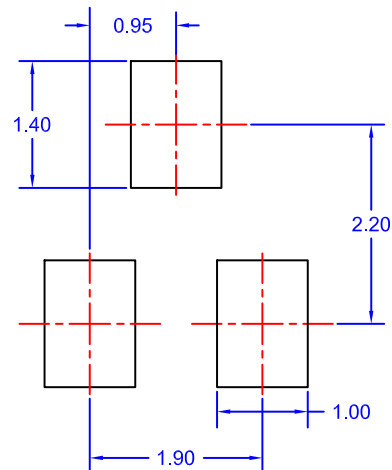
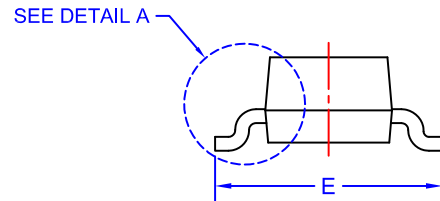
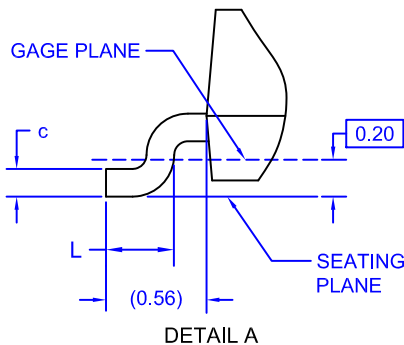
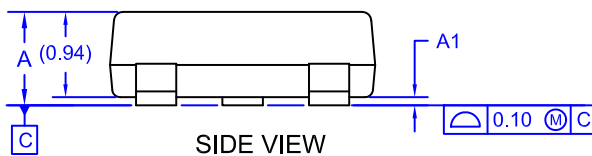
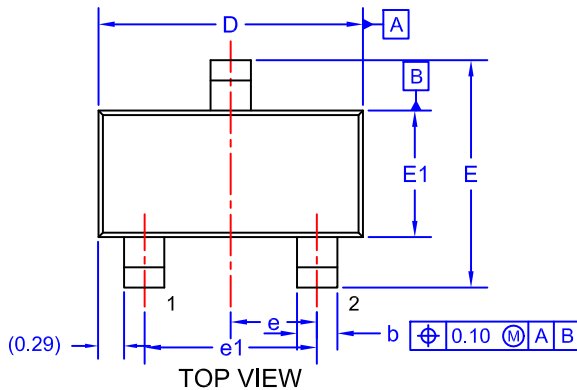


Figure 6. Input / Output Capacitance vs. Reverse Bias Voltage



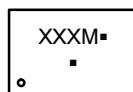
SOT-23/SUPERSOT™ –23, 3 LEAD, 1.4x2.9
CASE 527AG
ISSUE A

DATE 09 DEC 2019



*FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code
M = Month Code
▪ = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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