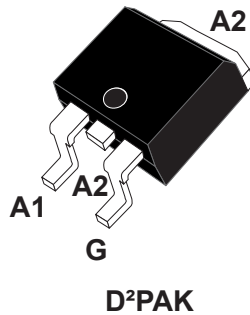
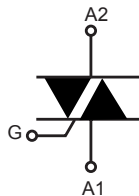


20 A 800 V D²PAK Snubberless™ Triac



A2: Anode2
A1: Anode1
G: Gate



Features

- High static dV/dt
- High dynamic turn-off commutation (dI/dt)_c
- 150 °C maximum junction temperature
- Three quadrants
- Surge capability V_{DSM} , V_{RSM} = 900 V
- Benefits:
 - High immunity to false turn-on thanks to high static dV/dt
 - Better turn-off in high temperature environments thanks to (dI/dt)_c
 - Increase of thermal margin due to extended working T_j up to 150 °C
 - Good thermal resistance due to non-insulated tab.

Applications

- General purpose AC line load switching
- Motor control circuits
- Home appliances
- Heating
- Lighting
- Inrush current limiting circuits
- Overvoltage crowbar protection

Description

Available in SMD, the T2035T-8G Triac can be used for the on/off or phase angle control function in general purpose AC switching where high commutation capability is required. The T2035T-8G can be used without a snubber RC circuit when the limits defined are respected.

D²PAK package is UL-94,V0 flammability resin compliance.

Package environmentally friendly Ecopack®2 graded (RoHS and Halogen Free compliance).

Snubberless™ is a trademark of STMicroelectronics.

Product status link

T2035T-8G

Product summary

$I_{T(RMS)}$	20 A
V_{DRM}/V_{RRM}	800 V
V_{DSM}/V_{RSM}	900 V
I_{GT}	35 mA

1 Characteristics

Table 1. Absolute maximum ratings (limiting values)

Symbol	Parameter		Value	Unit
I _{T(RMS)}	RMS on-state current (full sine wave)	T _c = 122 °C	20	A
I _{TSM}	Non repetitive surge peak on-state current (full cycle, T _j initial = 25 °C	t _p = 20 ms	160	A
		t _p = 16.7 ms	168	
I ² t	I ² t value for fusing	t _p = 10 ms	169	A ² s
di/dt	Critical rate of rise of on-state current, I _G = 2 x I _{GT} , tr ≤ 100 ns	T _j initial = 150 °C, f = 100 Hz	100	A/μs
V _{DRM} /V _{RRM}	Repetitive peak off-state voltage (50-60 Hz)	T _j = 125 °C	800	V
		T _j = 150 °C	600	V
V _{DSM} /V _{RSM}	Non Repetitive peak off-state voltage	t _p = 10 ms, T _j = 25 °C	900	V
I _{GM}	Peak gate current	t _p = 20 μs, T _j = 150 °C	4	A
V _{GM}	Peak Gate Voltage		5	V
P _{G(AV)}	Average gate power dissipation	T _j = 150 °C	1	W
T _{stg}	Storage junction temperature range		-40 to +150	°C
T _j	Operating junction temperature range		-40 to +150	°C

Table 2. Electrical characteristics ($T_j = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

Symbol	Test conditions		Quadrants; T_j		Value	Unit
$I_{GT}^{(1)}$	$V_D = 12\text{ V}$, $R_L = 30\text{ }\Omega$		I - II - III	Max.	35	mA
V_{GT}	$V_D = 12\text{ V}$, $R_L = 30\text{ }\Omega$		I - II - III	Max.	1.0	V
V_{GD}	$V_D = 600\text{ V}$, $R_L = 3.3\text{ k}\Omega$	$T_j = 150\text{ }^{\circ}\text{C}$	I - II - III	Max.	0.15	V
I_L	$I_G = 1.2 \times I_{GT}$		I - III	Max.	50	mA
			II	Max.	80	mA
$I_H^{(2)}$	$I_T = 500\text{ mA}$, gate open			Max.	45	mA
$dV/dt^{(2)}$	$V_D = 536\text{ V}$, gate open		$T_j = 125\text{ }^{\circ}\text{C}$	Min.	1500	$\text{V}/\mu\text{s}$
	$V_D = 402\text{ V}$, gate open		$T_j = 150\text{ }^{\circ}\text{C}$	Min.	1000	$\text{V}/\mu\text{s}$
$(di/dt)_c^{(2)}$	Without snubber		$T_j = 125\text{ }^{\circ}\text{C}$	Min.	27.5	A/ms
			$T_j = 150\text{ }^{\circ}\text{C}$	Min.	17.5	A/ms

1. Minimum I_{GT} is guaranteed at 5% of $I_{GT\text{ max}}$

2. For both polarities of A2 referenced to A1.

Table 3. Static characteristics

Symbol	Test conditions			Value	Unit
$V_{TM}^{(1)}$	$I_T = 28.2\text{ A}$, $t_p = 380\text{ }\mu\text{s}$	25 °C	Max.	1.50	V
$V_{TO}^{(1)}$	Threshold on-state voltage	150 °C	Max.	0.85	V
$R_D^{(1)}$	Dynamic resistance	150 °C	Max.	22	mΩ
I_{DRM}/I_{RRM}	$V_{DRM} = V_{RRM} = 800\text{ V}$	25 °C	Max.	5	μA
		125 °C		1.7	mA
	$V_{DRM} = V_{RRM} = 600\text{ V}$	150 °C	Max.	4.1	mA

1. For both polarities of A2 referenced to A1.

Table 4. Thermal resistance

Symbol	Parameter			Value	Unit
$R_{th(j-c)}$	Junction to case (AC)	D ² PAK	Max.	1.15	°C/W

1.2 Characteristics curves

Figure 1. Maximum power dissipation versus on-state RMS current

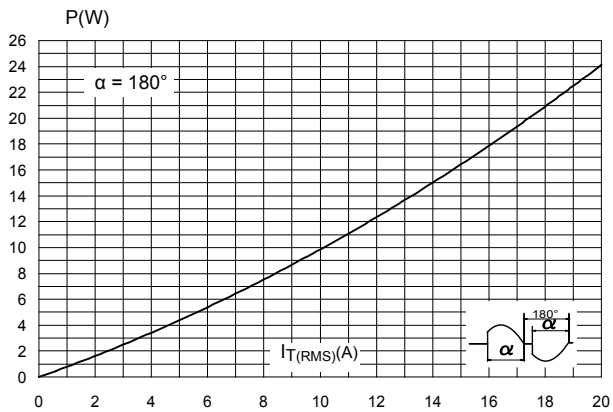


Figure 2. On-state RMS current versus case temperature

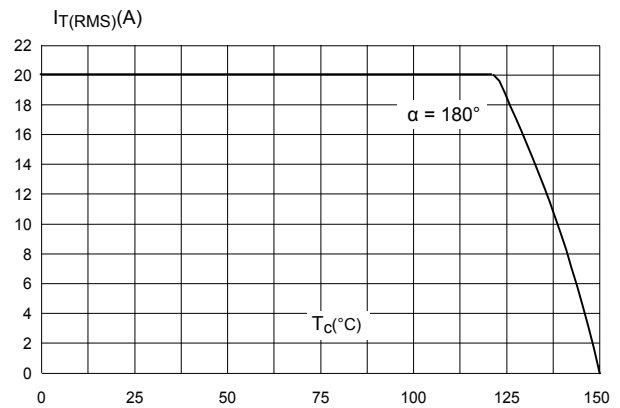


Figure 3. On-state RMS current versus ambient temperature (free air convection)

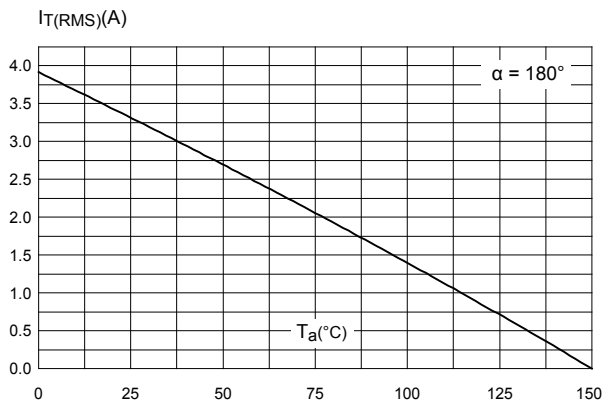


Figure 4. Relative variation of thermal impedance versus pulse duration

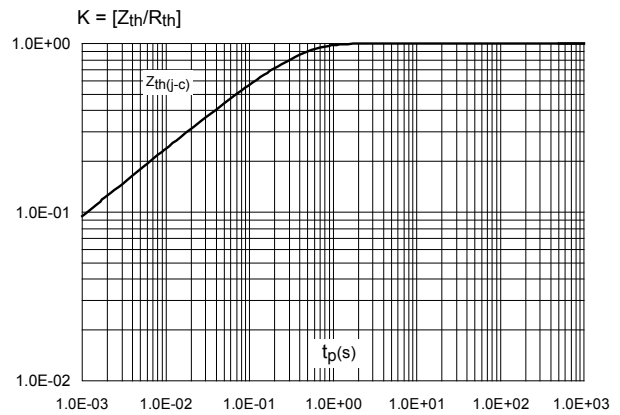


Figure 5. Relative variation of gate trigger current, holding current and latching current versus junction temperature (typical values)

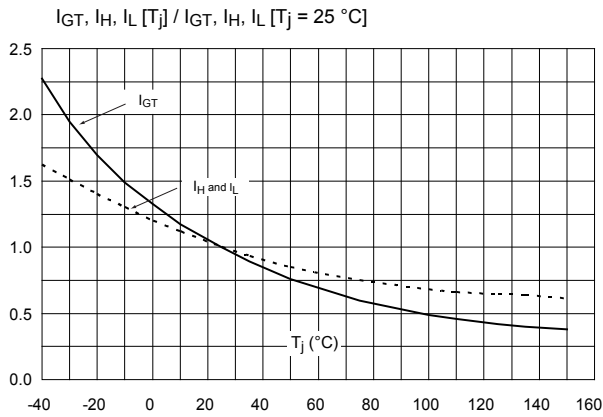


Figure 6. Surge peak on-state current versus number of cycles

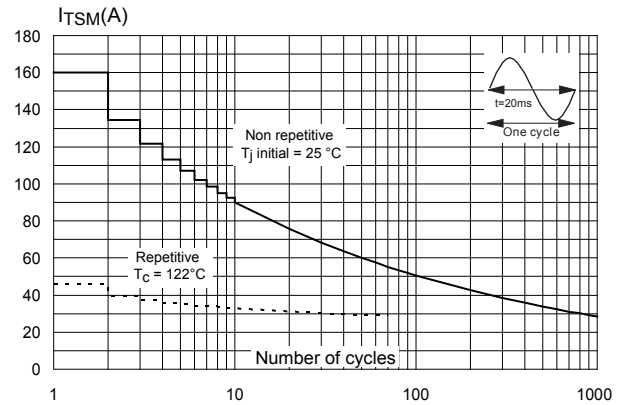


Figure 7. Non repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10\text{ ms}$

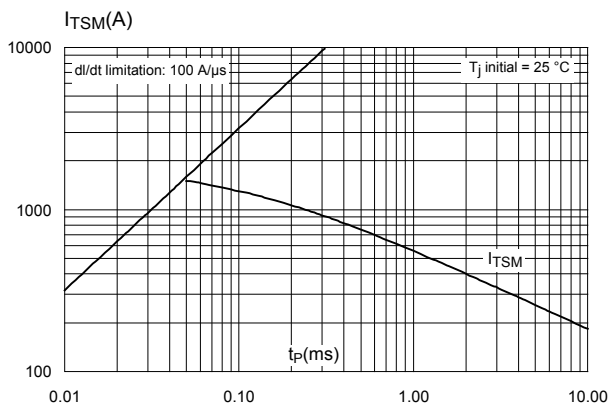


Figure 8. On-state characteristics (maximum values)

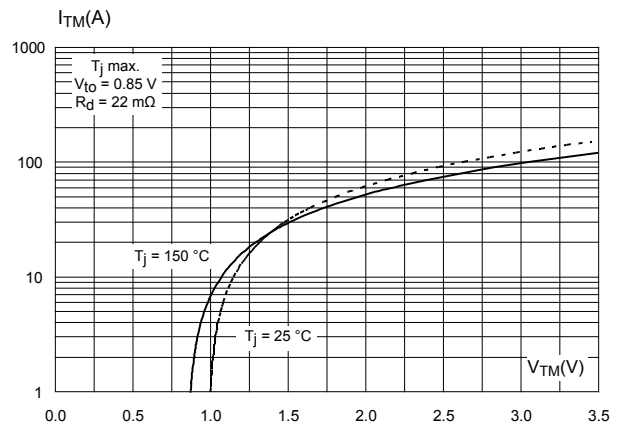


Figure 9. Relative variation of critical rate of decrease of main current versus junction temperature

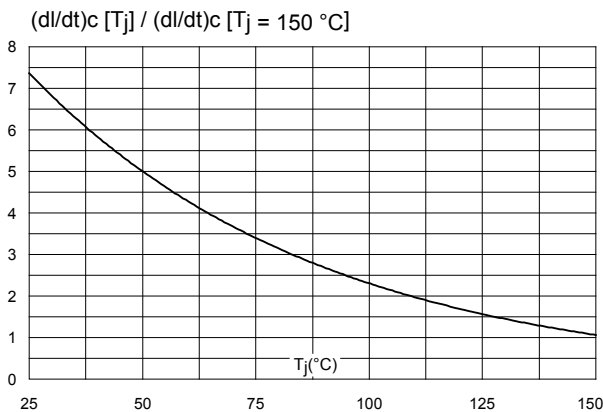


Figure 10. Relative variation of critical rate of decrease of main current versus reapplied $(dV/dt)_c$ (typical values)

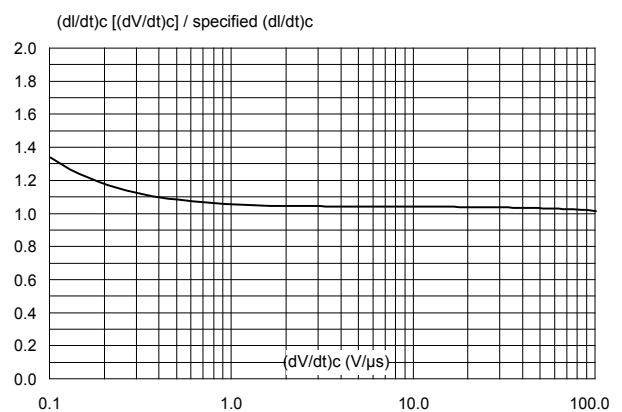


Figure 11. Relative variation of leakage current versus junction temperature for different values of blocking voltage (typical values)

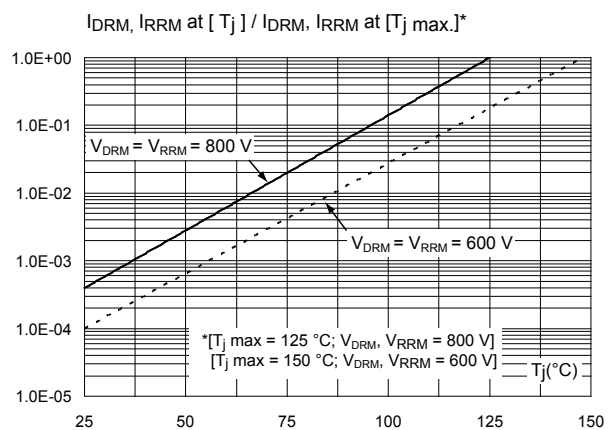
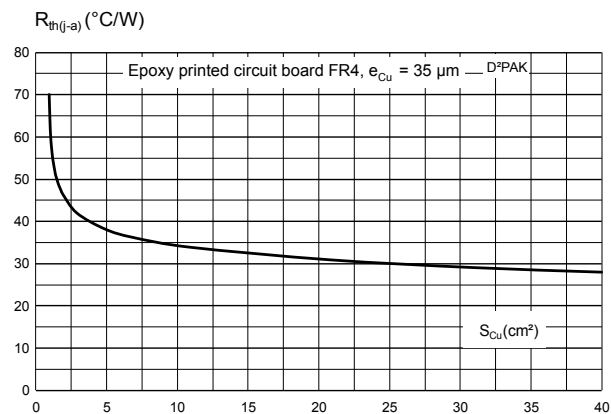


Figure 12. Thermal resistance junction to ambient versus copper surface under tab



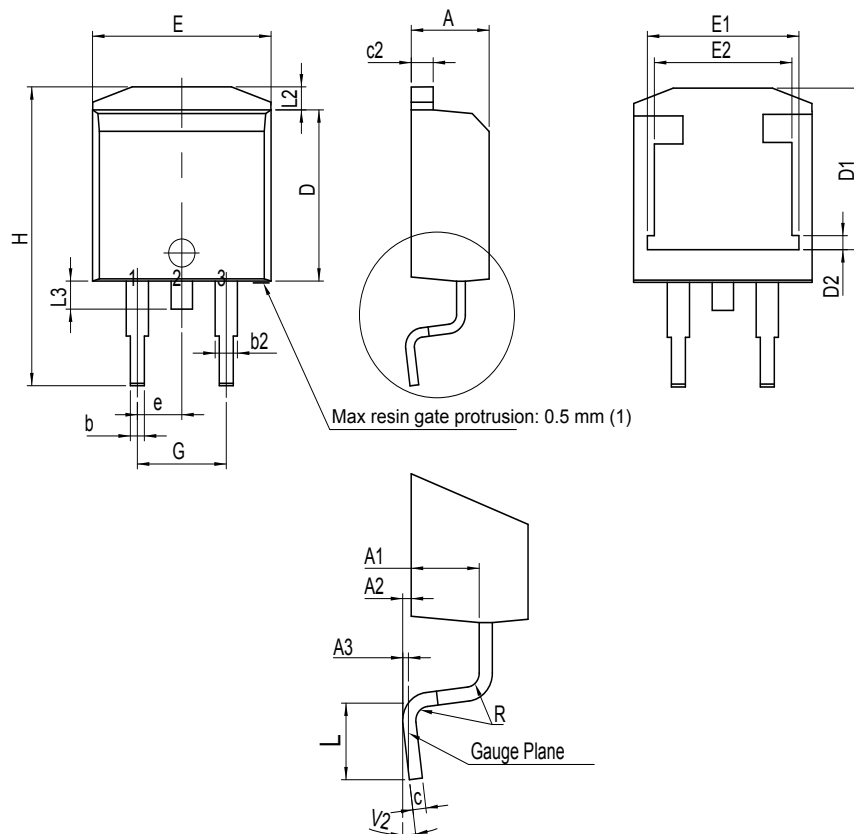
2 Package information

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

2.1 D²PAK package information

- ECOPACK®2compliant
- Lead-free package leads finishing
- Molding compound resin is halogen-free and meets UL94 standard level V0

Figure 13. D²PAK package outline



(1) Resin gate is accepted in each of position shown on the drawing, or their symmetrical.

Table 5. D²PAK package mechanical data

Ref.	Dimensions					
	Millimeters			Inches ⁽¹⁾		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.30		4.60	0.1693		0.1811
A1	2.49		2.69	0.0980		0.1059
A2	0.03		0.23	0.0012		0.0091
A3		0.25			0.0098	
b	0.70		0.93	0.0276		0.0366
b2	1.25		1.7	0.0492		0.0669
c	0.45		0.60	0.0177		0.0236
c2	1.21		1.36	0.0476		0.0535
D	8.95		9.35	0.3524		0.3681
D1	7.50		8.00	0.2953		0.3150
D2	1.30		1.70	0.0512		0.0669
e		2.54			0.1	
E	10.00		10.28	0.3937		0.4047
E1	8.30		8.70	0.3268		0.3425
E2	6.85		7.25	0.2697		0.2854
G	4.88		5.28	0.1921		0.2079
H	15		15.85	0.5906		0.6240
L	1.78		2.28	0.0701		0.0898
L2	1.27		1.40	0.0500		0.0551
L3	1.40		1.75	0.0551		0.0689
R		0.40			0.0157	
V2 ⁽²⁾	0°		8°	0°		8°

1. Dimensions in inches are given for reference only

2. Degrees

Technical drawing of a stepped profile. The profile consists of a large rectangular block on the left and two smaller rectangular blocks on the right. The dimensions are as follows:

- The total width of the profile is 16.90.
- The width of the large rectangular block is 8.90.
- The height of the large rectangular block is 10.30.
- The width of the top-right rectangular block is 3.70.
- The width of the bottom-right rectangular block is 3.70.
- The height of the top-right rectangular block is 5.08.
- The height of the bottom-right rectangular block is 1.30.

3 Ordering information

Figure 16. Ordering information scheme

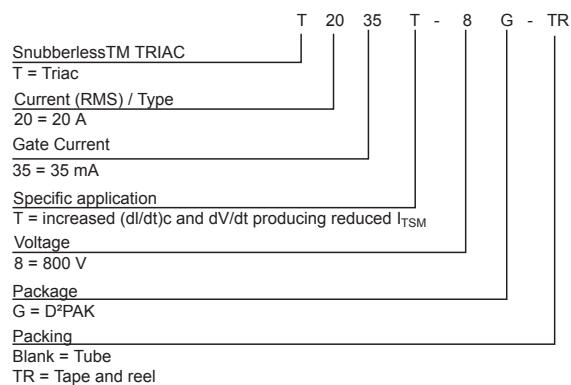


Table 6. Ordering information

Order code	Marking	Package	Weight	Base qty.	Delivery mode
T2035T-8G-TR	T2035T-8G	D²PAK	1.6 g	1000	Tape and reel
T2035T-8G				50	Tube

Revision history

Table 7. Document revision history

Date	Version	Changes
12-Sep-2018	1	Initial release.

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