

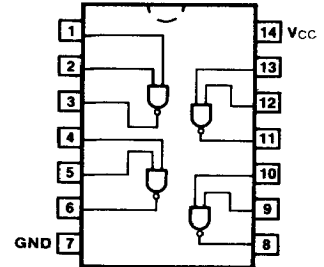
✓ 54/7437 011575
 ✓ 54LS/74LS37 011577

QUAD 2-INPUT NAND BUFFER

ORDERING CODE: See Section 9

PKGS	PIN OUT	COMMERCIAL GRADE	MILITARY GRADE	PKG TYPE
		$V_{CC} = +5.0 \text{ V} \pm 5\%$, $T_A = 0^\circ \text{C to } +70^\circ \text{C}$	$V_{CC} = +5.0 \text{ V} \pm 10\%$, $T_A = -55^\circ \text{C to } +125^\circ \text{C}$	
Plastic DIP (P)	A	7437PC, 74LS37PC		9A
Ceramic DIP (D)	A	7437DC, 74LS37DC	5437DM, 54LS37DM	6A
Flatpak (F)	A	7437FC, 74LS37FC	5437FM, 54LS37FM	31

**CONNECTION DIAGRAM
PINOUT A**



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INPUT LOADING/FAN-OUT: See Section 3 for U.L. definitions

PINS	54/74 (U.L.) HIGH/LOW	54/74LS (U.L.) HIGH/LOW
Inputs	1.0/1.0	0.5/0.25
Outputs	30/30	30/15 (7.5)

DC AND AC CHARACTERISTICS: See Section 3*

SYMBOL	PARAMETER		54/74		54/74LS		UNITS	CONDITIONS	
			Min	Max	Min	Max			
V _{OH}	Output HIGH Voltage	XM XC	2.4 2.4		2.5 2.7		V	V _{CC} = Max, I _{OH} = -1.2 mA V _{IN} = V _{IL}	
V _{OL}	Output LOW Voltage	XM, XC	0.4				V	I _{OL} = 48 mA	V _{CC} = Min V _{IN} = 2.0 V
		XM			0.4	I _{OL} = 12 mA			
		XC			0.5	I _{OL} = 24 mA			
I _{OS}	Output Short Circuit Current	XM XC	-20 -18	-70 -70	-30 -30	-130 -130	mA	V _{CC} = Min, V _{OUT} = 0 V	
I _{CCH} I _{CCL}	Power Supply Current		15.5 54		2.0 12		mA	V _{IN} = Gnd V _{IN} = Open	V _{CC} = Max
t _{PLH} t _{PHL}	Propagation Delay		22 15		20 20			ns	

*DC limits apply over operating temperature range; AC limits apply at $T_A = +25^\circ \text{C}$ and $V_{CC} = +5.0 \text{ V}$.