Unit: mm

TOSHIBA Transistor Silicon PNP Epitaxial Type (PCT Process)

## HN4A08J

Low Frequency Power Amplifer Applications **Power Switching Application** 

High DC Current Gain: hFE = 100 to 320

Low Saturation Voltage :  $V_{CE(sat)} = -0.4V$  (max)

 $(I_C = -500 \text{mA}, I_B = -20 \text{mA})$ 

#### Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

Characteristic	Symbol	Rating	Unit	
Collector-base voltage	V <sub>CBO</sub>	-30	V	
Collector-emitter voltage	V <sub>CEO</sub>	-25	V	
Emitter-base voltage	V <sub>EBO</sub>	<del>-</del> 5	V	
Collector current	Ic	-800	mA	
Base current	Ι <sub>Β</sub>	-160	mA	
Collector power dissipation	P <sub>C</sub> *	300	mW	
Junction temperature	Tj	150	°C	
Storage temperature range	T <sub>stg</sub>	-55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba

Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

\*Total rating. Power dissipation per element should not exceed 200mW.

### $2.9\pm0.2$ 1.BASE1 (B1) 2.EMITTER 3.BASE2 (B2) 4.COLLECTOR2 (C2) 5.COLLECTOR1 **SMV JEDEC** JEITA **TOSHIBA** 2-3L1A

+0.2 2.8 - 0.3

+0.2 1.6 -0.1

Weight: 0.014g (typ.)

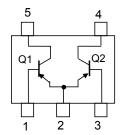
#### **Electrical Characteristics (Ta = 25°C) (Q1,Q2 Common)**

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	_	$V_{CB} = -30V$ , $I_{E} = 0$	_	_	-0.1	μA
Emitter cut-off current	I <sub>EBO</sub>	_	$V_{EB} = -5V, I_{C} = 0$	_	_	-0.1	μA
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	_	$I_C = -10 \text{mA}, I_B = 0$	-25	_	_	V
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	_	$I_E = -0.1 \text{mA}, I_C = 0$	-5	_	_	V
DC current gain	h <sub>FE(1)</sub>	_	$V_{CE} = -1V, I_{C} = -100mA$	100	_	320	
	h <sub>FE(2)</sub>	_	$V_{CE} = -1V, I_{C} = -800mA$	40	_	_	
Collector-emitter saturation voltage	V <sub>CE (sat)</sub>	_	I <sub>C</sub> = -500mA, I <sub>B</sub> = -20mA	_	_	-0.4	V
Collector-emitter saturation voltage	V <sub>BE</sub>	_	$V_{CE} = -1V, I_{C} = -10mA$	-0.5	_	-0.8	V
Transition frequency	f <sub>T</sub>	_	$V_{CE} = -5V, I_{C} = -10mA$	_	120	_	MHz
Collector output capacitance	C <sub>ob</sub>	_	V <sub>CB</sub> = -10V, I <sub>E</sub> = 0, f = 1MHz	_	13	_	pF

#### Marking

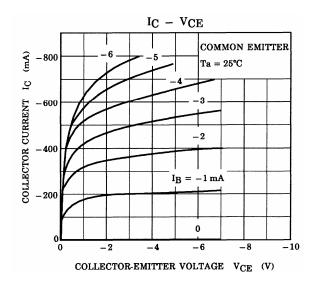
# 36

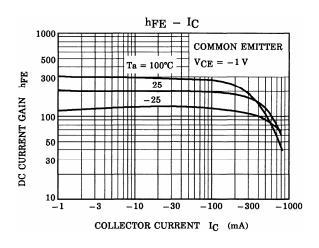
#### **Equivalent Circuit (Top View)**

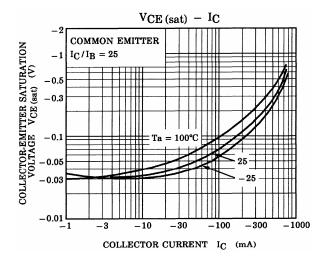


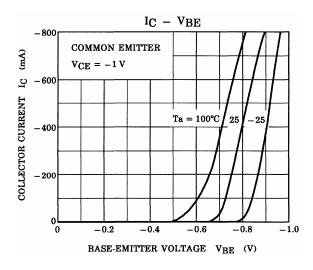
Start of commercial production 2000-09

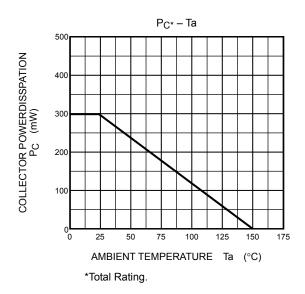
#### Q1,Q2 Common











2014-03-01

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