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September 2014

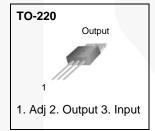
KA317 / LM317 3-Terminal Positive Adjustable Regulator

Features

- Output-Current In Excess of 1.5 A
- Output-Adjustable Between 1.2 V and 37 V
- Internal Thermal Overload Protection
- · Internal Short-Circuit Current Limiting
- Output-Transistor Safe Operating Area Compensation
- TO-220 Package

Description

This monolithic integrated circuit is an adjustable 3-terminal positive-voltage regulator designed to supply more than 1.5 A of load current with an output voltage adjustable over a 1.2 V to 37 V range. It employs internal current limiting, thermal shutdown, and safe area compensation.



Ordering Information

Product Number	Product Number Package		Operating Temperature	
LM317T	TO-220 (Single Gauge)	Rail	0°C to +125°C	
KA317TU	TO-220 (Dual Gauge)	Rail	0°C to +125°C	

Block Diagram

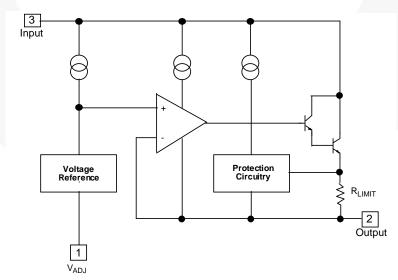


Figure 1. Block Diagram

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}\text{C}$ unless otherwise noted.

Symbol	Parameter	Value	Unit
V _I - V _O	Input-Output Voltage Differential	40	V
T _{LEAD}	Lead Temperature	230	°C
T _J	Operating Junction Temperature Range	0 to +125	°C
T _{STG}	Storage Temperature Range	-65 to +125	°C
$\Delta V_O/\Delta T$	Temperature Coefficient of Output Voltage	±0.02	%/°C

Thermal Characteristics

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Value	Units
P_{D}	Power Dissipation	Internally Limited	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	80	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction to Case	5	°C/W

Electrical Characteristics

 V_I - V_O = 5 V, I_O = 0.5 A, 0°C \leq T $_J$ \leq +125°C, I_{MAX} = 1.5 A, P_{DMAX} = 20 W, unless otherwise specified.

Symbol	Parameter	Conditions		Min.	Тур.	Max.	Unit	
D	Line Regulation ⁽¹⁾	$T_A = +25^{\circ}C, 3 \text{ V} \le V_I - V_O \le 40 \text{ V}$			0.01	0.04	- %/ V	
R _{LINE}	Line Regulation	3 V ≤ V _I - V _O ≤ 40 V			0.02	0.07		
R _{LOAD}	Load Regulation ⁽¹⁾	$T_A = +25^{\circ}C,$	V _O < 5 V		18	25	mV	
		$10\text{mA} \le I_{O} \le I_{MAX}$	V _O ≥ 5 V		0.4	0.5	%/V _O	
		10 mA ≤ I _O ≤ I _{MAX}	V _O < 5 V		40	70	mV	
			V _O ≥ 5 V		0.8	1.5	%/V _O	
I _{ADJ}	Adjustable Pin Current				46	100	μА	
Δl _{ADJ}	Adjustable Pin Current Change	$3 \text{ V} \le \text{V}_{\text{I}} - \text{V}_{\text{O}} \le 40 \text{ V},$ $10 \text{ mA} \le \text{I}_{\text{O}} \le \text{I}_{\text{MAX}}, \text{P}_{\text{D}} \le \text{P}_{\text{MAX}}$			2.0	5.0	μΑ	
V _{REF}	Reference Voltage	$3 \text{ V} \leq \text{V}_{\text{IN}} - \text{V}_{\text{O}} \leq 40 \text{ V},$ $10 \text{ mA} \leq \text{I}_{\text{O}} \leq \text{I}_{\text{MAX}}, \text{P}_{\text{D}} \leq \text{P}_{\text{MAX}}$		1.20	1.25	1.30	V	
ST _T	Temperature Stability				0.7		%/V _O	
I _{L(MIN)}	Minimum Load Current to Maintain Regulation	V _I - V _O = 40 V			3.5	12.0	mA	
I _{O(MAX)}	Maximum Output Current	Maximum Output	T 250C	$V_{I} - V_{O} \le 15 \text{ V},$ $P_{D} \le P_{MAX}$	1.5	2.2		
		T _A = 25°C	$V_{I} - V_{O} \le 40 \text{ V},$ $P_{D} \le P_{MAX}$		0.3		- A	
e _N	RMS Noise,% of V _{OUT}	$T_A = +25^{\circ}C$, 10 Hz $\leq f \leq$ 10 kHz			0.003	0.010	%/V _O	
RR	Ripple Rejection ⁽²⁾	V _O = 10 V,	without C _{ADJ}		60		40	
		$f = 120 \text{ Hz}$ $C_{ADJ} = 10 \mu\text{F}$		66	75		dB	
ST	Long-Term Stability, $T_J = T_{HIGH}$	T _A = +25°C for End Point Measurements, 1000 HR			0.3	1.0	%	

Notes:

- 1. Load and line regulation are specified at constant junction temperature. Change in V_D due to heating effects must be taken into account separately. Pulse testing with low duty is used ($P_{MAX} = 20 \text{ W}$).
- 2. C_{ADJ}, when used, is connected between the adjustment pin and ground.

Typical Performance Characteristics

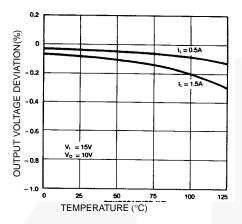


Figure 2. Load Regulation

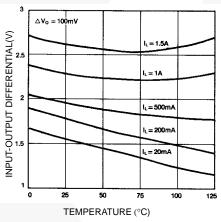


Figure 4. Dropout Voltage

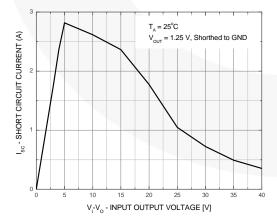


Figure 6. Short Circuit vs. Input-Output Voltage

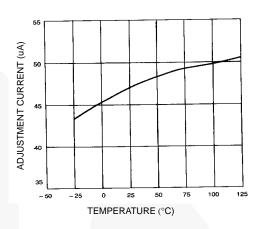


Figure 3. Adjustment Current

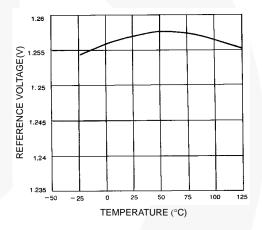
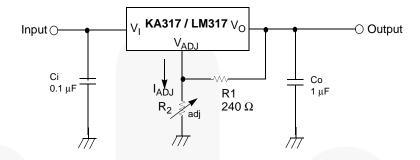


Figure 5. Reference Voltage

Typical Application(3)

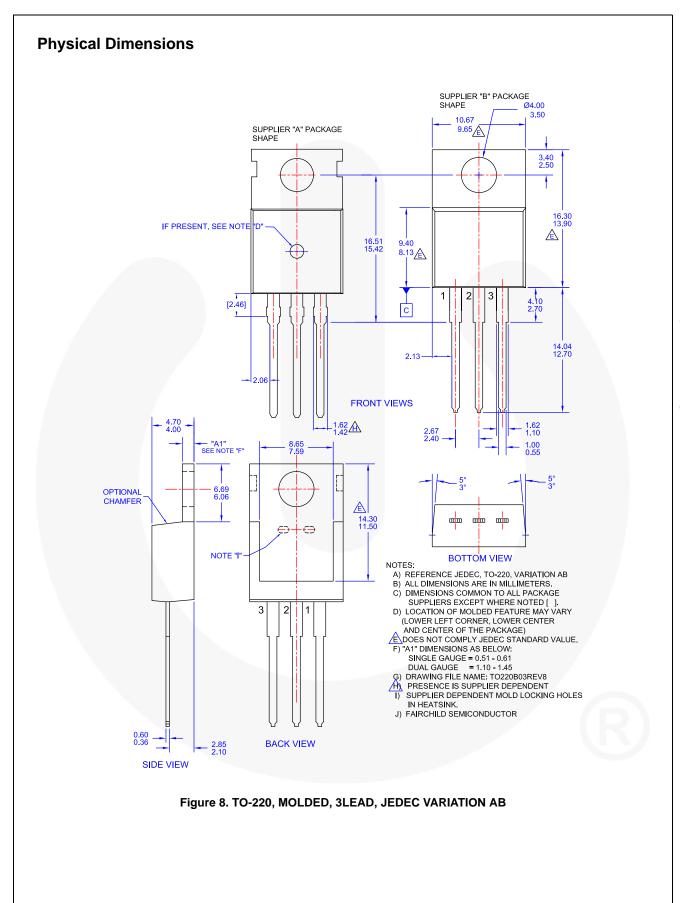


$$V_0 = 1.25 \text{ V} (1 + R_2 / R_1) + I_{ADJ}R_2$$

Figure 7. Typical Application

Note:

3. C_I is required when the regulator is located an appreciable distance from power supply filter. C_O is not needed for stability; however, it does improve transient response. Since I_{ADJ} is controlled to less than 100 μ A, the error associated with this term is negligible in most applications.







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Definition of Terms		
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