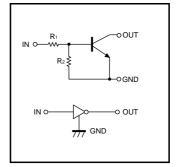
Digital transistors (built-in resistors)

DTC114EM / DTC114EE / DTC114EUA / DTC114ECA / DTC114ESA

Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on/off conditions need to be set for operation, making device design easy.

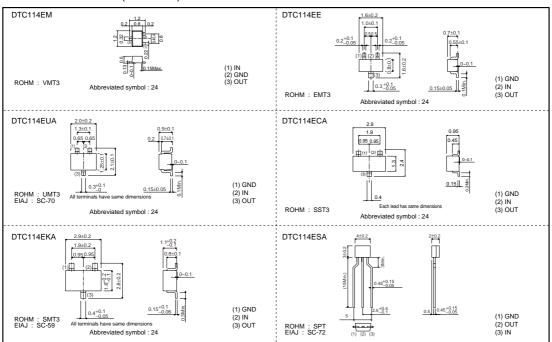
●Equivalent circuit



Structure

NPN digital transistor (with built-in resistors)

●External dimensions (Units : mm)



● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits(DTC114E □)							
Parameter		М	Е	UA	CA	KA	SA	Unit	
Supply voltage	Vcc	50							
Input voltage	VIN	-10~+40							
Output current	lo	50							
	IC(Max.)	100							
Power dissipation	Pd	150			200			mW	
Junction temperature	Tj	150							
Storage temperature	Tstg	-55~+150							

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
lancit coltana	VI(off)	-	-	0.5	.,	Vcc=5V, Io=100μA
Input voltage	VI(on)	3	-	-	V	Vo=0.3V, Io=10mA
Output voltage	V _{O(on)}	_	0.1	0.3	V	lo/l≔10mA/0.5mA
Input current	lı	-	-	0.88	mA	Vi=5V
Output current	IO(off)	-	-	0.5	μА	Vcc=50V, Vi=0V
DC current gain	Gı	30	_	-	-	Vo=5V, Io=5mA
Input resistance	R ₁	7	10	13	kΩ	-
Resistance ratio	R ₂ /R ₁	0.8	1	1.2	-	-
Transition frequency	f⊤	_	250	-	MHz	Vce=10V, Ie=-5mA, f=100MHz *

^{*} Transition frequency of the device

Packaging specifications

	Package	VMT3	EMT3	UMT3	SST3	SMT3	SPT
	Packaging type	Taping	Taping	Taping	Taping	Taping	Taping
	Code	T2L	TL	T106	T116	T146	TP
Туре	Basic ordering unit (pieces)	8000	3000	3000	3000	3000	5000
DTC114EM		0	-		-	-	_
DTC114EE		-	0	-	-	-	-
DTC114EUA		-	-	0	-	-	_
DTC114ECA		_	-	-	0	-	-
DTC114EKA		_	-	-	-	0	_
DTC114ESA		-	-	-	-	-	0

Electrical characteristic curves

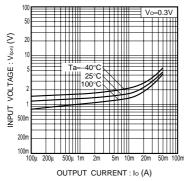


Fig.1 Input voltage vs. output current (ON characteristics)

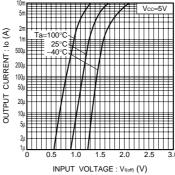


Fig.2 Output current vs. input voltage (OFF characteristics)

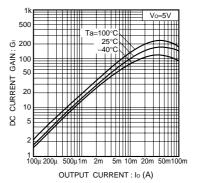


Fig.3 DC current gain vs. output current

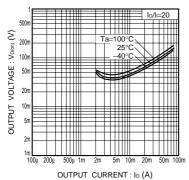


Fig.4 Output voltage vs. output current

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