





# NPN PRE-BIASED SMALL SIGNAL DUAL SURFACE MOUNT TRANSISTOR

### **Features**

- **Epitaxial Planar Die Construction**
- Complementary PNP Types Available (DDA)
- **Built-In Biasing Resistors**

**Mechanical Data** Case: SOT-363

202, Method 208

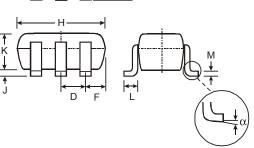
42 leadframe)

- Lead Free/RoHS Compliant (Note 3)
- "Green" Device (Note 4 and 5)

Classification Rating 94V-0

# MY XXN В NXX YM

→|A|←



	SOT-363	3					
Dim	Min	Max					
Α	0.10	0.30					
В	1.15	1.35					
С	2.00 2.20						
D	0.65 Nominal						
F	0.30	0.40					
Н	1.80	2.20					
J	_	0.10					
K	0.90	1.00					
L	0.25	0.40					
М	0.10 0.25						
α	0°	8°					
All Din	nensions	in mm					

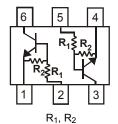
### Terminal Connections: See Diagram Marking Information: See Diagrams & Page 3 Ordering Information: See Page 3 Weight: 0.006 grams (approximate)

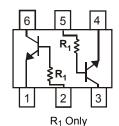
P/N	R1 (NOM)	R2 (NOM)	MARKING
DDC122LU	0.22K	10K	N81
DDC142JU	0.47K	10K	N82
DDC122TU	0.22K	OPEN	N83
DDC142TU	0.47K	OPEN	N84

Case Material: Molded Plastic. UL Flammability

Lead Free Plating (Matte Tin Finish annealed over Alloy

Moisture Sensitivity: Level 1 per J-STD-020C Terminals: Finish - Matte Tin Solderable per MIL-STD-





SCHEMATIC DIAGRAM

#### **Maximum Ratings** @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Value	Unit
Supply Voltage (6) to (1) and (3) to (4)		$V_{CC}$	50	V
Input Voltage (2) to (1) and (5) to (4)	DDC122LU DDC142JU	Vini	-5 to +6 -5 to +6	V
Input Voltage (1) to (2) and (4) to (5)	DDC122TU DDC142TU	V <sub>EBO (MAX)</sub>	5	V
Output Current	All	Ic	100	mA
Power Dissipation	(Note 1)	$P_d$	200	mW
Thermal Resistance, Junction to Ambient Air	(Note 2)	$R_{ heta JA}$	625	°C/W

### Notes:

- 150mW per element must not be exceeded.
- Mounted on FR4 PC Board with recommended pad layout at http://www.diodes.com/datasheets/ap02001.pdf.
- No purposefully added lead.
- Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead free/index.php.
- Product manufactured with Date Code UO (week 40, 2007) and newer are built with Green Molding Compound. Product manufactured prior to Date Code UO are built with Non-Green Molding Compound and may contain Halogens or Sb2O3 Fire Retardants.

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#### R1, R2 Types **Electrical Characteristics** $@T_A = 25^{\circ}C$ unless otherwise specified

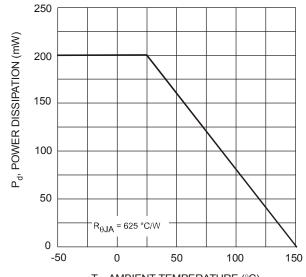
Characteristic	-	Symbol	Min	Тур	Max	Unit	Test Condition		
Input Voltage	DDC122LU DDC142JU	$V_{I(off)}$	0.3 0.3	_	_	٧	V <sub>CC</sub> = 5V, I <sub>O</sub> = 100μA		
	DDC122LU DDC142JU	\ \/,, \		_	2.0 2.0	/	$V_O = 0.3V$ , $I_O = 20mA$ $V_O = 0.3V$ , $I_O = 20mA$		
Output Voltage		$V_{O(on)}$		_	0.3V	V	$I_{O}/I_{I} = 5mA/0.25mA$		
Input Current DDC122 DDC142		I <sub>I</sub>	_	_	28 13	mA	V <sub>I</sub> = 5V		
Output Current		I <sub>O(off)</sub>	_	_	0.5	μΑ	$V_{CC} = 50V, V_I = 0V$		
DC Current Gain DDC122LU DDC142JU		Gı	56 56	_			V <sub>O</sub> = 5V, I <sub>O</sub> = 10mA		
Gain-Bandwidth Product*		$f_T$		200		MHz	V <sub>CE</sub> = 10V, I <sub>E</sub> = 5mA, f = 100MHz		

<sup>\*</sup> Transistor - For Reference Only

#### **Electrical Characteristics** R1- Only @T<sub>A</sub> = 25°C unless otherwise specified

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	50	_	_	V	$I_C = 50\mu A$	
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	40	_	_	V	I <sub>C</sub> = 1mA	
Emitter-Base Breakdown Voltage DDC122TU DDC142TU		BV <sub>EBO</sub>	5	_		V	$I_E = 50 \mu A$ $I_E = 50 \mu A$
Collector Cutoff Current		I <sub>CBO</sub>	_	_	0.5	μА	V <sub>CB</sub> = 50V
Emitter Cutoff Current DDC122TU DDC142TU		I <sub>EBO</sub>	_	_	0.5 0.5	μА	V <sub>EB</sub> = 4V
Collector-Emitter Saturation Voltage		V <sub>CE(sat)</sub>	_	_	0.3	V	I <sub>C</sub> = 5mA, I <sub>B</sub> = 0.25mA
DC Current Transfer Ratio DDC122TU DDC142TU		h <sub>FE</sub>	100 100	250 250	600 600	_	I <sub>C</sub> = 1mA, V <sub>CE</sub> = 5V
Gain-Bandwidth Product*		f <sub>T</sub>	_	200		MHz	V <sub>CE</sub> = 10V, I <sub>E</sub> = -5mA, f = 100MHz

<sup>\*</sup> Transistor - For Reference Only



T<sub>A</sub>, AMBIENT TEMPERATURE (°C) Fig. 1 Power Derating Curve

(150mW per element must not be exceeded).

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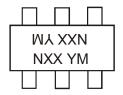


## Ordering Information (Note 6)

Device	Packaging	Shipping
DDC122LU-7-F	SOT-363	3000/Tape & Reel
DDC142JU-7-F	SOT-363	3000/Tape & Reel
DDC122TU-7-F	SOT-363	3000/Tape & Reel
DDC142TU-7-F	SOT-363	3000/Tape & Reel

Notes: 6. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**



NXX = Product Type Marking Code See Page 1 Diagrams YM = Date Code Marking Y = Year ex: T = 2006 M = Month ex: 9 = September

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012
Code	Ν	Р	R	S	Т	C	٧	W	Х	Υ	Z

Ī	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
	Code	1	2	3	4	5	6	7	8	9	0	N	D

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