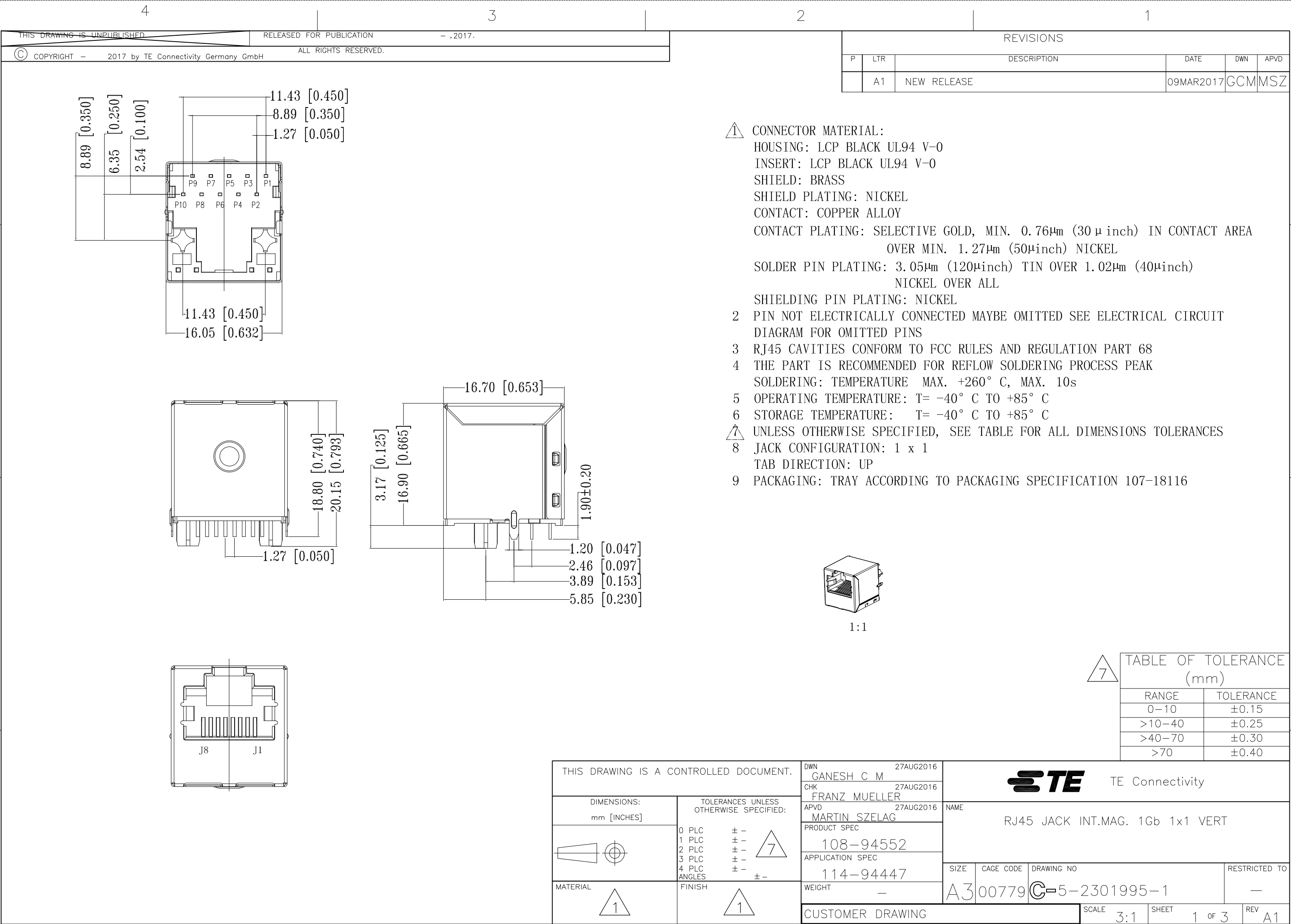
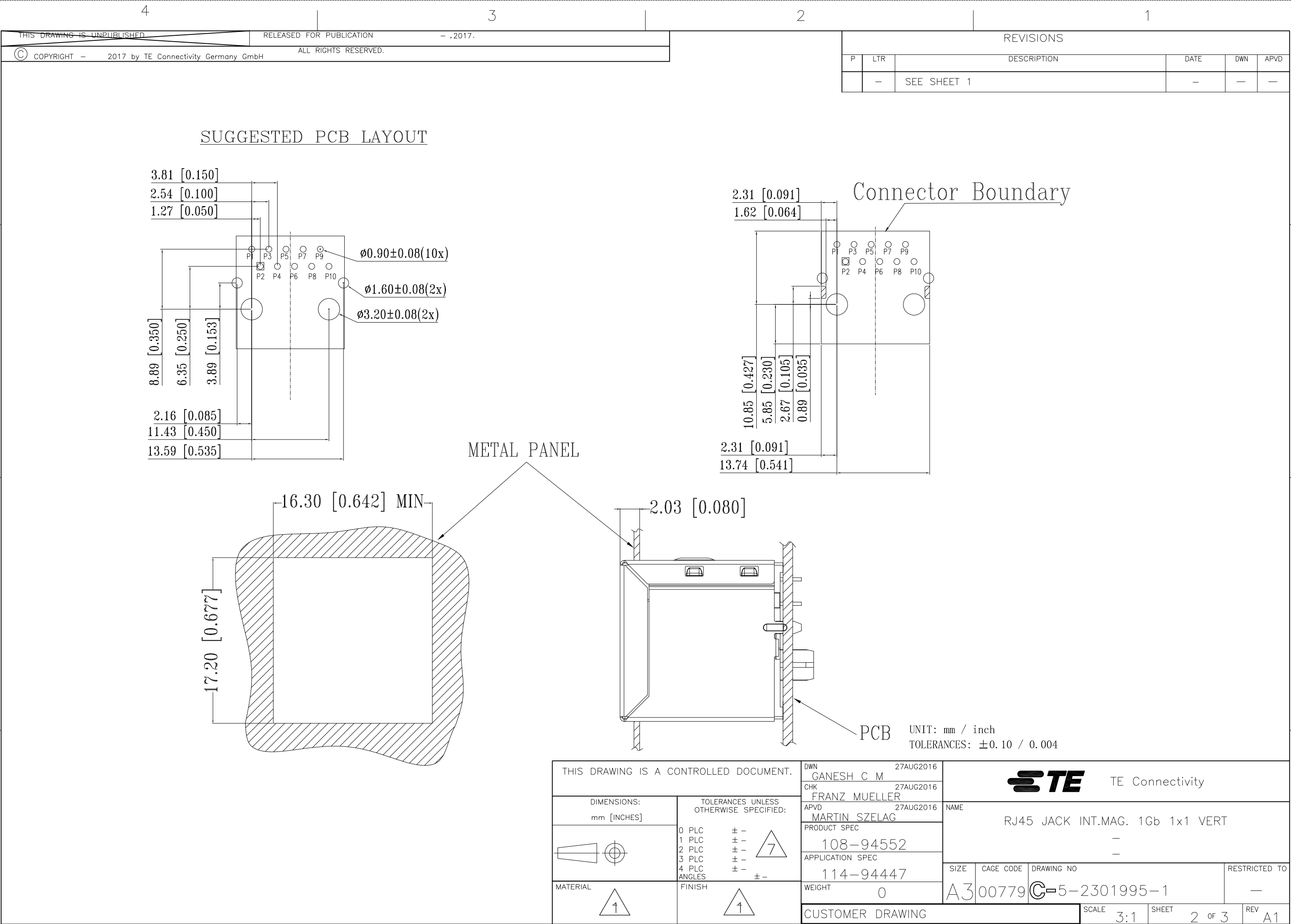
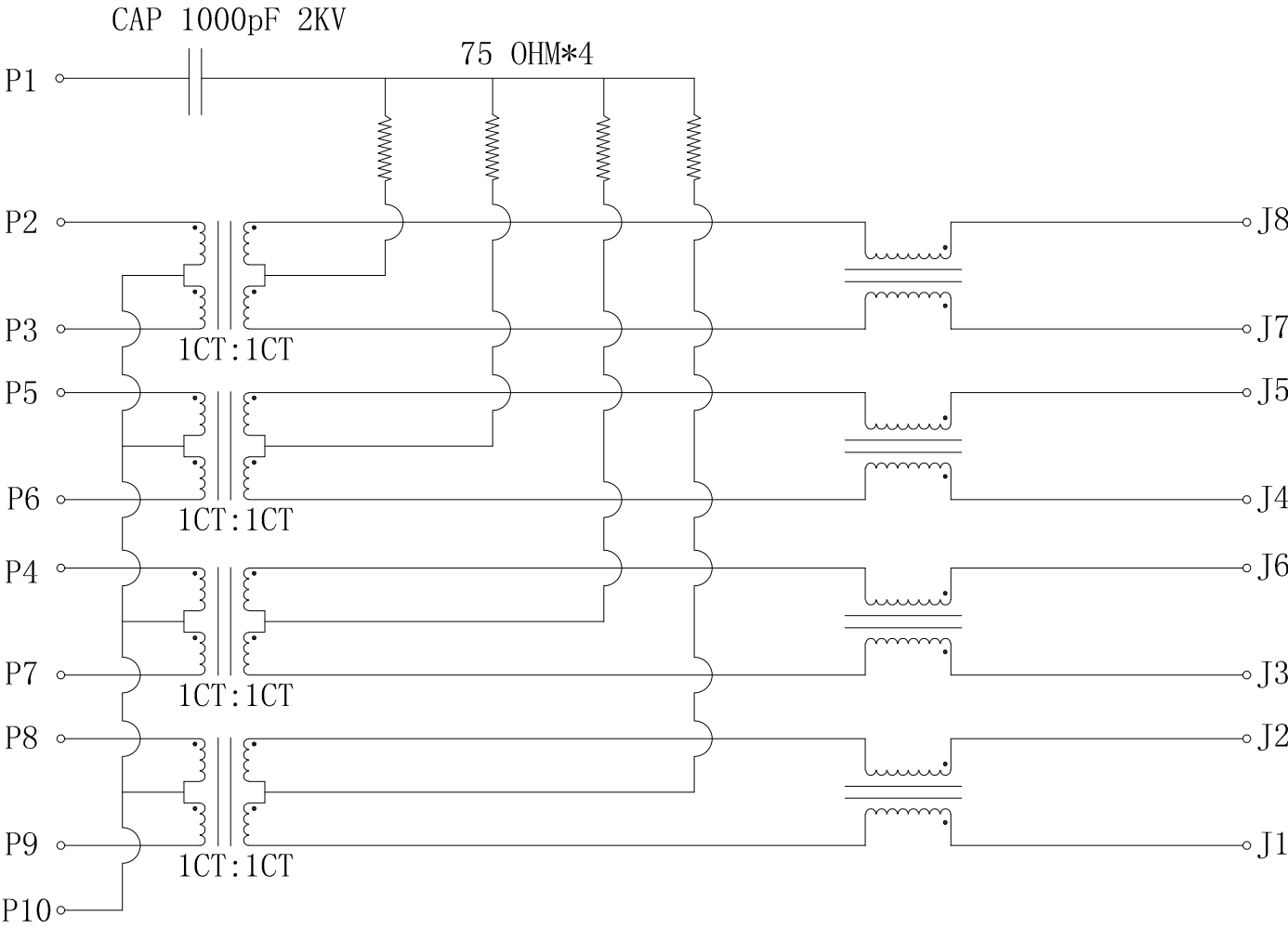






REVISIONS					
P	LTR	DESCRIPTION	DATE	DWN	APVD
	—	SEE SHEET 1	—	—	—

ELECTRICAL CIRCUIT DIAGRAM



- 1.0 Turn Ratio @100KHz: (P9~P8):(J1~J2) = 1:1±2%
(P7~P4):(J3~J6) = 1:1±2%
(P6~P5):(J4~J5) = 1:1±2%
(P3~P2):(J7~J8) = 1:1±2%
- 2.0 Primary Inductance: 350uH MIN @100KHZ,0.1V 8mA DC BIAS
- 3.0 DC Resistance: 1.2 OHMS MAX
- 4.0 Insertion Loss: 1-100MHz -1.0dB MAX
100-125MHz -1.2dB MAX
- 5.0 Return Loss: 1-40MHz -16dB MIN
40-60MHz -12dB MIN
60-80MHz -10dB MIN
80-100MHz -8dB MIN
- 6.0 CROSS TALK: 1-100MHz -30dB MIN
- 7.0 COMMON TO COMMON MODE ATTENUATION: 1-100MHz -30dB MIN
- 8.0 Isolation: PHY Side to Line Side: 2250VDC

5-2301995-1
WWYY-xxxx

CONTROL CODE

DATE CODE
"0213"

YY-YEAR

WW-WEEK

FCC

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN GANESH C M 27AUG2016	TE Connectivity		
		CHK FRANZ MUELLER 27AUG2016			
DIMENSIONS: mm [INCHES]		APVD MARTIN SZELAG 27AUG2016	NAME RJ45 JACK INT.MAG. 1Gb 1x1 VERT		
	TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± - 1 PLC ± - 2 PLC ± - 3 PLC ± - 4 PLC ± - ANGLES ± -	PRODUCT SPEC 108-94552			
		APPLICATION SPEC 114-94447			
MATERIAL 	FINISH 	WEIGHT 0	SIZE A3	CAGE CODE 00779	DRAWING NO C-5-2301995-1
CUSTOMER DRAWING			SCALE 3:1	SHEET 3 OF 3	REV A1