

DRP-480S Series - 480W Single Output PFC Function DIN RAIL Power Sup

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Features	Standards
AC input range selected by switch	
Built-in passive PFC function compliance to EN61000-3-2	
High efficiency 89% and low dissipation	
Protections: Short circuit/Over load/Over voltage/Over temperature	
Cooling by free air convection	
Built-in constant current limiting circuit	
Can be installed on DIN rail TS-35/7.5 or 15	
UL 508 (industrial control equipment) approved	
EN61000-6-2(EN50082-2) industrial immunity level	
100% full load burn-in test	

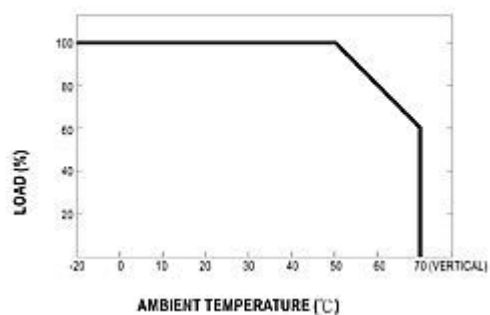
Specification	
INRUSH CURRENT (MAX.)	30A/115VAC 50A/230VAC, cold start
EFFICIENCY	89% typically
AC CURRENT	11A/115VAC 4A/230VAC
POWER FACTOR	>0.7 230VAC only
FREQUENCY RANGE	47 ~ 63Hz
VOLTAGE RANGE	90 ~ 132VAC / 180 ~ 264VAC by switch
VIBRATION	10 ~ 500Hz, 2G 10min/1cycle, 60min. each along X,Y,Z axes
TEMP. COEFFICIENT	±0.03%/°C (0 ~ 50°C)
WITHSTAND VOLTAGE	I/P-O/P: 3KVAC I/P-FG: 1.5KVAC O/P-FG: 0.5KVAC
ISOLATION RESISTANCE	I/P-O/P, I/P-FG, O/P-FG:100M Ohms/500VDC
STORAGE TEMPERATURE, HUMIDITY	-40°C ~ +85°C, 10 ~ 95% RH
MTBF	187.9Khrs min. MIL-HDBK-217F (25°C)
EMC	Compliance to EN55011 CISPR11), EN55022 (CISPR22), EN61204-3 Class B; EN61204-3; EN61000-3-2,-3; EN6
WORKING TEMPERATURE, HUMIDITY	-10°C ~ +70°C (Refer to output load derating curve), 20 ~ 90% RH non-condensing
LEAKAGE CURRENT	<3.5mA / 240VAC
OVER LOAD PROTECTION	105~150% of rate load. Constant current limiting, recovers automatically after fault condition is r
OVER VOLTAGE PROTECTION	Should an over voltage occur the outputs shall shutdown. Repower to recover
OVER TEMPERATURE PROTECTION	100°C±5°C (TSW:Detect on heatsink of power switch)Outputs will shutdown and auto recover once the te
SETUP, RISE, HOLD TIME	1200ms, 40ms, 20ms at full load
SAFETY STANDARDS	UL508, UL60950-1, TUV EN60950-1 Approved

Items

DRP-480 S Series

Part No	Output	R&N	Volt Adj.	Tol.	Line Reg.	Load Reg.	OVP
DRP-480S-48	48V, 10A	120mV	48V~55V	±1%	±0.5%	±1%	59V~66V
DR-480S-24	24V, 20A	120mV	24V~28V	±1%	±0.5%	±1%	30V~36V

■ Derating Curve



■ Output Derating VS Input Voltage

