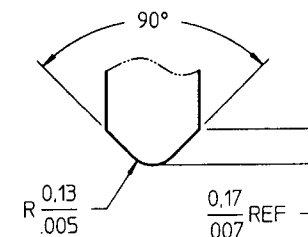
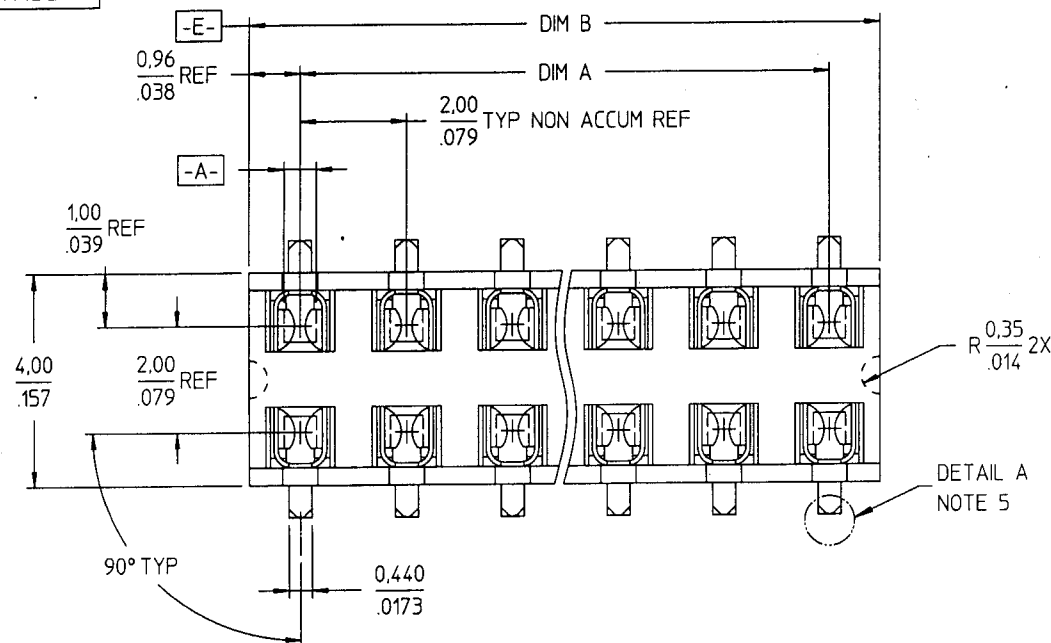
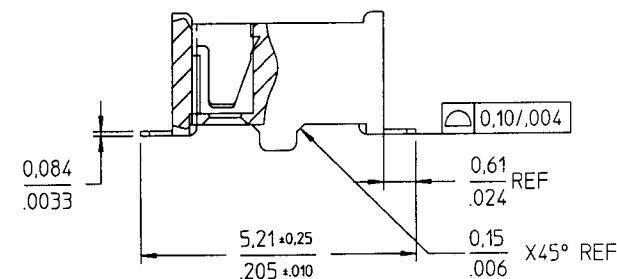
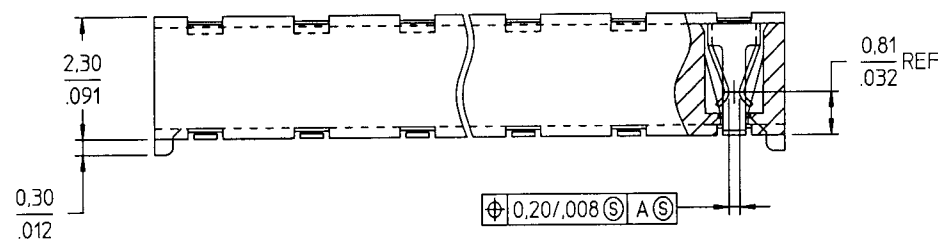


REV	DESCRIPTION	REV'D BY	DATE
M	ECR S40312	BS LOW	30DEC94
N	ECR S80123	BS LOW	19MAY98
P	ECR S80251	BS LOW	17NOV98

PRODUCT NO
SEE TABLE



DETAIL A

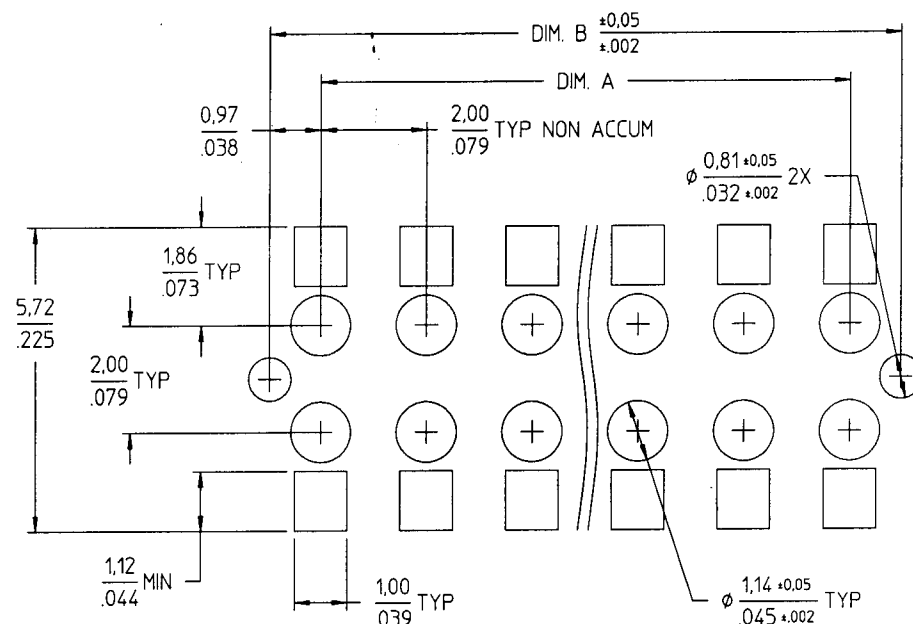


GEN. TOLERANCES		DO NOT SCALE DWG.		
X.X±0.3/.XX±.01 X.XX±0.13/.XXX±.005 X.XXX±0.051/.XXX±.0020 ANGLES = ±2°		 MM INCH		
		 		
		NAME	DATE	DESCRIPTION LOW PROFILE VCC SMT 2,3 BOTTOM ENTRY ASSY WITH 0.30/.012 LOCATING FEATURES
DRAWN	BS LOW	17NOV98		
CHECKED		17NOV98		
APPROVED		17NOV98		
			PRODUCT NO.	92084
			SCALE	SHT 1 OF 2

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PRODUCT NO	POSITIONS	DIM. A	DIM. B
92084-X04	2 X 2	2,00/.079	3,92/.154
-X06	2 X 3	4,00/.158	5,92/.233
-X08	2 X 4	6,00/.236	7,92/.312
-X10	2 X 5	8,00/.315	9,92/.391
-X12	2 X 6	10,00/.394	11,92/.469
-X14	2 X 7	12,00/.472	13,92/.548
-X16	2 X 8	14,00/.551	15,92/.627
-X18	2 X 9	16,00/.630	17,92/.706
-X20	2 X 10	18,00/.709	19,92/.784
-X22	2 X 11	20,00/.787	21,92/.863
-X24	2 X 12	22,00/.866	23,92/.942
-X26	2 X 13	24,00/.945	25,92/1,020
-X28	2 X 14	26,00/1,024	27,92/1,099
-X30	2 X 15	28,00/1,102	29,92/1,178

PLATING OPTIONS		REV
1	0,76um/30u" Au CONTACT AREA, SnPb SOLDER TAIL	M
3	0,38um/15u" Au CONTACT AREA, SnPb SOLDER TAIL	N
5	0,38um/15u" PdNi WITH 0,076um/3u" Au FLASH (POST PLATED)	P
6	0,76um/30u" PdNi WITH 0,076um/3u" Au FLASH (POST PLATED)	



RECOMMENDED PAD LAYOUT

NOTES :

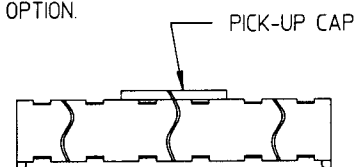
- HOUSING MAT'L : HIGH TEMP PLASTIC (PCT BLACK).
- TERMINAL MAT'L : BeCu.
-
- TERMINAL RETENTION 0,5 Kg/1,1 Lbs.
- 'V' CUT OPTIONAL - MANUFACTURING PROCESS OPTION.
- PRODUCT DESCRIPTION CODE :

XXXXX-X XX

— TOTAL POSITIONS
— PLATING
— BASE NUMBER

7 PACKAGING :

- XXXXX-XXX --- STD PACKAGING IN TUBE.
- XXXXX-XXXTR --- TAPE AND REEL (CONDUCTIVE)
PACKAGING WITH PICK-UP CAP (SEE FIG 1).
- XXXXX-XXXTRN --- TAPE AND REEL (CONDUCTIVE)
PACKAGING WITHOUT PICK-UP CAP.
- XXXXX-XXXCR --- TAPE AND REEL (NON-CONDUCTIVE)
PACKAGING WITH PICK-UP CAP (SEE FIG 1).
- XXXXX-XXXCRN --- TAPE AND REEL (NON-CONDUCTIVE)
PACKAGING WITHOUT PICK-UP CAP.



PICK-UP CAP

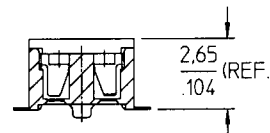
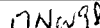
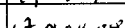


FIG. 1

NOTE 7(b) & (d)

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GEN. TOLERANCES		DO NOT SCALE DWG.		BERG ELECTRONICS
X,X±0,3/XX±.01 X,XX±0,13/XXX±.005 X,XXX±0,051/XXXX±.0020 ANGLES = ±2°		MM INCH		
				
		NAME	DATE	DESCRIPTION LOW PROFILE VCC SMT 2,3 BOTTOM ENTRY ASSY WITH 0,30/.012 LOCATING FEATURES
DRAWN	BS LOW		17NOV98	
CHECKED				
APPROVED				
				PRODUCT NO. 92084
				SCALE SHT 2 OF 2