KA4558/KA4558A

Dual Operational Amplifier

Features

- No frequency compensation required.
- No latch-up.
- Large common mode and differential voltage range.
- Parameter tracking over temperature range.
- Gain and phase match between amplifiers.
- Internally frequency compensated.
- Low noise input transistors.

Descriptions

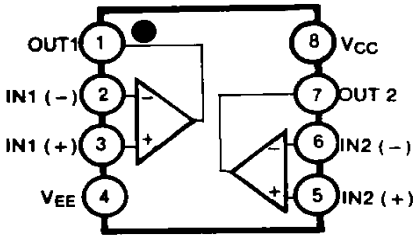
The KA4558/KA4558A are a monolithic integrated circuit designed for dual operational amplifier.

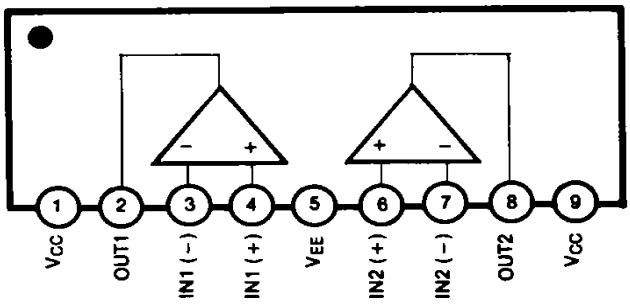
8-DIP

8-SOP

9-SIP

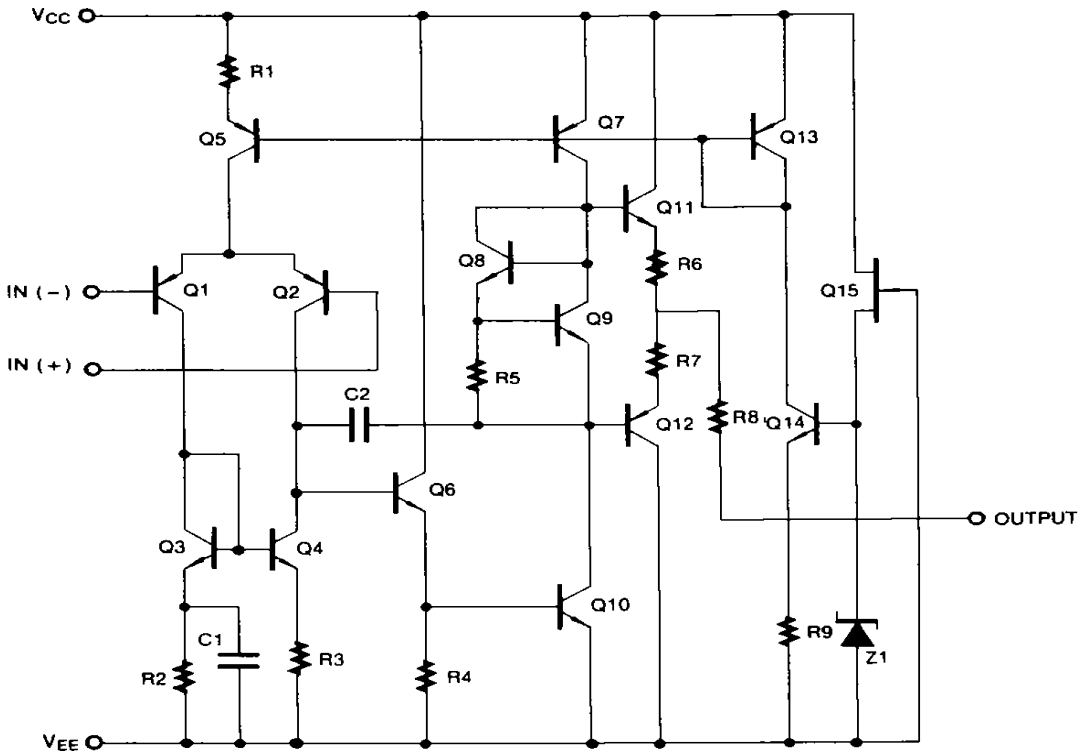
Internal Block Diagram





Schematic Diagram

(One Section Only)



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Supply Voltage	V_{CC}	±22	V
Differential Input Voltage	$V_{I(DIFF)}$	30	V
Input Voltage	V_I	±15	V
Power Dissipation	P_D	400	mW
Operating Temperature Range	T_{OPR}	0 ~ 70	°C
Storage Temperature Range	T_{STG}	-65 ~ 150	°C

Electrical Characteristics

(V_{CC} = 15V, V_{EE} = - 15V ,T_A = 25 °C unless otherwise specified)

Parameter	Symbol	Conditions	KA4558A			KA4558			Unit
			Min	Typ	Max	Min	Typ	Max	
Input Offset Voltage	V_{IO}	R_S≤10KΩ	-	1	5	-	2	6	mV
		NOTE 1	-	1	6	-	-	7.5	
Input Offset Current	I_{IO}		-	5	200	-	5	200	nA
		T_A=$T_{A(MAX)}$	-	3	200	-	-	300	
		T_A =$T_{A(MIX)}$	-	20	500	-	-	300	
Input Bias Current	I_{BIAS}		-	30	500	-	30	500	nA
		T_A=$T_{A(MAX)}$	-	20	500	-	-	800	
		T_A =$T_{A(MIN)}$	-	100	1500	-	-	800	
Large Signal Voltage Gain	G_V	$V_{O(P-P)}$= ±10V,R_L≤2KΩ	50	200	-	20	200	-	V/mV
		NOTE 1	25	-	-	-	-	-	
Common Mode Input Voltage Range	$V_{I(R)}$		±12	±13	-	±12	±13	-	V
		NOTE 1	±12	±13	-	-	-	-	
Common Mode Rejection Ratio	CMRR	R_S≤10KΩ	70	90	-	70	90	-	dB
		NOTE 1	70	90	-	-	-	-	
Supply Voltage Rejection Ratio	PSRR	R_S≤10KΩ	76	90	-	76	90	-	dB
		NOTE 1	76	90	-	76	90	-	
Output Voltage Swing	$V_{O(P.P)}$	R_L≥10KΩ	±12	±14	-	±12	±14	-	V
		R_L≥2KΩ	±10	±13	-	±10	±13	-	
Supply Current (Both Amplifiers)	I_{CC}		-	3.5	5.0	-	3.5	5.8	mA
		T_A =$T_{A(MAX)}$	-	-	4.5	-	-	5.0	
		T_A =$T_{A(MIN)}$	-	-	6.0	-	-	6.7	
Power Consumption (Both Amplifiers)	P_C		-	70	150	-	70	170	mV
		T_A =$T_{A(MAX)}$	-	-	135	-	-	150	
		T_a = $T_{A(MIN)}$	-	-	180	-	-	200	
Slew Rate	SR	V_I =10V, R_L≥2KΩ C_I≤100pF	1.2	-	-	1.2	-	-	V/μs
Rise Time	T_R	V_I =20mV, R_L≥2KΩ C_I≤100pF	-	0.3	-	-	0.3	-	μs
Overshoot	OS	V_I =20mV, R_L≥2KΩ C_I≤100pF	-	15	-	-	15	-	%

Note 1

KA4558A : $T_{A(MIN)}$ ≤T_A≤$T_{A(MAX)}$ = 0≤T_A≤ 70 °C

Typical Performance Characteristics

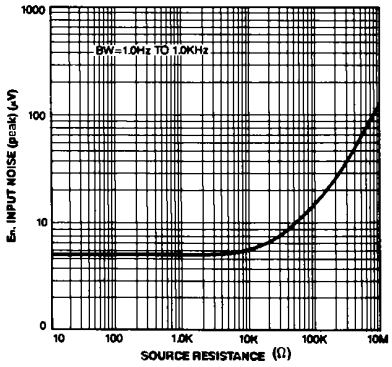


Figure 1. Burst Noise vs Source Resistance

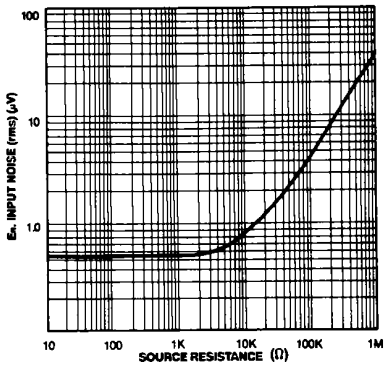


Figure 2. RMS Noise vs Source Resistance

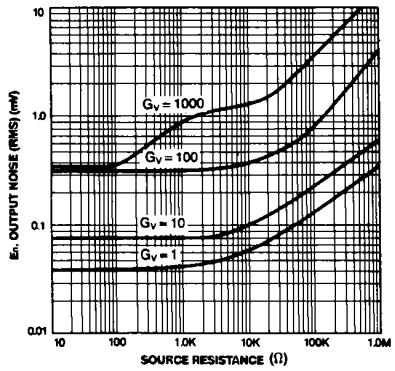


Figure 3. Output Noise vs Source Resistance

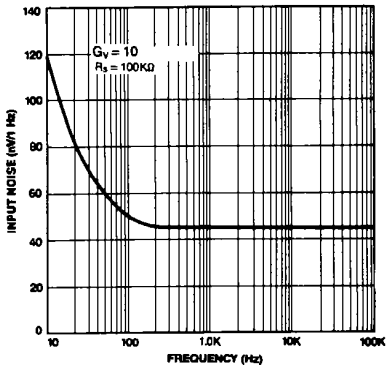


Figure 4. Spectral Noise Density

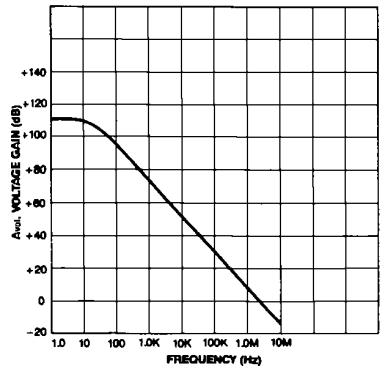


Figure 5. Open Loop Frequency Response

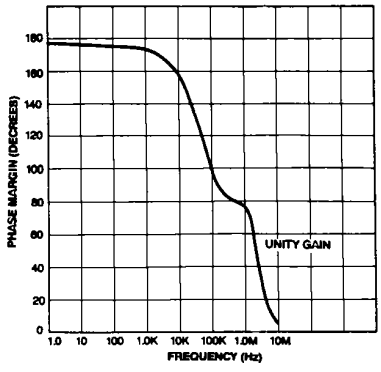


Figure 6. Phase Margin vs Frequency

Typical Performance Characteristics (continued)

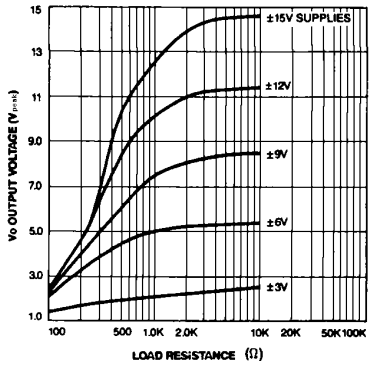


Figure 7. Positive Output Voltage Swing vs Load Resistance

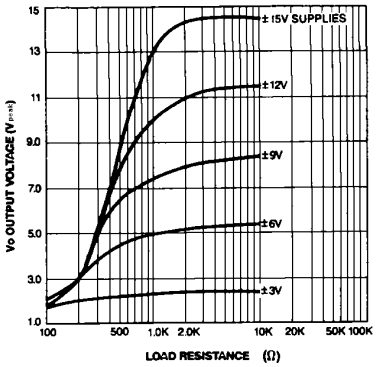


Figure 8. Negative Output Voltage Swing vs Load Resistance

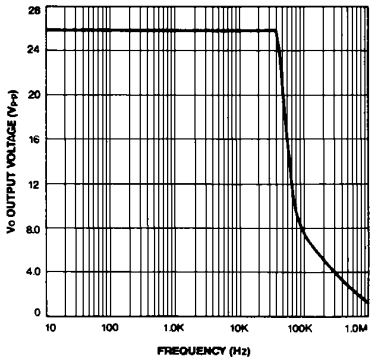
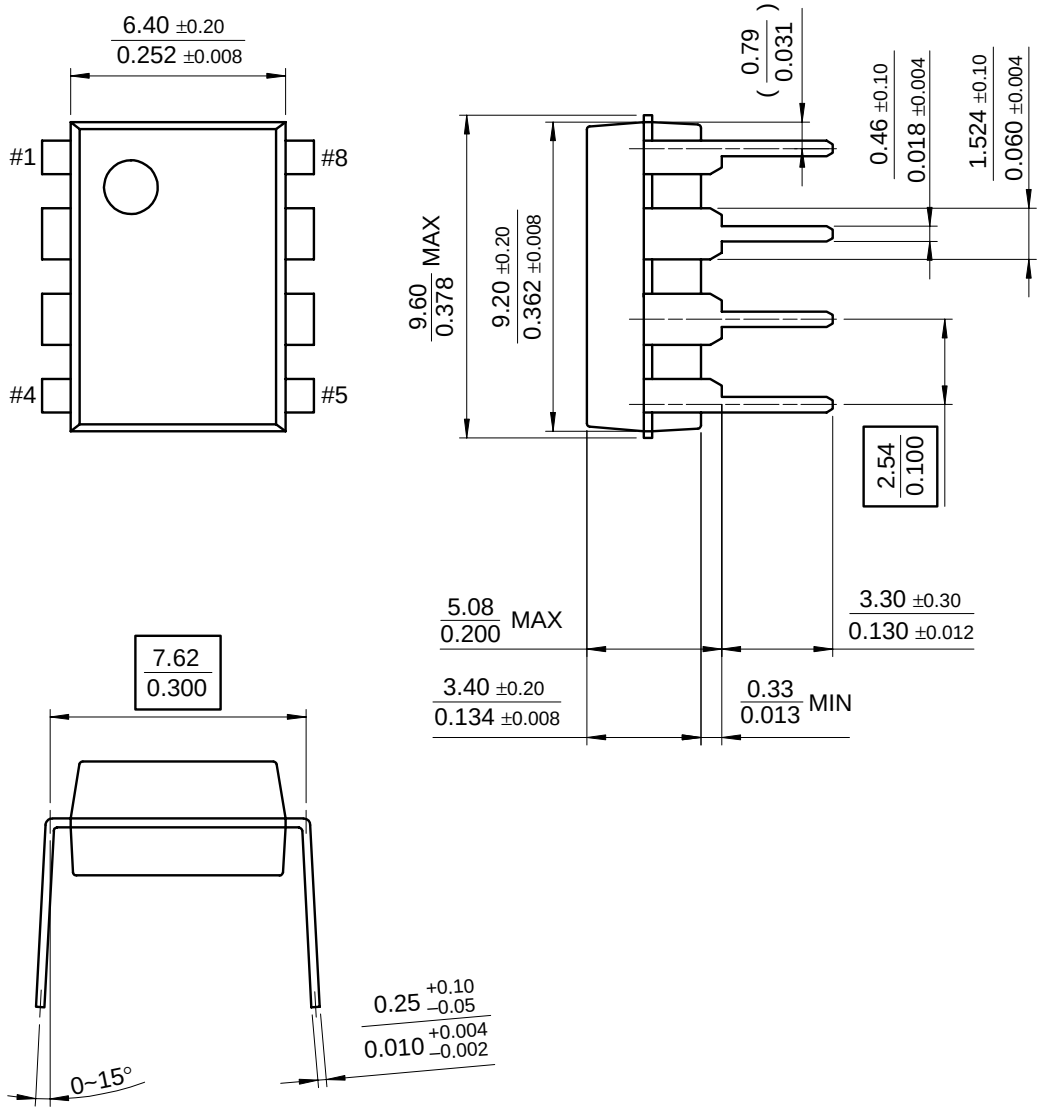


Figure 9. Power Bandwidth (Large Signal Output Swing Versus Ferquency)

Mechanical Dimensions

Package

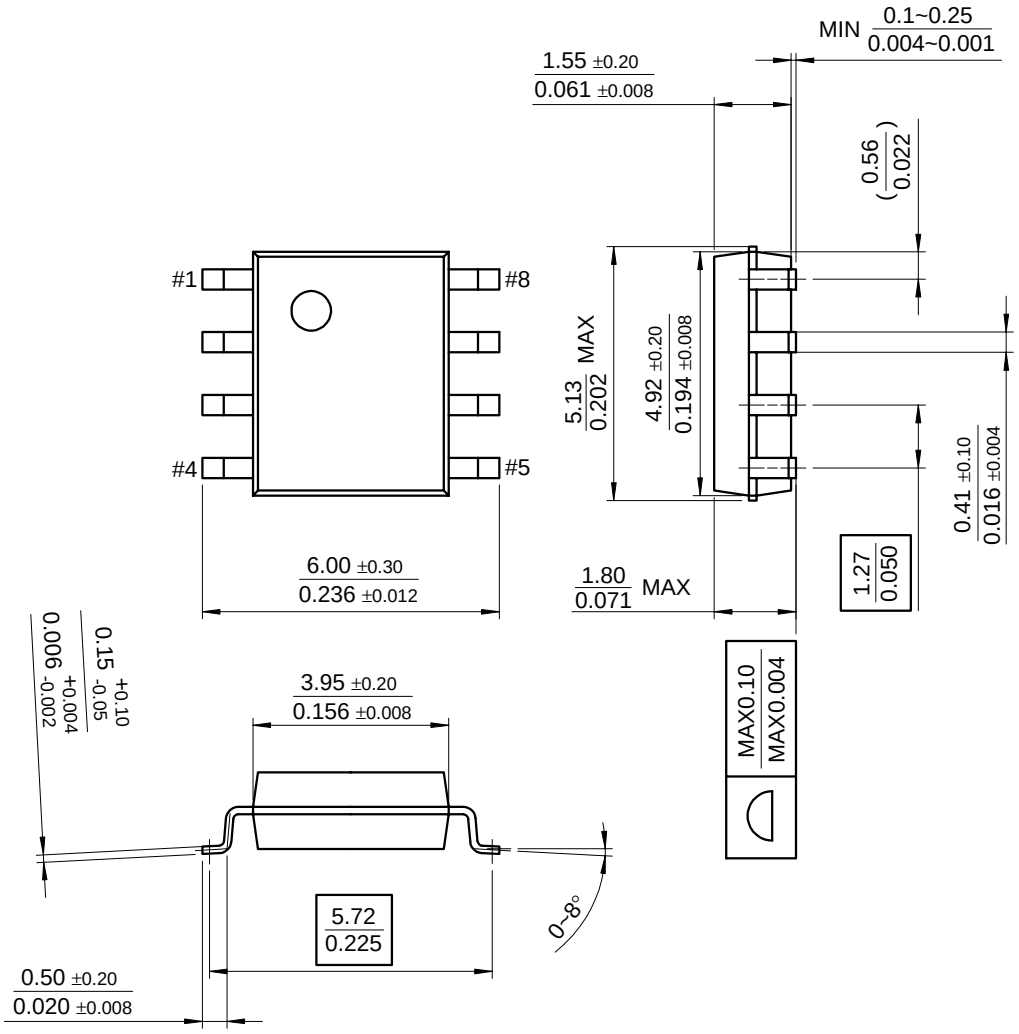
8-DIP



Mechanical Dimensions (Continued)

Package

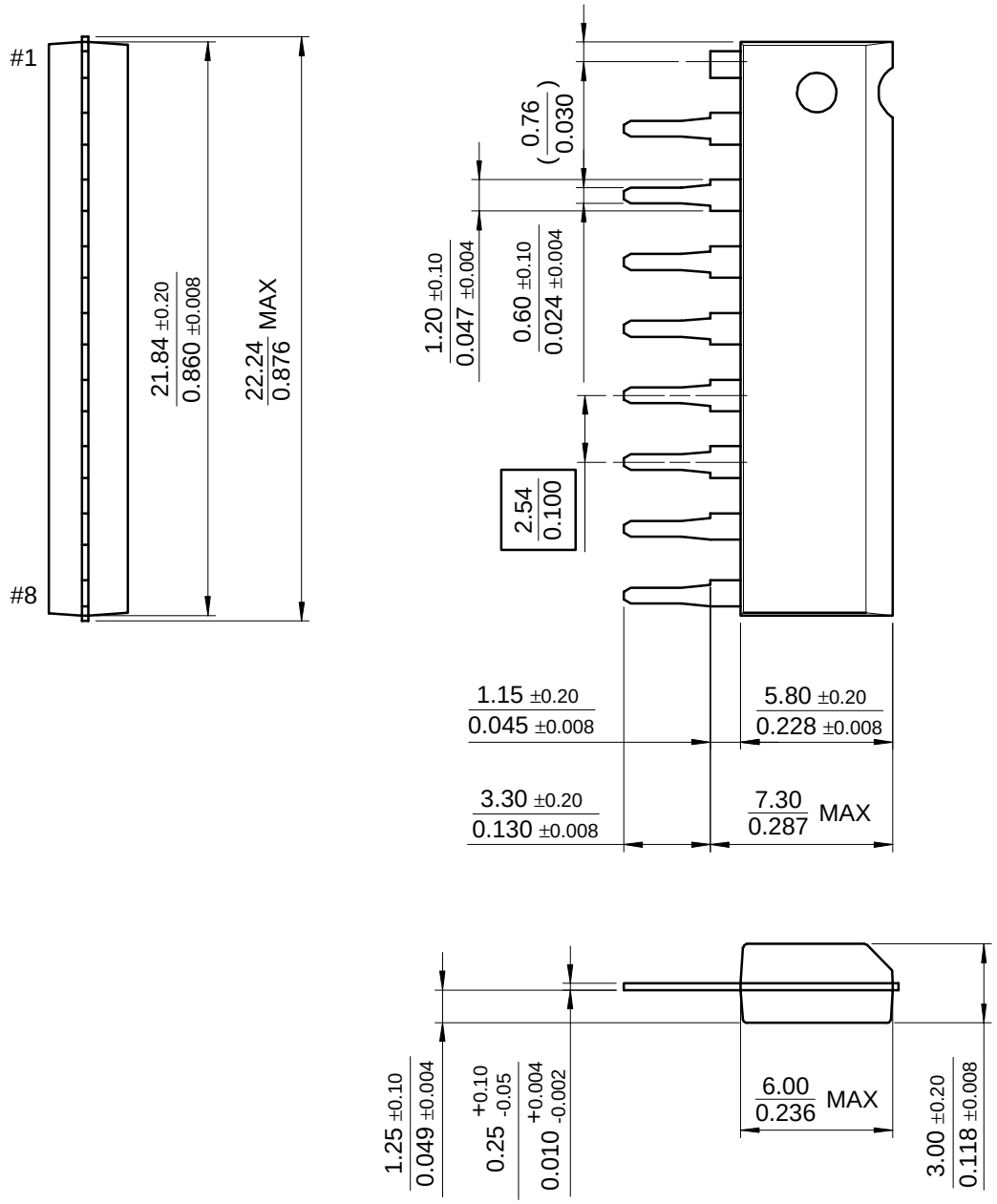
8-SOP



Mechanical Dimensions (Continued)

Package

9-SIP



Ordering Information

Product Number	Package	Operating Temperature
KA4558	8 DIP	0 ~ + 70°C
KA4558A		
KA4558S	9 SIP	
KA4558AS		
KA4558D	8 SOP	
KA4558AD		

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