

R4F2113NBG

User's Manual: Hardware



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Basic Information

Memory

CPU (Bit, Clock, DMA, External Bus etc)

Timers

Analog

Operating Conditions

Remarks

Package

Basic Information

Product Status	Mass Production
Family	H8S
Series	H8S/2100
Group	H8S/2113

Memory

Program memory	128 KB Flash memory
RAM	12 KB
Data Flash	8 KB

CPU (Bit, Clock, DMA, External Bus etc)

CPU	H8S/2000 (16-bit)
Max. Frequency	20 MHz
Power-On Reset	YES
Low Voltage Detection	YES
External Interrupt Pins	17

Timers

8-bit Timers	4 ch
16-bit Timers	6 ch
Watchdog Timers	2 ch

Analog

A/D Converters	10-bit x 12 ch
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Operating Conditions	
Operating Temperature	-20 to 75 °C

Remarks	
Remarks	8-bit PWM timer 12ch

Package	
Pins	176
Terminal pitch (mm)	0.8 mm
Package Type	LFBGA
Dimensions (mm)	13x13 mm ²
Drawing Link	 plbg0176ga_a
Mount pad	 fig0013e
Mounting height (mm)[MAX]	1.4 mm
Mass (g) [TYP,]	0.45 g
Renesas code	PLBG0176GA-A (old: BP-176V)
JEITA code	P-LFBGA176-13x13-0.80
Packing	Tray
Terminal material - Base	Sn-Ag-Cu
Terminal material - Surface	-

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